



### ESDA6V1W5

### Transil array for data protection

Revision:B

#### General Description

The ESDA6V1W5 is monolithic suppressor designed to protect components connected to data and transmission lines against ESD. This device clamps the voltage just above the logic level supply for positive transients and to a diode drop below ground for negative transients.

#### Applications

- Computers
- Printers
- Communication systems
- Cellular phones handsets and accessories
- Wireline and wireless telephone sets
- Set top boxes

#### Features

- 4 Unidirectional Transil functions
- Low leakage current: < 1  $\mu$ A
- Very small PCB area < 4.2 mm<sup>2</sup> typically
- High integration

#### Complies with the following standards

IEC61000-4-2

Level 4 15 kV (air discharge)

8 kV(contact discharge)

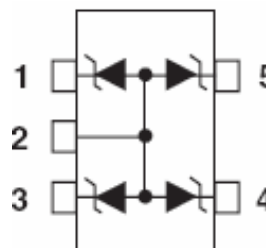
MIL STD 883E - Method 3015-7 Class 3

25 kV HBM (Human Body Model)

#### Functional diagram



SOT-353



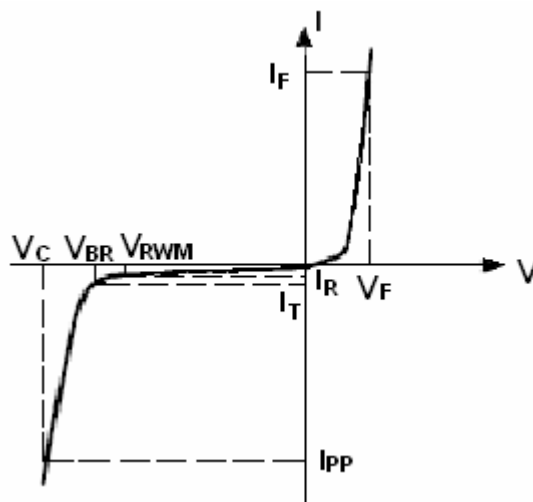
ESDA6V1W5

#### Absolute Ratings (T<sub>amb</sub>=25°C)

Symbol	Parameter	Value	Units
P <sub>PP</sub>	Peak Pulse Power (t <sub>p</sub> = 8/20 $\mu$ s)	150	W
T <sub>L</sub>	Maximum lead temperature for soldering during 10s	260	°C
T <sub>stg</sub>	Storage Temperature Range	-40 to +125	°C
T <sub>op</sub>	Operating Temperature Range	-40 to +125	°C

## Electrical Parameter

Symbol	Parameter
$I_{PP}$	Maximum Reverse Peak Pulse Current
$V_C$	Clamping Voltage @ $I_{PP}$
$V_{RWM}$	Working Peak Reverse Voltage
$I_R$	Maximum Reverse Leakage Current @ $V_{RWM}$
$I_T$	Test Current
$V_{BR}$	Breakdown Voltage @ $I_T$
$I_F$	Forward Current
$V_F$	Forward Voltage @ $I_F$

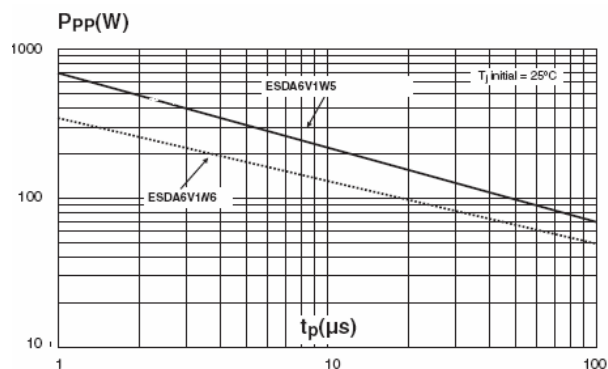


## Electrical Characteristics

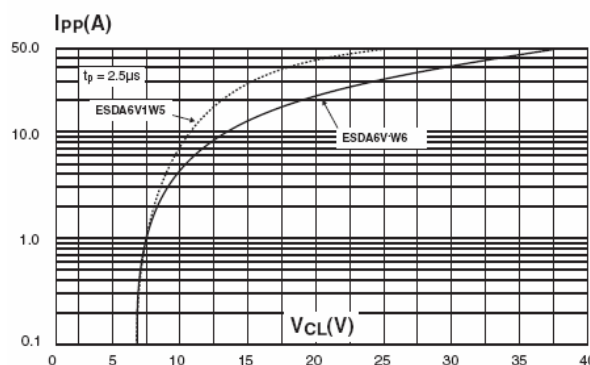
Part Numbers	$V_{BR}$			$I_T$	$V_{RWM}$	$I_R$	$V_F$	$I_F$	$C$
	Min.	Typ	Max.				Max.		Typ. 0v bias
	V	V	V				V		pF
ESDA6V1W5	6.1	6.7	7.2	1	5	1	1.25	200	35

1. Square pulse  $I_{PP}=15A, t_p=2.5\mu s$     2.  $V_{BR}=aT^*(T_{amb}-25^\circ C)*V_{BR}(25^\circ C)$

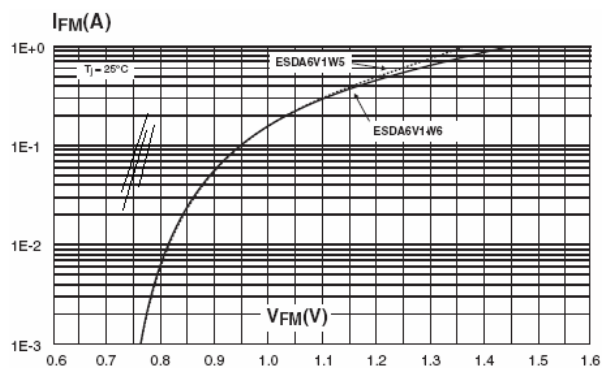
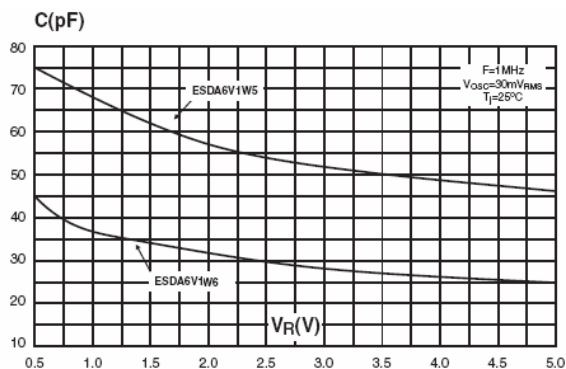
## Typical Characteristics



**Fig1. Peak pulse power versus exponential Pulse duration ( $T_j$  initial=25°C)**



**Fig2. Clamping voltage versus peak pulse current ( $T_j$  initial=25°C, rectangular Waveform,  $t_p=2.5\mu s$ )**



**Fig 3 Capacitance Versus reverse applied voltage**    **Fig 4 Peak Forward Voltage Drop versus forward current**

## SOT-353 Mechanical Data

REF.	DIMENSIONS			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.8	1.1	0.031	0.043
A1	0	0.1	0	0.004
A2	0.8	1	0.031	0.039
b	0.15	0.3	0.006	0.012
c	0.1	0.18	0.004	0.007
D	1.8	2.2	0.071	0.086
E	1.15	1.35	0.045	0.053
e	0.65 Typ.		0.025 Typ.	
H	1.8	2.4	0.071	0.094
Q1	0.1	0.4	0.004	0.016

SHANGHAI LEIDITECH ELECTRONICS TECHNOLOGY CO., LTD  
 Phone: +86- 021-50827201  
 Email:sale1@leiditech.com  
<http://www.leiditech.com>