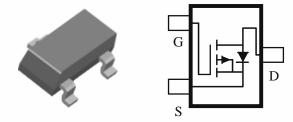
P-Channel 20-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r_{DS(on)} provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOT-23 saves board space
- Fast switching speed
- High performance trench technology

PRODUCT SUMMARY

V _{DS} (V)	r _{DS(on)} (OHM)	I _D (A)
-20	$0.130 @ V_{GS} = -4.5V$	-2.6
	$0.190 @ V_{GS} = -2.5V$	-2.1



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)						
Parameter			Maximum	Units		
Drain-Source Voltage		V _{DS}	-20	V		
Gate-Source Voltage		V _{GS}	′GS ±8			
Continuous Dusin Connect ^a	T _A =25°C	I.	-2.6			
Continuous Drain Current ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	ID	-1.5	А		
Pulsed Drain Current ^b		I _{DM}	-10			
Continuous Source Current (Diode Conduction) ^a			±1.6	А		
	T _A =25°C	Do	1.25	W		
Power Dissipation ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	гD	0.8	**		
Operating Junction and Storage Temperature Range			-55 to 150	°C		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum	Units	
Maximum Junction-to-Ambient ^a	t <= 5 sec	D	100	°C/W	
	Steady-State	THJA	166	C/VV	

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

	Sympol		Limits			TT •4	
Parame te r	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_D=-250\;uA$	-0.4		-1		
Gate-Body Leakage	Igss	$V_{\rm DS}=0$ V, $V_{\rm GS}=+/\text{-}8$ V			±100	nA	
Zana Cata Valta an Duain Cumant	IDSS	$V_{DS} = -16 V, V_{GS} = 0 V$			-1	uА	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55 ^{\circ}\text{C}$			-10	uA	
On-State Drain Current ^A	Ι	V = -5 V, V = -4.5 V	-3			А	
		$V_{GS} = -4.5 V$, $I_D = -2.6 A$			0.130	Ω	
Drain-Source On-Resistance ^A	ľDS(on) D(on)	V _{DS} = -2.5 V, L _{DS} = -2.1 A			0.190	12	
Forward Tranconductance ^A	g	V = -5 V, I = -2.8 A		3		S	
Diode Forward Voltage	V _{SD}	$I_S = -1.6 \text{ A}, V_{GS} = 0 \text{ V}$		-0.70		V	
Dynamic ^b	fs	DS D					
Total Gate Charge	Qg			12.2		nC	
Gate-Source Charge	Qgs	$V_{DS} = -5 V, V_{GS} = -4.5 V,$ ID = -2.6 A		1.1			
Gate-Drain Charge	Qgd	ID = -2.0 A		1.5			
Turn-On Delay Time	t _{d(on)}			6.5			
Rise Time	tr	$V_{DD} = -5 \text{ V}, R_L = 5 \text{ OHM},$		20		ns	
Turn-Off Delay Time	t _{d(off)}	$V_{\rm GEN}=-4.5~V,R_{\rm G}=6~OHM$		31			
Fall-Time	tf			21		1	

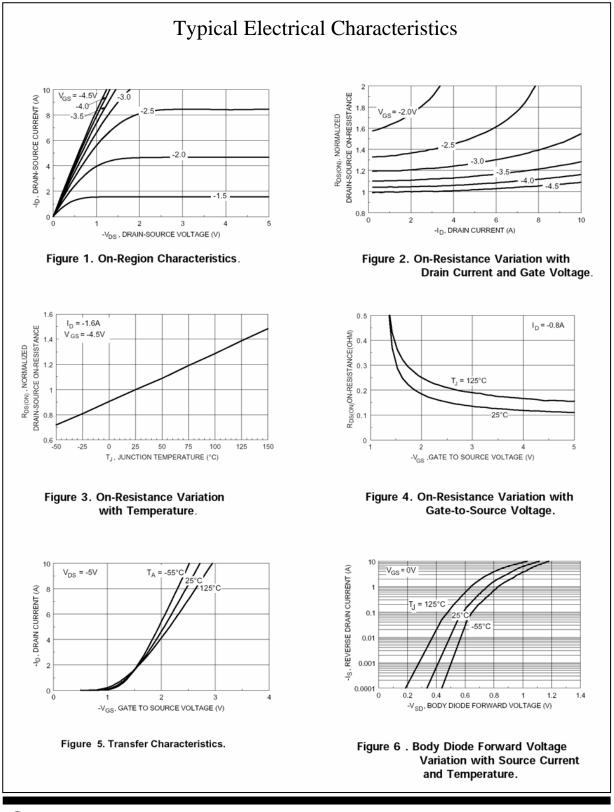
Notes

- a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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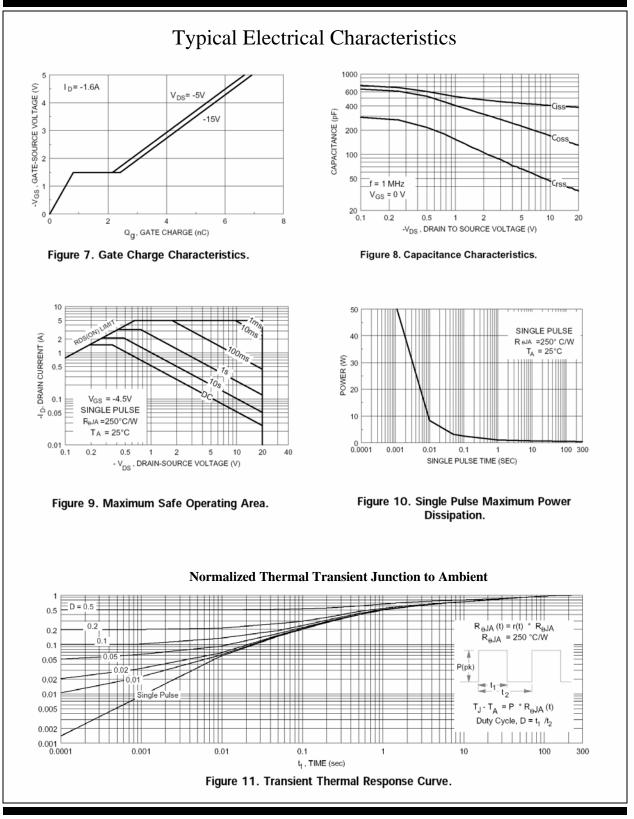
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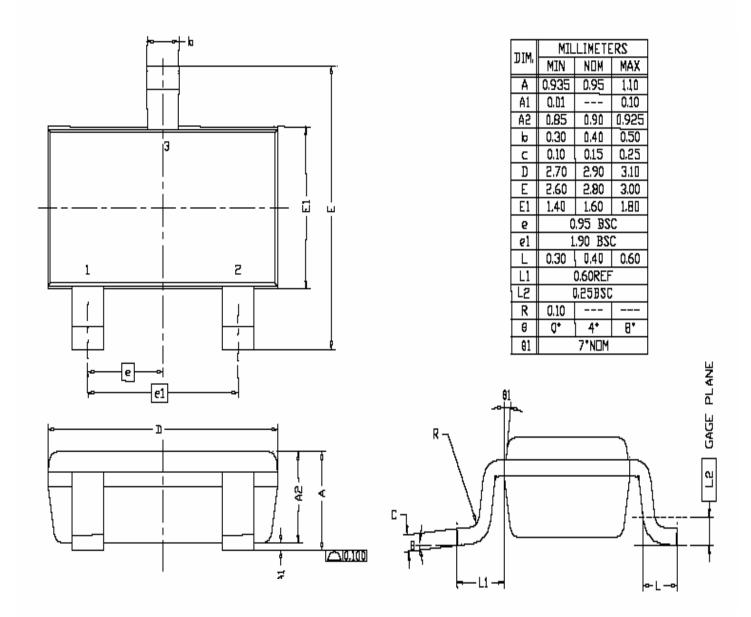
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AO3403/MC3403



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Package Information



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Ordering information

• AM2301P-T1-XX

- A: Analog Power
- M: MOSFET
- 2301: Part number
- P: P-Channel
- T1: Tape & reel
- XX: Blank: StandardPF: Leadfree