查询BAV70TT1G供应商

BAV70TT1

Preferred Device



Features

• Pb-Free Package May be Available.* The G-Suffix Denotes a Pb-Free Lead Finish

MAXIMUM RATINGS (T _A = 25° C)				
Rating	Symbol	Мах	Unit	
Reverse Voltage	VR	70	Vdc	
Forward Current	١F	200	mAdc	
Peak Forward Surge Current	IFM(surge)	500	mAdc	

THERMAL CHARACTERISTICS

1. FR-4 @ Minimum Pad 2. FR-4 @ 1.0 × 1.0 Inch Pad

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (1) $T_A = 25^{\circ}C$ Derated above 25°C	PD N.025	225	mW mW/°C
Thermal Resistance, Junction to Ambient (1)	R _{θJA}	555	°C/W
Total Device Dissipation, FR-4 Board (2) $T_A = 25^{\circ}C$ Derated above 25°C	PD	360 2.9	mW mW/°C
Thermal Resistance, Junction-to-Ambient (2)	R _{θJA}	345	°C/W
Junction and Storage	T _J , T _{stg}	–55 to +150	°C

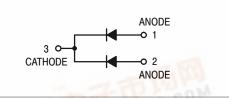


,24小时加急出货

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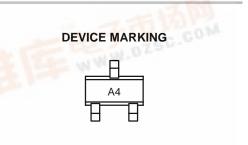
唐多邦 .

专业PCB打样工厂





SOT-416/SC-75 STYLE 3



ORDERING INFORMATION

Device	Package	Shipping [†]
BAV70TT1	SOT-416	3000 / Tape & Reel
BAV70TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Reverse Breakdown Voltage (I _(BR) = 100 μAdc)	V _(BR)	70	-	Vdc	
Reverse Voltage Leakage Current (Note 3) (V _R = 70 Vdc) (V _R = 50 Vdc)	I _R I _R		5.0 100	μAdc nAdc	
Diode Capacitance ($V_R = 0, f = 1.0 \text{ MHz}$)	CD	-	1.5	pF	
Forward Voltage $(I_F = 1.0 \text{ mAdc})$ $(I_F = 10 \text{ mAdc})$ $(I_F = 50 \text{ mAdc})$ $(I_F = 150 \text{ mAdc})$	VF	- - - -	715 855 1000 1250	mVdc	
Reverse Recovery Time (IF = IR = 10 mAdc, RL = 100 Ω , IR(REC) = 1.0 mAdc) (Figure 1)	t _{rr}	-	6.0	ns	
Forward Recovery Voltage ($I_F = 10 \text{ mAdc}, t_r = 20 \text{ ns}$) (Figure 2)	V _{RF}	-	1.75	V	

3. For each individual diode while the second diode is unbiased.

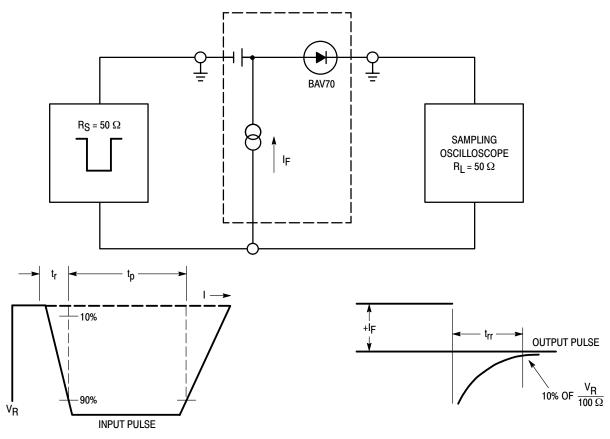
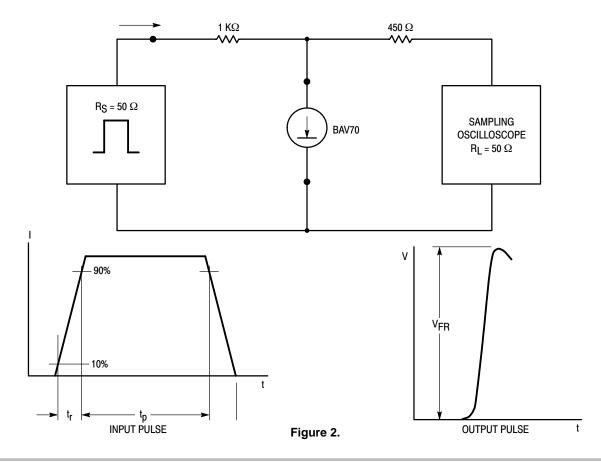


Figure 1. Recovery Time Equivalent Test Circuit



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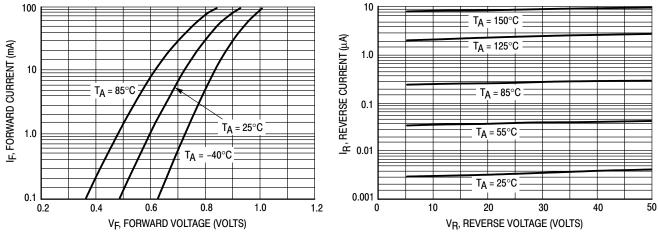
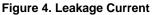


Figure 3. Forward Voltage



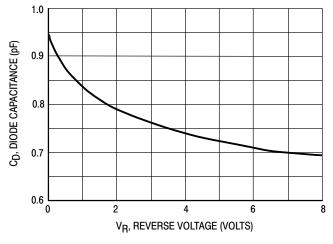
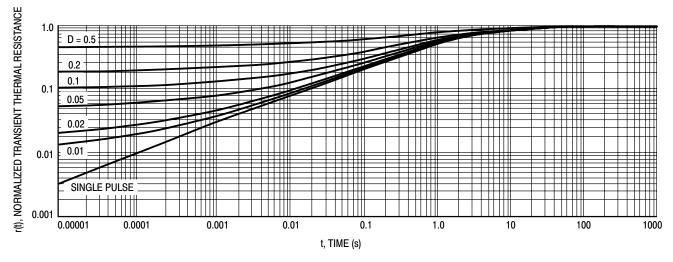
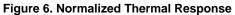
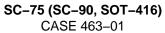


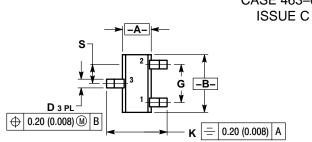
Figure 5. Capacitance

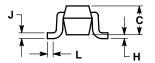




PACKAGE DIMENSIONS







NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROL ING DIMENSION: MILLIMETER.

2.	CONT	ROLLING DIM	ENSION: MILLI	METER.
				-

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	0.70	0.90	0.028	0.035
В	1.40	1.80	0.055	0.071
С	0.60	0.90	0.024	0.035
D	0.15	0.30	0.006	0.012
G	1.00 BSC		0.039 BSC	
н		0.10		0.004
J	0.10	0.25	0.004	0.010
ĸ	1.45	1.75	0.057	0.069
L	0.10	0.20	0.004	0.008
S	0.50 BSC		0.020	BSC

STYLE 3: PIN 1. ANODE 2. ANODE 3. CATHODE

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