

# **HAT1069C**

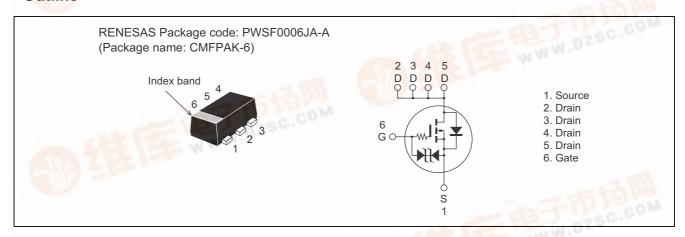
# Silicon P Channel Power MOS FET Power Switching

REJ03G0164-0300 Rev.3.00 Oct 19, 2007

### **Features**

- Low on-resistance  $R_{DS(on)} = 38 \ m\Omega \ typ \ (at \ V_{GS} = -4.5 \ V)$
- High speed switching
- Capable of 1.8 V gate drive
- High density mounting

### **Outline**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit	
Drain to source voltage	V <sub>DSS</sub>	-12	V	
Gate to source voltage	V <sub>GSS</sub>	±8	V	
Drain current	I <sub>D</sub>	-4	A	
Drain peak current	I <sub>D(pulse)</sub> Note1	-16	- A	
Body-drain diode reverse drain current	I <sub>DR</sub>	-4	AG	
Channel dissipation	Pch <sup>Note2</sup>	900	mW	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%

2. When using the grass epoxy board. (FR4  $40 \times 40 \times 1.6$  mm)



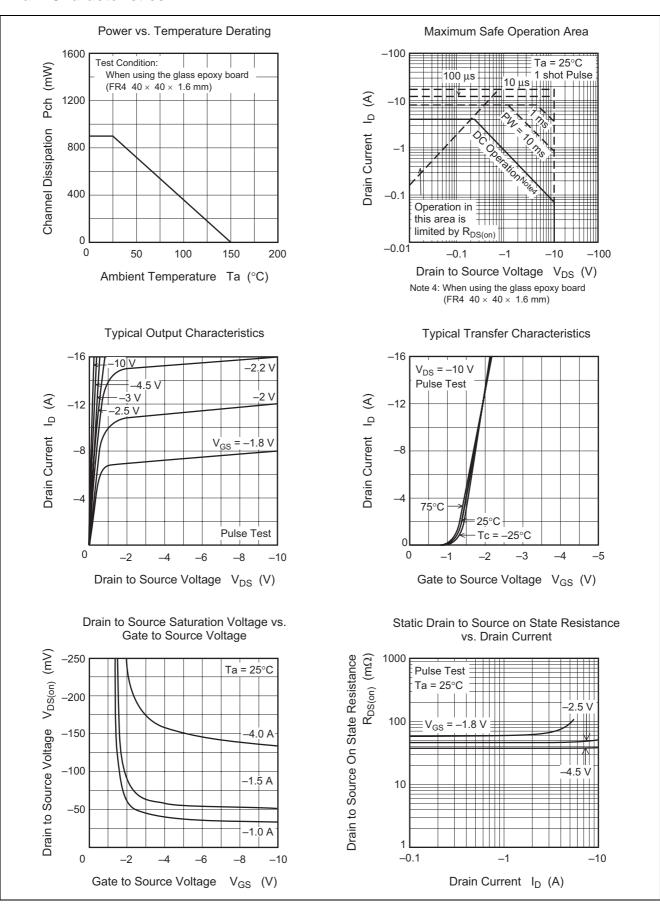
### **Electrical Characteristics**

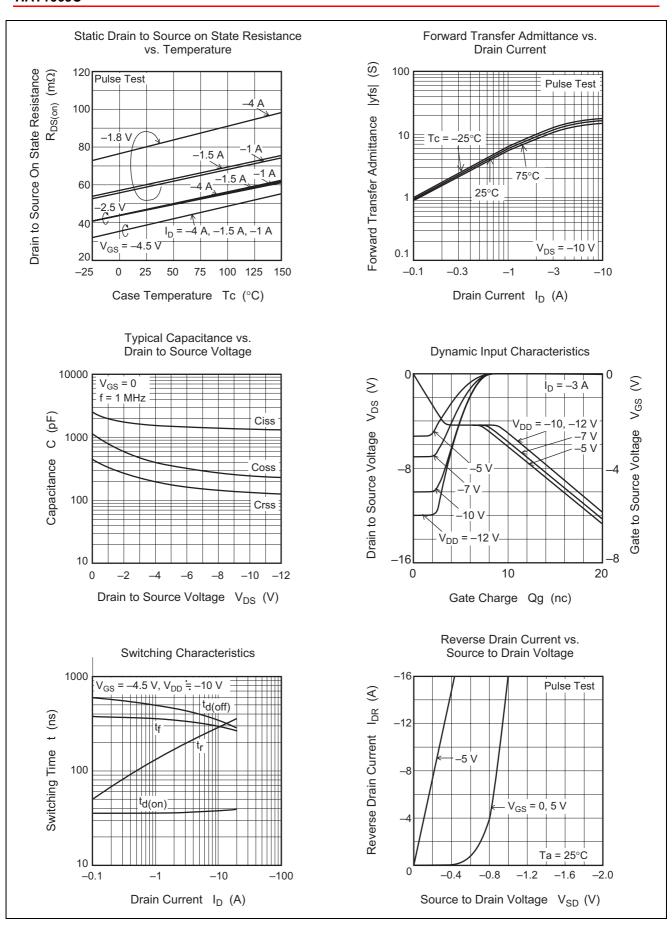
 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	-12	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±8	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 6.4 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	-1	μΑ	$V_{DS} = -12 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-0.3	_	-1.2	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	38	52	mΩ	$I_D = -1.5 \text{ A}, V_{GS} = -4.5 \text{ V}$
resistance	R <sub>DS(on)</sub>	_	48	70	mΩ	$I_D = -1.5 \text{ A}, V_{GS} = -2.5 \text{ V}$
	R <sub>DS(on)</sub>	_	60	93	mΩ	$I_D = -1.5 \text{ A}, V_{GS} = -1.8 \text{ V}$
Forward transfer admittance	y <sub>fs</sub>	5	8	_	S	$I_D = -1.5 \text{ A}, V_{DS} = -10 \text{ V}$
Input capacitance	Ciss	_	1380	_	pF	$V_{DS} = -10 \text{ V}$
Output capacitance	Coss	_	235	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	115	_	pF	f = 1 MHz
Total gate charge	Qg	_	16	_	nC	$V_{DS} = -10 \text{ V}$
Gate to source charge	Qgs	_	3	_	nC	$V_{GS} = -4.5 \text{ V}$
Gate to drain charge	Qgd	_	6.2	_	nC	$I_D = -3 A$
Turn-on delay time	t <sub>d(on)</sub>	_	35	_	ns	$V_{GS} = -4 \text{ V}, I_D = -1.5 \text{ A}$
Rise time	t <sub>r</sub>	_	150	_	ns	V <sub>DD</sub> ≅ −10 V
Turn-off delay time	t <sub>d(off)</sub>	_	490	_	ns	$R_L = 6.6 \Omega$
Fall time	t <sub>f</sub>	_	350	_	ns	$R_g = 4.7 \Omega$
Body-drain diode forward voltage	$V_{DF}$	_	-0.8	-1.1	V	$I_F = -4 \text{ A}, V_{GS} = 0^{\text{Note3}}$

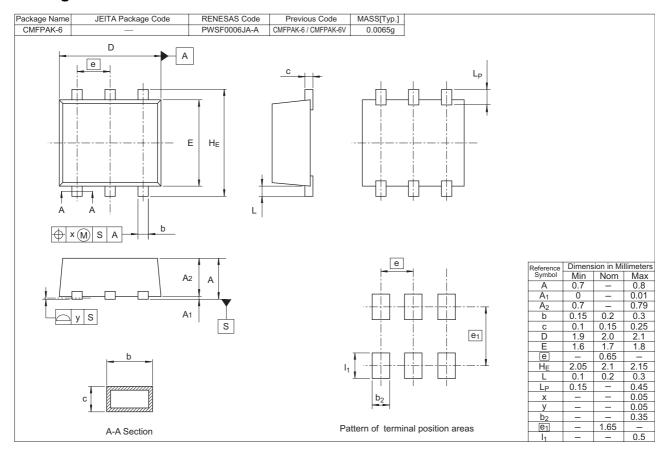
Notes: 3. Pulse test

### **Main Characteristics**





### **Package Dimensions**



### **Ordering Information**

Part Name	Quantity	Shipping Container
HAT1069C-EL-E	3000 pcs	Taping

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Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510