捷多邦,专业PCB打样工厂,24小时加急出货



# HAT2134H

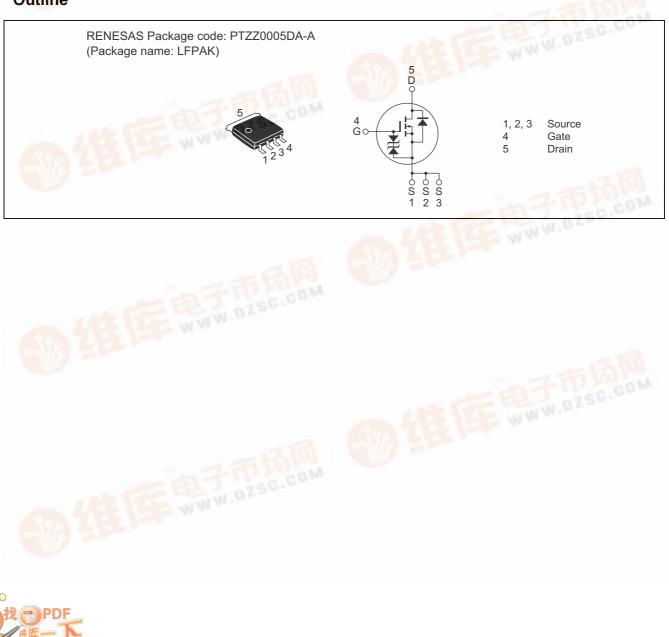
Silicon N Channel Power MOS FET Power Switching

> REJ03G1190-0300 (Previous: ADE-208-1578A) Rev.3.00 Sep 07, 2005

## Features

- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance  $R_{DS (on)} = 2.3 \text{ m}\Omega \text{ typ.}$  (at  $V_{GS} = 10 \text{ V}$ )

# Outline



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# **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	20	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	60	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	240	A
Body-drain diode reverse drain current	I <sub>DR</sub>	60	A
Avalanche current	I <sub>AP</sub> Note 3	20	A
Avalanche energy	E <sub>AR</sub> Note 3	40	mJ
Channel dissipation	Pch Note 2	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Tc = 25 °C

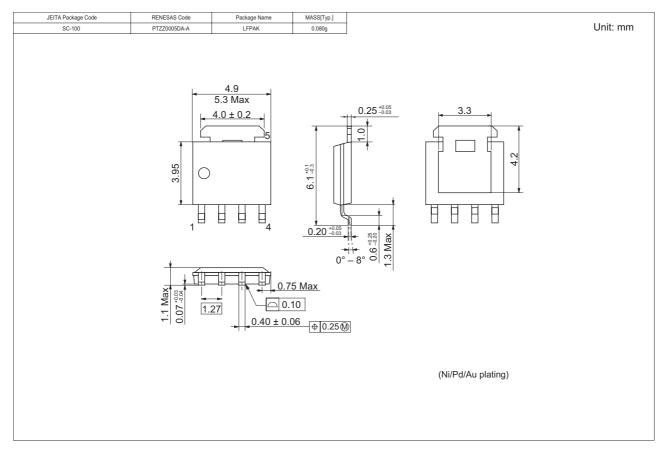
3. Value at Tch =  $25^{\circ}$ C, Rg  $\geq 50 \Omega$ 

# **Electrical Characteristics**

						(Ta = 25°C)
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	20		_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V (BR) GSS	±20	_	-	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 V, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μA	$V_{DS} = 20 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	1.0	_	2.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	_	2.3	2.9	mΩ	$I_D = 30 \text{ A}, V_{GS} = 10 \text{ V}^{Note 4}$
	R <sub>DS (on)</sub>		4.0	5.8	mΩ	$I_D = 30 \text{ A}, V_{GS} = 4.5 \text{ V}^{Note 4}$
Forward transfer admittance	y <sub>fs</sub>	51	85	_	S	$I_D = 30 \text{ A}, V_{DS} = 10 \text{ V}^{Note 4}$
Input capacitance	Ciss	_	4500		pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	1200		pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	560		pF	f = 1 MHz
Total gate charge	Qg	_	70	_	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	_	15		nC	V <sub>GS</sub> = 10 V
Gate to drain charge	Qgd	_	11	_	nC	I <sub>D</sub> = 60 A
Turn-on delay time	t <sub>d (on)</sub>	—	20	_	ns	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 30 \text{ A}$
Rise time	tr	_	60	_	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	t <sub>d (off)</sub>		85	_	ns	$R_L = 0.33 \Omega$
Fall time	t <sub>f</sub>		17	_	ns	Rg = 4.7 Ω
Body-drain diode forward voltage	V <sub>DF</sub>	—	0.85	1.10	V	$I_F = 60 \text{ A}, V_{GS} = 0^{Note 4}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	50	—	ns	$I_F = 60 \text{ A}, V_{GS} = 0$
						di <sub>F</sub> /dt = 50 A/µs

Note: 4. Pulse test

# Package Dimensions



# **Ordering Information**

Part Name	Quantity	Shipping Container			
HAT2134H-EL-E	2500 pcs	Taping			
Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of					

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