

Silicon NPN Power Transistors

2SC1577

DESCRIPTION

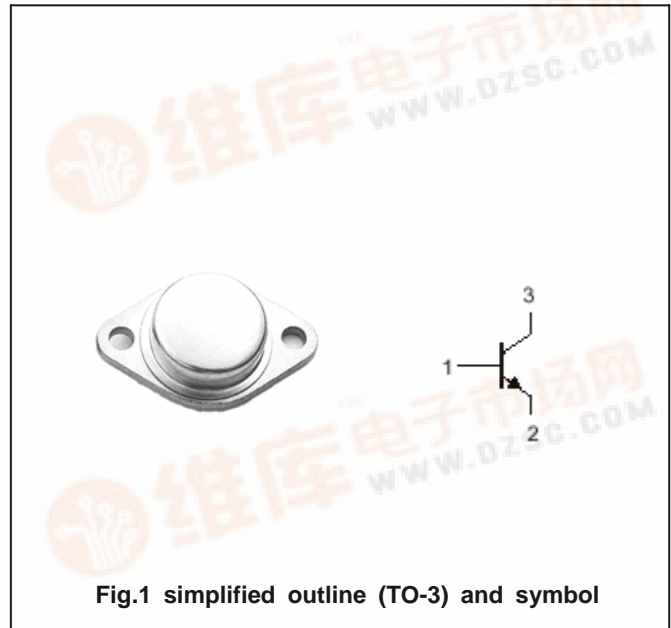
- With TO-3 package
- High voltage
- High speed switching

APPLICATIONS

- For switching regulator and general purpose applications

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector



Absolute maximum ratings(Ta=°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	500	V
V _{CEO}	Collector-emitter voltage	Open base	400	V
V _{EBO}	Emitter-base voltage	Open collector	7	V
I _c	Collector current		8	A
P _c	Collector power dissipation	T _c =25°C	80	W
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

Silicon NPN Power Transistors

2SC1577

CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =25mA ; I _B =0	400			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =1mA ; I _C =0	7			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =5A; I _B =1A			0.5	V
V _{BEsat}	Base-emitter saturation voltage	I _C =5A; I _B =1A			1.3	V
I _{CBO}	Collector cut-off current	V _{CB} =500V; I _E =0			0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =7V; I _C =0			0.1	mA
h _{FE}	DC current gain	I _C =3A ; V _{CE} =4V	30			
f _T	Transition frequency	I _C =0.5A ; V _{CE} =12V		7		MHz

Silicon NPN Power Transistors

2SC1577

PACKAGE OUTLINE

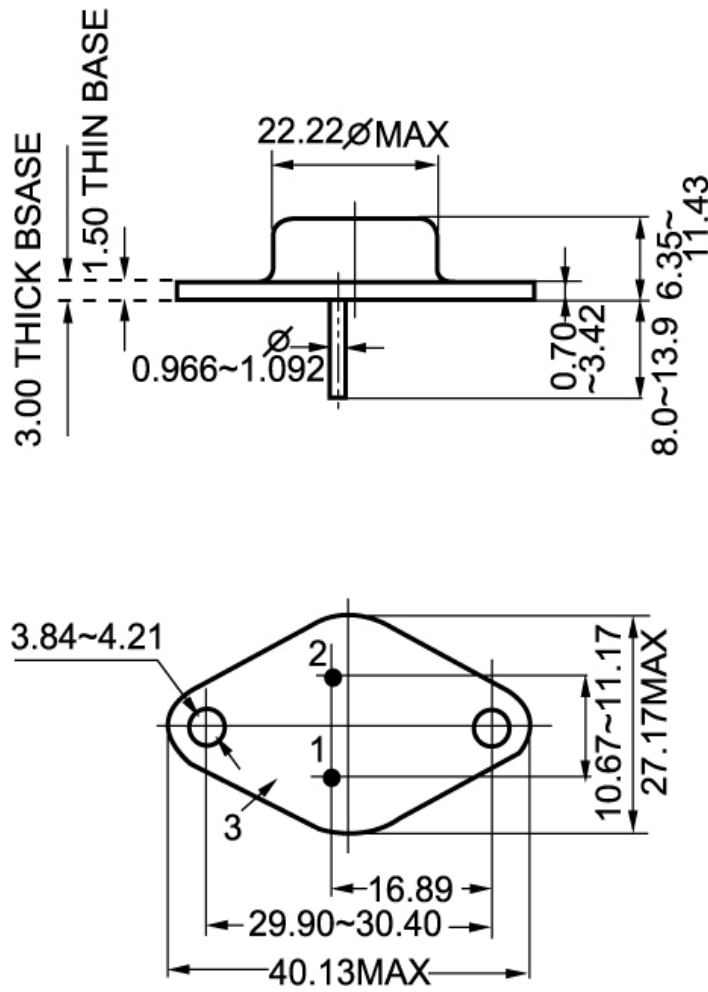


Fig.2 outline dimensions (unindicated tolerance: ± 0.1 mm)