PCI 9052RDK-LITE

Rapid Development Kit For ISA-to-PCI Migration

Flexible ISA-to-PCI Hardware Conversion Platform

- PCI v2.1 compliant PCI form factor based on the PLX PCI 9052 I/O Bus Target Accelerator
  - Direct ISA-to-PCI bus interface
  - Supports 32-bit, 33MHz PCI bus operation
  - Supports 32-bit, 40MHz generic local bus operation
- Piggyback pre-wired ISA slot enables existing ISA boards to interface with the PCI 9052 for software and hardware development
- 30 surface mount footprints and 25x30 0.1" through-hole grid space for additional PLDs, I/O devices, memories, FIFOs, etc.
- PLD local bus memory controller with 128KB SRAM demonstrates the long burst capability of the PCI 9052

Complete and Reusable Hardware Design Documentation

- OrCAD schematics
- Bill of Materials (BOM)
- OrCAD layout source and Gerber output files
- PLD memory controller Verilog source code

Complete Windows Host Software Development Environment

- Windows 98/NT/2000 host side device drivers with source code
- PCI 9052 Windows Host API and object code library
- PLXMon Windows GUI debug tool for monitoring, debugging, configuration, and code downloading

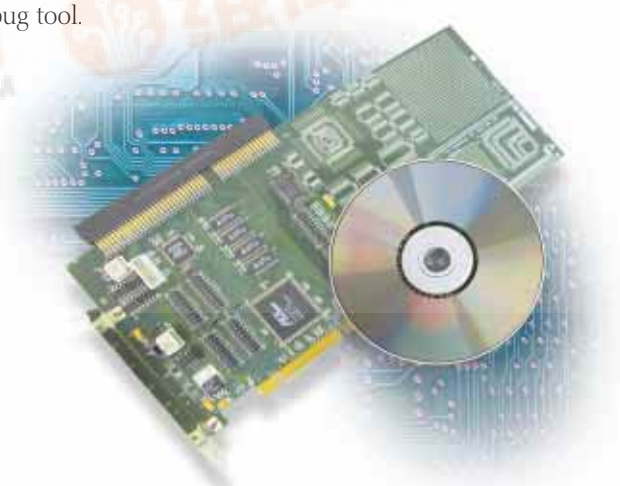
The PCI 9052RDK-LITE (RDK-LITE) Rapid development Kit delivers a complete solution for converting existing ISA bus add-in cards to PCI target adapters. The RDK-LITE board features an ISA slot connected to the PCI 9052 ISA bus port. Designers can plug their existing 8-bit or 16-bit ISA cards into this slot to begin their PCI adapter software development on a proven hardware platform. A prototyping area featuring 30 surface-mount footprints provides space to add additional memories, FIFOs, ASICs and I/O devices. This prototyping area allows designers to develop their own custom hardware without having to wait for fabrication of their own PCI-compliant boards. The RDK-LITE kit includes the PLX Hardware Development Kit (HDK) CD, containing reference design information for hardware development. Also included is the PLX Software Development Kit Lite Edition (SDK-LITE), providing a complete Microsoft Windows host-side development environment.

A Complete Package

The RDK-LITE hardware reference board serves as both hardware and software development platforms for PCI 9052 based designs. The board ships pre-configured for ISA bus mode and de-multiplexed address and data bus operation (C Mode), but it is user configurable for multiplexed address and data bus operation (J Mode). Its local bus memory controller and SRAM enable immediate Direct Slave and DMA code development and testing. Its large and flexible prototyping area enables the easy extension of the test and debug features of the RDK to include your value added logic.

The HDK CD includes complete documentation of the RDK board design, making its components easily reusable in your projects. This documentation includes the board's schematics, OrCAD layout source and Gerber output files, BOM, memory controller CPLD Verilog source code and PDF manuals.

The SDK-LITE provides a complete set of Windows host side software and tools, including host-side Windows 98/NT/2000 drivers for the reference board, PCI 9052 specific APIs and object code libraries, and the PLXMon Windows GUI debug tool.



PCI 9052RDK-LITE Board

Function	Description
PCI Bus Interface	PCI 9052 I/O Accelerator
ISA Bus Interface	PCI 9052 I/O Accelerator
PCI Bus Speed	32-bit, 33 MHz Max
Local Bus Speed	32-bit, 40 MHz Max with C or J mode
32K x 32 synchronous SRAM with CPLD memory controller	Demonstrating the infinite bursting capability of the PCI 9052 for both C and J modes
BGA Footprint	0.050" 26x26
QFP/PLCC Footprints	0.8mm pitch: 44-pin QFP, 54(2)-pin TSOP0. 5mm pitch: 208/176/144/100/80/48 QFP 0.05" pitch: 84/68/44/32/28/20-pin PLCC, 28(2)/20/14(2)-pin SOIC wide,16(4)/14-pin SOIC narrow 0.025"pitch: 48(4)/24(2)/16(2)-pin SSOP
EEPROM Socket	Supports boot-up FLASH memory
Logic Analyzer Test Header	Six (6) 2x10-pin
25x25 0.1" Through Hole Grid Space	For mounting through-hole devices

PCI 9052HDK-LITE CD-ROM

Contents	Description
PCI 9052HDK-LITE CD-ROM	A CD-ROM containing all hardware design information: OrCAD schematics, OrCAD layout source and Gerber output files, Bill of Materials (BOM), glue logic Verilog code, hardware manuals in PDF format

PLX SDK-LITE CD-ROM

Contents	Description
PCI 9052 host side API and object code libraries	Simplifies the programming of complex hardware control with simple, powerful API calls. The reusable components enables easy creation of device drivers for customer environments and provides for easy porting to future PLX PCI devices
Windows 98/NT/2000 drivers	PCI 9052 Windows reference drivers with source code
PLXMon™	Enables easy monitoring, debugging, and configuring of PLX's PCI devices and other PCI/local bus devices.Supports downloading of sample boot FLASH code onto RDK or customer design and allows debugging via serial and/or Ethernet ports
Comprehensive Manuals (PDF)	Shorten learning curve and development cycle

Product Ordering Information

Part Number	Description
PCI 9052	PCI 9052 I/O Accelerator Chip
PCI 9052RDK-LITE	PCI 9052 prototyping Rapid Development Kit
SDK-LITE	Windows host side software development kit for PLX I/O Accelerators and Processor
SDK-PRO	Windows host and local sides software development kit, plus Linux host drivers, RTOS and source code for PLX I/O Accelerators and Processor

Please visit PLX Web site at http://www.plxtech.com or contact PLX sales at 408-774-9060 for pricing and sample.

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