

# MOTOROLA SEMICONDUCTOR TECHNICAL DATA

## GENERAL DATA APPLICABLE TO ALL SERIES IN THIS GROUP Zener Transient Voltage Suppressors

**1SMC5.0AT3  
SERIES  
1500 WATT  
PEAK POWER**

The SMC series is designed to protect voltage sensitive components from high voltage, high energy transients. They have excellent clamping capability, high surge capability, low zener impedance and fast response time. The SMC series is supplied in Motorola's exclusive, cost-effective, highly reliable Surmetic package and is ideally suited for use in communication systems, numerical controls, process controls, medical equipment, business machines, power supplies and many other industrial/consumer applications.

### Specification Features:

- Standard Zener Breakdown Voltage Range — 6.8 to 91 V
- Stand-off Voltage Range — 5 to 78 V
- Peak Power — 1500 Watts @ 1 ms
- Maximum Clamp Voltage @ Peak Pulse Current
- Low Leakage < 5  $\mu$ A Above 10 V
- UL Recognition
- Maximum Temperature Coefficient Specified
- Available in Tape and Reel
- Response Time Typically < 1 ns

### Mechanical Characteristics:

**CASE:** Void-free, transfer-molded, thermosetting plastic

**FINISH:** All external surfaces are corrosion resistant and leads are readily solderable

**POLARITY:** Cathode indicated by molded polarity notch. When operated in zener mode, will be positive with respect to anode

**MOUNTING POSITION:** Any

**LEADS:** Modified L-Bend providing more contact area to bond pads

**MAXIMUM CASE TEMPERATURE FOR SOLDERING PURPOSES:** 260°C for 10 seconds

**WAFER FAB LOCATION:** Phoenix, Arizona

**ASSEMBLY/TEST LOCATION:** Seremban, Malaysia

**PLASTIC SURFACE MOUNT  
ZENER OVERVOLTAGE  
TRANSIENT  
SUPPRESSORS  
6.8–91 VOLTS  
1500 WATT PEAK POWER**



**CASE 403  
PLASTIC**

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Power Dissipation (1) @ $T_L \leq 25^\circ\text{C}$	$P_{PK}$	1500	Watts
Forward Surge Current (2) @ $T_A = 25^\circ\text{C}$	$I_{FSM}$	200	Amps
Thermal Resistance from Junction to Lead (typical)	$R_{\theta JL}$	15	$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_J, T_{stg}$	– 65 to +150	$^\circ\text{C}$

NOTES: 1. Nonrepetitive current pulse per Figure 2 and derated above  $T_A = 25^\circ\text{C}$  per Figure 3.

2. 1/2 sine wave (or equivalent square wave),  $PW = 8.3$  ms, duty cycle = 4 pulses per minute maximum.

# GENERAL DATA — 1500 WATT PEAK POWER

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted).

Device††	Reverse Stand-Off Voltage V <sub>R</sub> Volts (1)	Breakdown Voltage*		Maximum Clamping Voltage V <sub>C</sub> @ I <sub>pp</sub> Volts	Peak Pulse Current (See Figure 2) I <sub>pp</sub> † Amps	Maximum Reverse Leakage @ V <sub>R</sub> I <sub>R</sub> μA	Device Marking
		V <sub>BR</sub> @ I <sub>T</sub>					
		Volts Min	mA				
1SMC5.0AT3	5.0	6.40	10	9.2	163.0	1000	GDE
1SMC6.0AT3	6.0	6.67	10	10.3	145.6	1000	GDG
1SMC6.5AT3	6.5	7.22	10	11.2	133.9	500	GDK
1SMC7.0AT3	7.0	7.78	10	12.0	125.0	200	GDM
1SMC7.5AT3	7.5	8.33	1.0	12.9	116.3	100	GDP
1SMC8.0AT3	8.0	8.89	1.0	13.6	110.3	50	GDR
1SMC8.5AT3	8.5	9.44	1.0	14.4	104.2	20	GDT
1SMC9.0AT3	9.0	10.0	1.0	15.4	97.4	10	GDV
1SMC10AT3	10	11.1	1.0	17.0	88.2	5.0	GDX
1SMC11AT3	11	12.2	1.0	18.2	82.4	5.0	GDZ
1SMC12AT3	12	13.3	1.0	19.9	75.3	5.0	GEE
1SMC13AT3	13	14.4	1.0	21.5	69.7	5.0	GEG
1SMC14AT3	14	15.6	1.0	23.2	64.7	5.0	GEK
1SMC15AT3	15	16.7	1.0	24.4	61.5	5.0	GEM
1SMC16AT3	16	17.8	1.0	26.0	57.7	5.0	GEP
1SMC17AT3	17	18.9	1.0	27.6	53.3	5.0	GER
1SMC18AT3	18	20.0	1.0	29.2	51.4	5.0	GET
1SMC20AT3	20	22.2	1.0	32.4	46.3	5.0	GEV
1SMC22AT3	22	24.4	1.0	35.5	42.2	5.0	GEX
1SMC24AT3	24	26.7	1.0	38.9	38.6	5.0	GEZ
1SMC26AT3	26	28.9	1.0	42.1	35.6	5.0	GFE
1SMC28AT3	28	31.1	1.0	45.4	33.0	5.0	GFG
1SMC30AT3	30	33.3	1.0	48.4	31.0	5.0	GFK
1SMC33AT3	33	36.7	1.0	53.3	28.1	5.0	GFM
1SMC36AT3	36	40.0	1.0	58.1	25.8	5.0	GFP
1SMC40AT3	40	44.4	1.0	64.5	23.2	5.0	GFR
1SMC43AT3	43	47.8	1.0	69.4	21.6	5.0	GFT
1SMC45AT3	45	50.0	1.0	72.7	20.6	5.0	GFV
1SMC48AT3	48	53.3	1.0	77.4	19.4	5.0	GFX
1SMC51AT3	51	56.7	1.0	82.4	18.2	5.0	GFZ
1SMC54AT3	54	60.0	1.0	87.1	17.2	5.0	GGE
<b>1SMC58AT3</b>	<b>58</b>	<b>64.4</b>	<b>1.0</b>	<b>93.6</b>	<b>16.0</b>	<b>5.0</b>	<b>GGG</b>
1SMC60AT3	60	66.7	1.0	96.8	15.5	5.0	GGK
1SMC64AT3	64	71.1	1.0	103	14.6	5.0	GGM
1SMC70AT3	70	77.8	1.0	113	13.3	5.0	GGP
1SMC75AT3	75	83.3	1.0	121	12.4	5.0	GGR
1SMC78AT3	78	86.7	1.0	126	11.4	5.0	GGT

Note 1: A transient suppressor is normally selected according to the reverse "Stand Off Voltage" (V<sub>R</sub>) which should be equal to or greater than the DC or continuous peak operating voltage level.

\* V<sub>BR</sub> measured at pulse test current I<sub>T</sub> at an ambient temperature of 25°C.

† Surge current waveform per Figure 2 and derate per Figure 3 of the General Data — 1500 Watt at the beginning of this group.

†† T3 suffix designates tape and reel of 2500 units.

## ABBREVIATIONS AND SYMBOLS

**V<sub>R</sub>** Stand Off Voltage. Applied reverse voltage to assure a non-conductive condition (See Note 1).

**V<sub>(BR)min</sub>** This is the minimum breakdown voltage the device will exhibit and is used to assure that conduction does not occur prior to this voltage level at 25°C.

**V<sub>C</sub>** Maximum Clamping Voltage. The maximum peak voltage appearing across the transient suppressor when

subjected to the peak pulse current in a one millisecond time interval. The peak pulse series resistance and thermal rise.

**I<sub>pp</sub>** Peak Pulse Current — See Figure 2

**P<sub>p</sub>** Peak Pulse Power

**I<sub>R</sub>** Reverse Leakage

## GENERAL DATA — 1500 WATT PEAK POWER

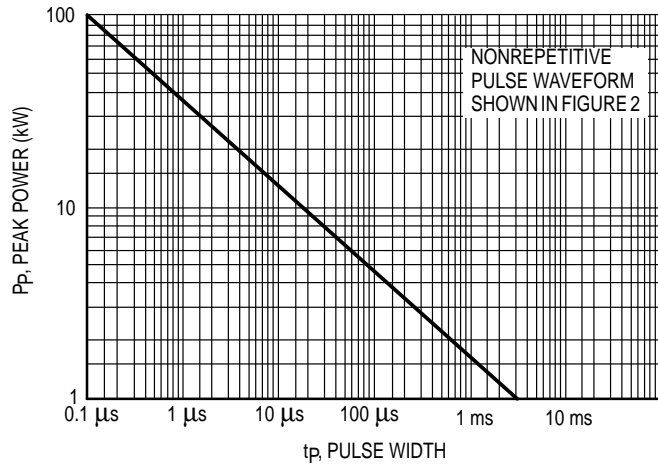


Figure 1. Pulse Rating Curve

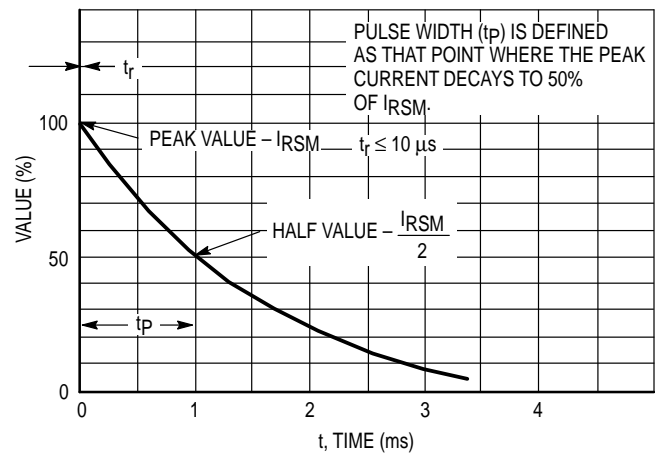


Figure 2. Pulse Waveform

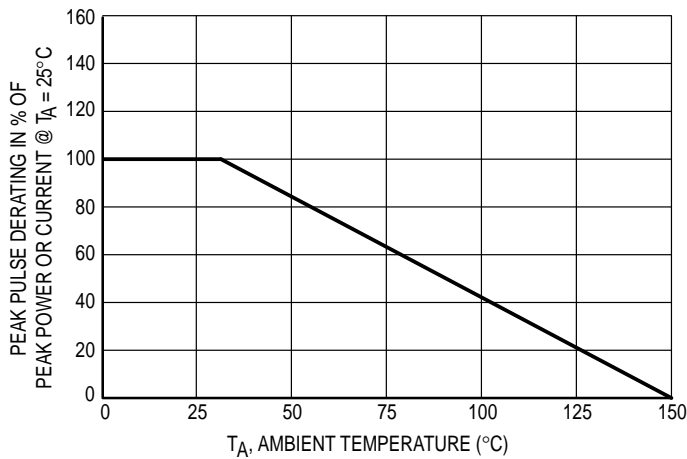


Figure 3. Pulse Derating Curve

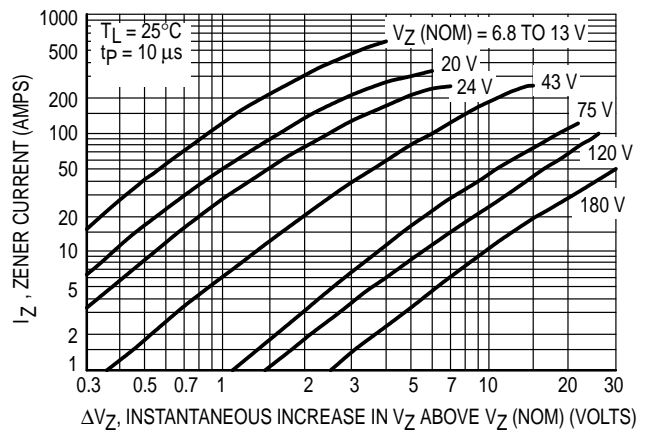


Figure 4. Dynamic Impedance

## UL RECOGNITION

The entire series has *Underwriters Laboratory Recognition* for the classification of protectors (QVGV2) under the UL standard for safety 497B and File #116110. Many competitors only have one or two devices recognized or have recognition in a non-protective category. Some competitors have no recognition at all. With the UL497B recognition, our parts successfully passed several tests including Strike Voltage

Breakdown test, Endurance Conditioning, Temperature test, Dielectric Voltage-Withstand test, Discharge test and several more.

Whereas, some competitors have only passed a flammability test for the package material, we have been recognized for much more to be included in their Protector category.

# GENERAL DATA — 1500 WATT PEAK POWER

## APPLICATION NOTES

### RESPONSE TIME

In most applications, the transient suppressor device is placed in parallel with the equipment or component to be protected. In this situation, there is a time delay associated with the capacitance of the device and an overshoot condition associated with the inductance of the device and the inductance of the connection method. The capacitive effect is of minor importance in the parallel protection scheme because it only produces a time delay in the transition from the operating voltage to the clamp voltage as shown in Figure 5.

The inductive effects in the device are due to actual turn-on time (time required for the device to go from zero current to full current) and lead inductance. This inductive effect produces an overshoot in the voltage across the equipment or component being protected as shown in Figure 6. Minimizing this overshoot is very important in the application, since the main purpose for adding a transient suppressor is to clamp voltage spikes. The SMC series have a very good response time, typically  $< 1$  ns and negligible inductance. However, external inductive effects could produce unacceptable overshoot. Proper circuit layout, minimum lead lengths and placing the

suppressor device as close as possible to the equipment or components to be protected will minimize this overshoot.

Some input impedance represented by  $Z_{in}$  is essential to prevent overstress of the protection device. This impedance should be as high as possible, without restricting the circuit operation.

### DUTY CYCLE DERATING

The data of Figure 1 applies for non-repetitive conditions and at a lead temperature of  $25^{\circ}\text{C}$ . If the duty cycle increases, the peak power must be reduced as indicated by the curves of Figure 7. Average power must be derated as the lead or ambient temperature rises above  $25^{\circ}\text{C}$ . The average power derating curve normally given on data sheets may be normalized and used for this purpose.

At first glance the derating curves of Figure 7 appear to be in error as the 10 ms pulse has a higher derating factor than the  $10\ \mu\text{s}$  pulse. However, when the derating factor for a given pulse of Figure 7 is multiplied by the peak power value of Figure 1 for the same pulse, the results follow the expected trend.

### TYPICAL PROTECTION CIRCUIT

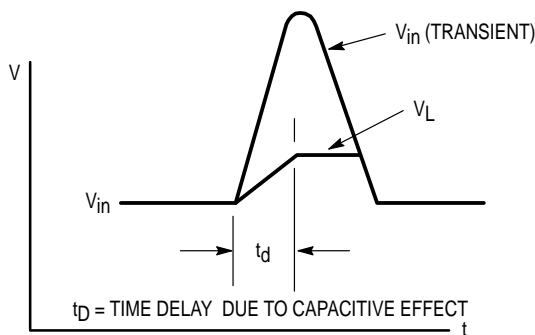
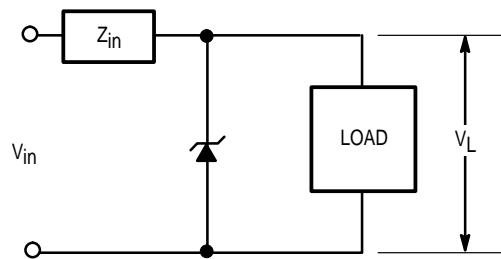


Figure 5.

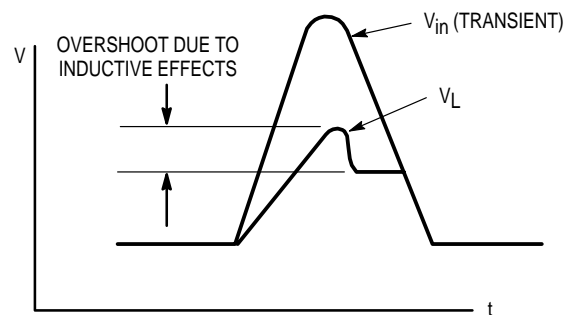


Figure 6.

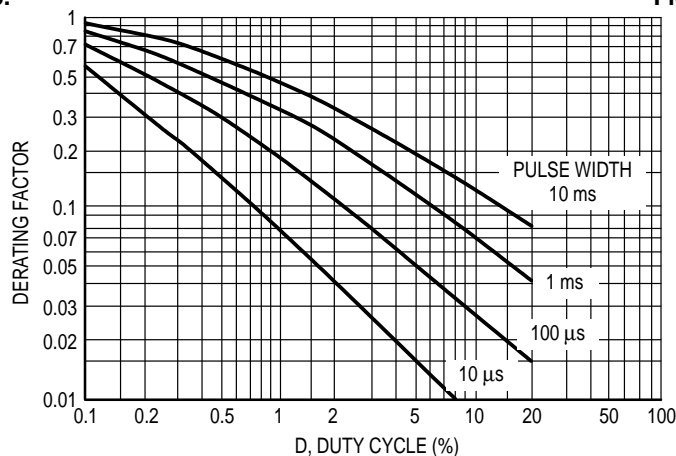
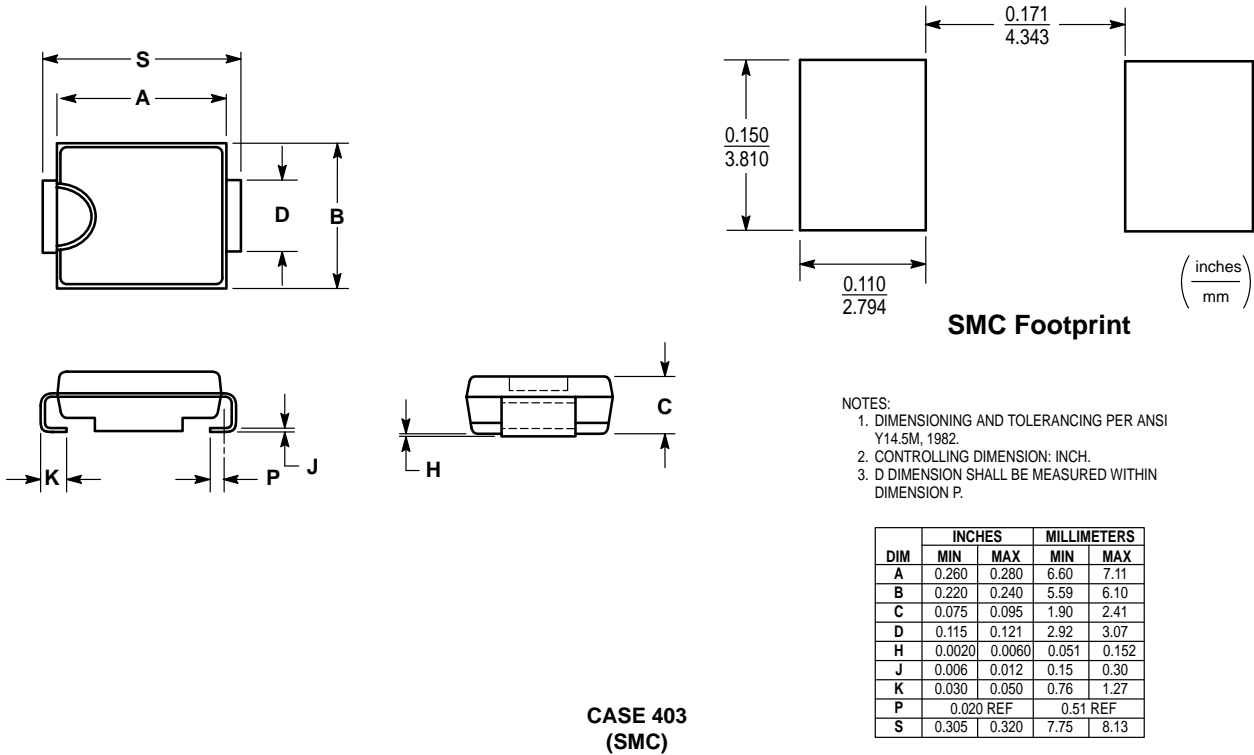


Figure 7. Typical Derating Factor for Duty Cycle

# GENERAL DATA — 1500 WATT PEAK POWER

## Transient Voltage Suppressors — Surface Mounted

### 1500 Watt Peak Power



(Refer to Section 10 for Surface Mount, Thermal Data and Footprint Information.)

### MULTIPLE PACKAGE QUANTITY (MPQ) REQUIREMENTS

Package Option	Type No. Suffix	MPQ (Units)
Tape and Reel	T3 (13 inch reel)	2.5K

(Refer to Section 10 for more information on Packaging Specifications.)