

T300A SERIES (ECL) STANDARD SPECIFICATIONS

FREQUENCY RANGE FREQUENCY ACCURACY @ + 25 °C FREQUECY STABILITY Vs. TEMPERATURE OPERATING TEMPERATURE RANGE INPUT VOLTAGE (See Note Below)

INPUT CURRENT @ - 4.5 VDC

OUTPUT LOAD SYMMETRY RISE & FALL TIMES (10% to 90% Level)

START-UP TIME

FREQUENCY STABILITY Vs. VOLTAGE

AGING @ +25°C

PACKAGE, SEAL & LEAD FINISH

10 MHz to 240 MHz ± 0.0015% (± 15 PF See Options Below See Options Below - 4.5 VDC ± 5%

50 mA Max.

100K Compatible 100Ω to -2.0 VDC60/40% @ 50% Leve 2 nS Max.

15 mS Max.

± 0.0002% (± 2 PPM) Max.

(for 5% change in Voltage)

± 0.0005% (± 5 PPM)/year Max.

Conforms with the Requirements of MIL-PRF-55310

0.025" 0.200" Max Ceramic Stand-off (Optional) Pin 1 is identified with a Sq. Corne 0.439 0.254" Max. 0.507" 0.877

Pin Connections

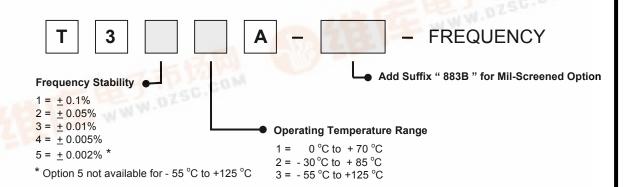
14 **GND/CASE** 7 -4.5 VDC 8 **OUTPUT** N/C

All Others MISSING

NOTE: For PECL applications, Xsis 300 Series ECL oscillators can be operated with +5 VDC ± 10% on Pin 14 and power supply return on Pin 7. The output logic levels will still be referenced to +5 VDC and the case will be at +5 VDC, however, 0.8 V peak to peak output signal can be AC or DC coupled as necessary.

Contact Xsis Engineering for special requirements such as, Output Symmetry, Start-up Time, Frequency Accuracy, Complementary Outputs, Multiple Outputs, etc.

ORDERING INFORMATION (Select from options below):



EXAMPLE: T343A - 883B - 24.000 MHz = 14 Pin Package, 100K ECL, \pm 0.005% over -55 °C to +125 °C, Mil-Screened, and 24.000 MHz