Ordering number: EN1756B

NPN Epitaxial Planar Silicon Transistor



2SC3595

# **Ultrahigh-Definition CRT Display Video Output Applications**

## **Applications**

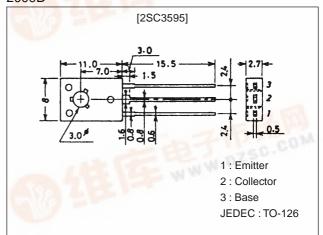
- · Ultrahigh-definition CRT display.
- · Video output driver.
- · Wideband amplifiers.

### **Features**

· High  $f_T$ :  $f_T$  typ=2.0GHz. · High current : I<sub>C</sub>=500mA.

## **Package Dimensions**

unit:mm 2009B



## **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V <sub>CBO</sub>		30	V
Collector-to-Emitter Voltage	V <sub>CEO</sub>		20	V
Emitter-to-Base Voltage	V <sub>EBO</sub>	140	3	V
Collector Current	IC		500	mA
Collector Current (Pulse)	ICP	- LA [17]	1000	mA
Collector Dissipation	PC	AND AND THE W	1.2	W
		Tc=25°C	5	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg	D Last	-55 to +150	°C

#### Electrical Characteristics at Ta = 25°C

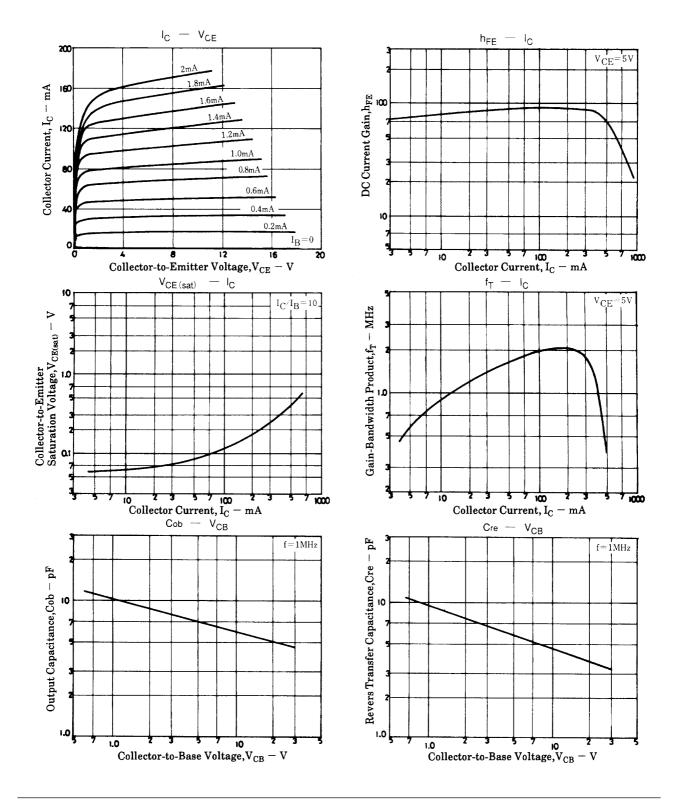
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I <sub>CBO</sub>	V <sub>CB</sub> =20V, I <sub>E</sub> =0			0.1	μΑ
Emitter Cutoff Current	I <sub>EBO</sub>	V <sub>EB</sub> =2V			5.0	μΑ
DC Current Gain	h <sub>FE</sub> 1	V <sub>CE</sub> =5V, I <sub>C</sub> =50mA	40*	-m	200*	100
	h <sub>FE</sub> 2	V <sub>CE</sub> =5V, I <sub>C</sub> =500mA	20		20.	
Gain-Bandwidth Product	fT	V <sub>CE</sub> =5V, I <sub>C</sub> =100mA	AL W	2.0		GHz
Collector-to-Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =300mA, I <sub>B</sub> =3 <mark>0mA</mark>		0.25	0.6	V
Base-to-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =300mA, I <sub>B</sub> =30mA		0.92	1.2	V

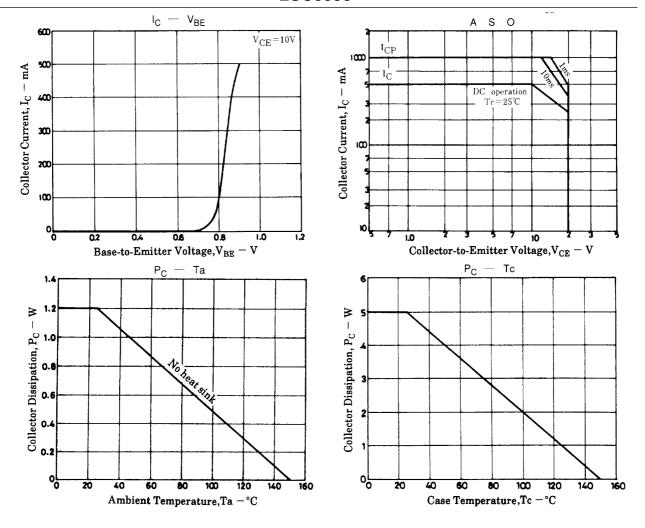
<sup>\*:</sup> The 2SC3595 is classified by 50mA h<sub>FE</sub> as follows: 40 C 80

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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Base Breakdown Voltage	V <sub>(BR)</sub> CBO	I <sub>C</sub> =10μA, I <sub>E</sub> =0	30			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I <sub>C</sub> =1mA, R <sub>BE</sub> =∞	20			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I <sub>E</sub> =100μA, I <sub>C</sub> =0	3			V
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =10V, f=1MHz		6.0		pF
Reverse Transfer Capacitance	Cre	V <sub>CB</sub> =10V, f=1MHz		4.6		pF





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