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AAT4250
Slew Rate Controlled Load Switch

SmartSwitch™

General Description

The AAT4250 SmartSwitch™ is a member of AATI's Application Specific Power MOSFET™ (ASPM™) product family. It is a Slew Rate Controlled P-channel MOSFET power switch designed for high-side load-switching applications. This switch operates with an input voltage range from 1.8V to 5.5V, making it ideal for 2.5V, 3.3V or 5V systems. The part features 1.5ms turn on and 10µs turn off time. The AAT4250 has an under volt-age lock out which turns off the switch when an under-voltage condition exists. Input logic levels are TTL compatible. The quiescent supply current is very low, typically 2µA. In shutdown mode, the supply current is typically reduced to 0.1µA or less.

The AAT4250 is available in a 5-pin SOT23 (SOT25) and 8-pin SC70JW specified over -40 to 85°C.
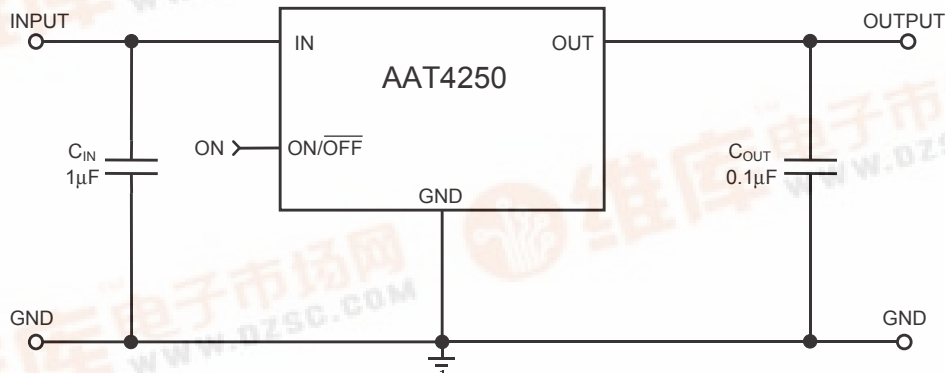
Features

- 1.8V to 5.5V Input voltage range
- 120mΩ (5V) typical $R_{DS(ON)}$
- Low quiescent current
  - Typical 2µA
  - Typical 0.1µA with Enable off
- Only 2.0V needed for ON/$\overline{OFF}$ Control
- Temperature range -40º to 85°C
- 5kV ESD rating
- 5-pin SOT23 (SOT25) or SC70JW-8 package

Applications

- Hot swap supplies
- Notebook computers
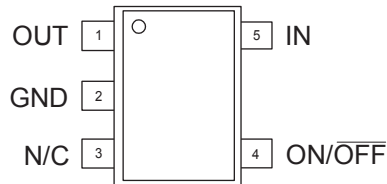- Personal communication devices
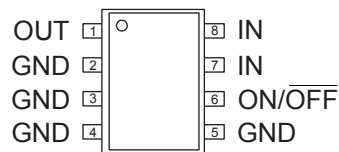
Typical Application

Pin Descriptions

Pin #			
SOT23-5	**SC70JW**	**Symbol**	**Function**
1	1	OUT	P-channel MOSFET drain
2	2, 3, 4, 5	GND	Ground connection
3	n/a	NC	Not internally connected
4	6	ON/$\overline{\text{OFF}}$	Active-High Enable Input (Logic high turns the switch on)
5	7, 8	IN	P-channel MOSFET source

Pin Configuration

SOT23-5 (SOT25)
(Top View)

OUT [1]   [5] IN
GND [2]
N/C [3]   [4] ON/$\overline{\text{OFF}}$

SC70JW-8
(Top View)

OUT [1]   [8] IN
GND [2]   [7] IN
GND [3]   [6] ON/$\overline{\text{OFF}}$
GND [4]   [5] GND

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Description		Value	Units
V_{IN}	IN to GND		-0.3 to 6	V
V_{ON}	ON/$\overline{OFF}$ to GND		-0.3 to 6	V
V_{OUT}	OUT to GND		-0.3 to V_{IN}+0.3	V
I_{MAX}	Maximum Continuous Switch Current		1.7	A
I_{DM}	Maximum Pulsed Current	IN ≥ 2.5V	4	A
		IN < 2.5V	2	A
T_J	Operating Junction Temperature Range		-40 to 150	°C
T_{LEAD}	Maximum Soldering Temperature (at Leads)		300	°C
V_{ESD}	ESD Rating[1] - HBM		5000	V

Note: Stresses above those listed in Absolute Maximum Ratings may cause permanent damage to the device. Functional operation at conditions other than the operating conditions specified is not implied. Only one Absolute Maximum rating should be applied at any one time.

Note 1: Human body model is a 100pF capacitor discharged through a 1.5kΩ resistor into each pin.

Thermal Characteristics

Symbol	Description	Value	Units
Θ_{JA}	Thermal Resistance (SOT23-5 [SOT25] or SC70JW-8)[2]	150	°C/W
P_D	Power Dissipation (SOT23-5 [SOT25]or SC70JW-8)[2]	667	mW

Note 2: Mounted on an AAT4250 demo board in still 25°C air.

Electrical Characteristics (V_{IN} = 5V, T_A = -40 to 85°C unless otherwise noted. Typical values are at T_A=25°C)

| Symbol	Description	Conditions	Min	Typ	Max	Units
V_{IN}	Operation Voltage		1.8 [3]		5.5	V
I_Q	Quiescent Current	V_{IN} = 5V, ON/$\overline{OFF}$ = V_{IN}, I_{OUT} = 0		2	4	µA
$I_{Q(OFF)}$	Off Supply Current	ON/$\overline{OFF}$ = GND, V_{IN} = 5V, OUT open			1	µA
$I_{SD(OFF)}$	Off Switch Current	ON/$\overline{OFF}$ = GND, V_{IN} = 5V, V_{OUT} = 0		0.1	1	µA
V_{UVLO}	Undervoltage Lockout	V_{IN} falling	1.0	1.5	1.8	V
$V_{UVLO(hys)}$	Undervoltage Lockout hysteresis			250		mV
$R_{DS(ON)}$	On-Resistance	V_{IN} = 5V, T_A = 25° C		120	175	mΩ
		V_{IN} = 3V, T_A = 25° C		135	200	mΩ
		V_{IN} =1.8V		165		mΩ
TC_{RDS}	On-Resistance Temp-Co			2800		ppm/°C
V_{IL}	ON/$\overline{OFF}$ Input Logic Low Voltage	V_{IN} = 2.7V to 5.5V [4]			0.8	V
V_{IH}	ON/$\overline{OFF}$ Input Logic High Voltage	V_{IN} = 2.7V to ≤ 4.2V	2.0			V
		V_{IN} = > 4.2V to 5.5V	2.4			
I_{SINK}	ON Input leakage	V_{ON} = 5V		0.01	1	µA
T_D	Output Turn-On Delay Time			300		µs
T_{DOFF}	Turn-Off Delay Time	V_{IN}=5V, R_{LOAD}=10Ω			10	µs
		V_{IN}=3V, R_{LOAD}=5Ω			10	
T_{ON}	Turn-On Rise Time	V_{IN}=5V, R_{LOAD}=16.5Ω, T_A=0 to 50° C	1000			µs
		V_{IN}=5V, R_{LOAD}=10Ω, C_{OUT}=0.1µF		1500		
		V_{IN}=3V, R_{LOAD}=5Ω, C_{OUT}=0.1µF		1500		

Note 3: Part requires minimum start-up of V_{IN} ≥ 2.0V to ensure operation down to 1.8V.

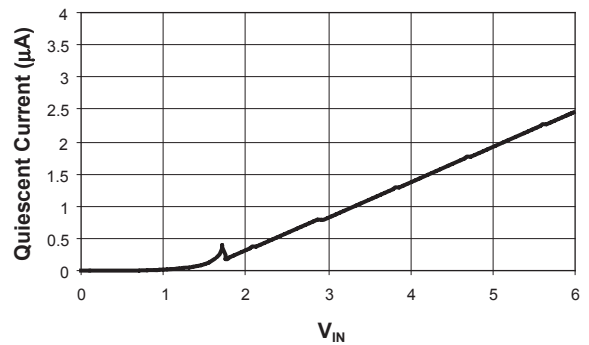Note 4: For V_{IN} outside this range consult typical ON/OFF threshold curve.

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Typical Characteristics

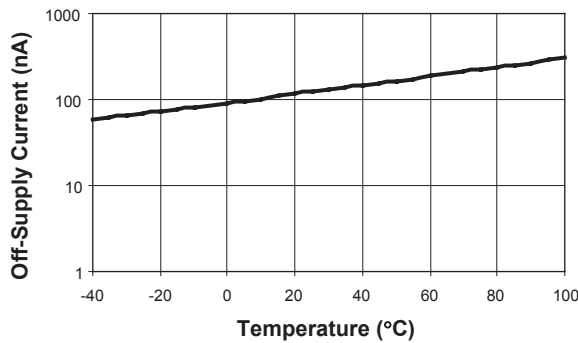(Unless otherwise noted, V_{IN} = 5V, T_A = 25°C)

Quiescent Current vs. Temperature
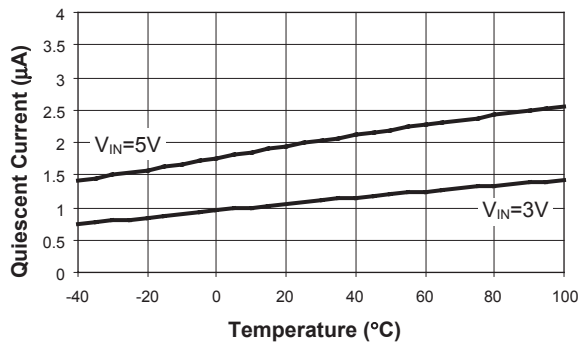
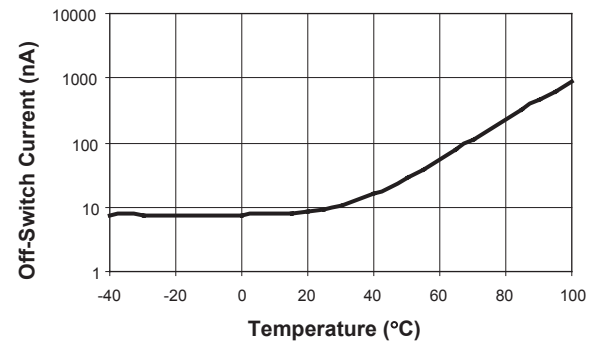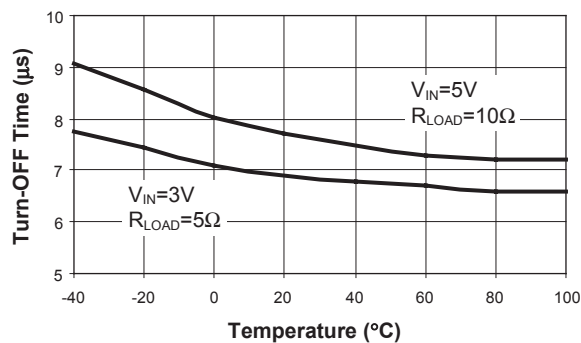### Quiescent Current vs. V_{IN}

Off-Supply Current vs. Temperature
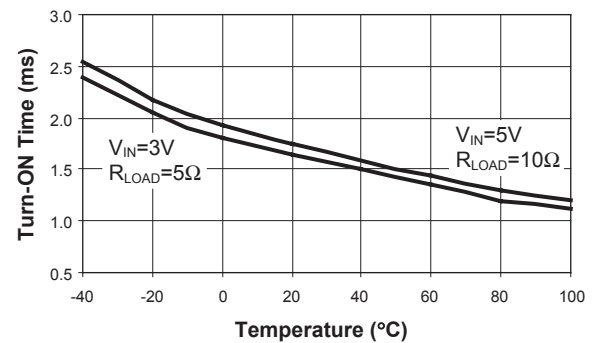

Off-Switch Current vs. Temperature

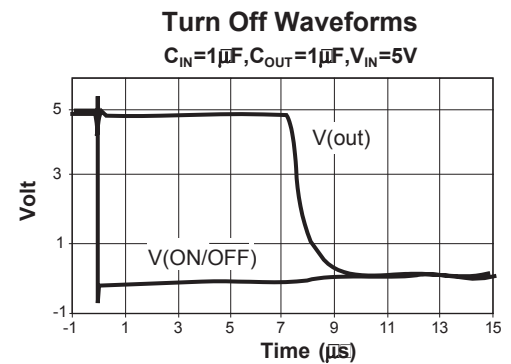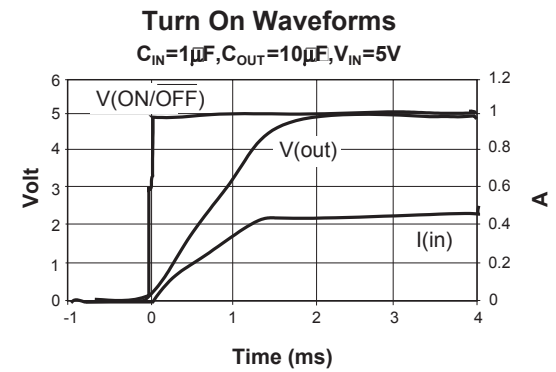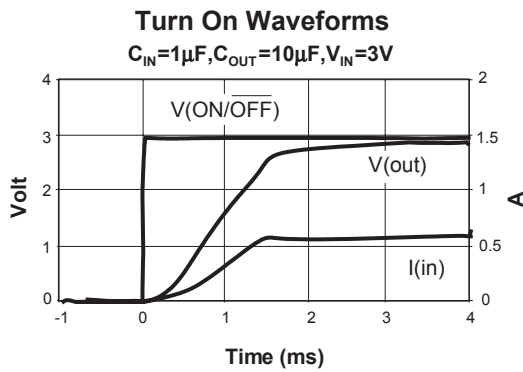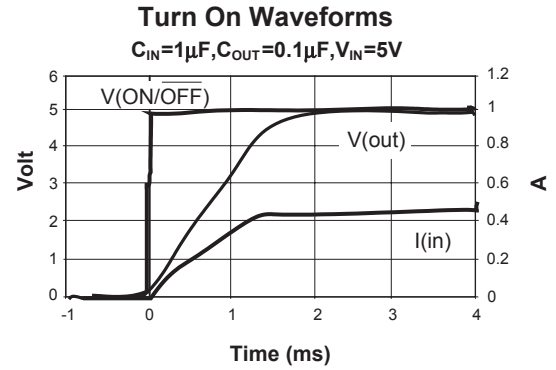### Turn-OFF Time vs. Temperature
C_{IN}=1μF, C_{OUT}=0.1μF

Turn-On Time vs. Temperature
C_{IN}=1μF, C_{OUT}=0.1μF

(Unless otherwise noted, V_{IN} = 5V, T_A = 25°C)

Turn On Waveforms
C_{IN}=1µF,C_{OUT}=0.1µF,V_{IN}=3V

Turn On Waveforms
C_{IN}=1µF,C_{OUT}=0.1µF,V_{IN}=5V

Turn On Waveforms
C_{IN}=1µF,C_{OUT}=10µF,V_{IN}=3V

Turn On Waveforms
C_{IN}=1µF,C_{OUT}=10µF,V_{IN}=5V

Turn Off Waveforms
C_{IN}=1µF,C_{OUT}=1µF,V_{IN}=3V

Turn Off Waveforms
C_{IN}=1µF,C_{OUT}=1µF,V_{IN}=5V
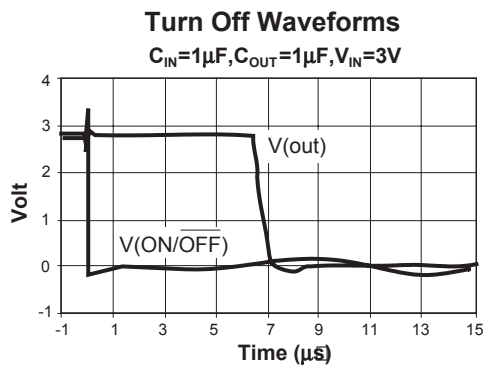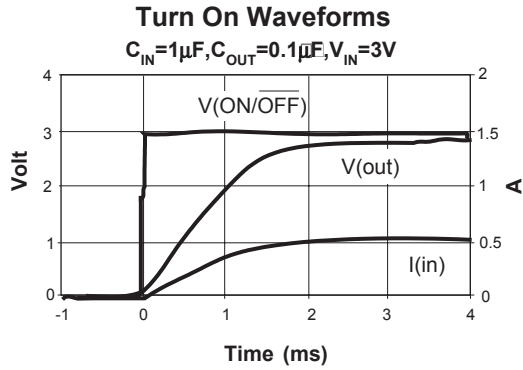
(Unless otherwise noted, V_{IN} = 5V, T_A = 25°C)

$R_{DS(ON)}$ vs. Temperature
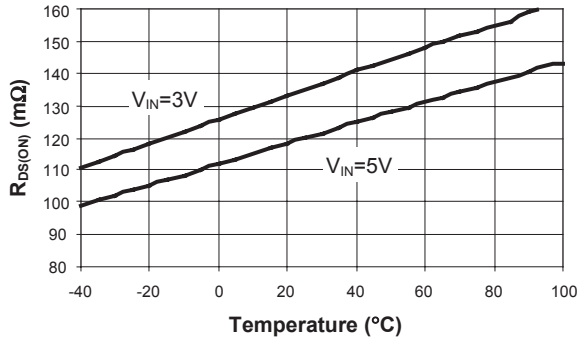
$R_{DS(ON)}$ vs. V_{IN}
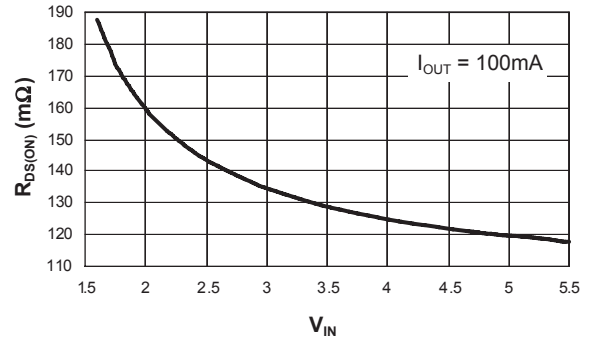
Typical ON/$\overline{OFF}$ Threshold vs. V_{IN}
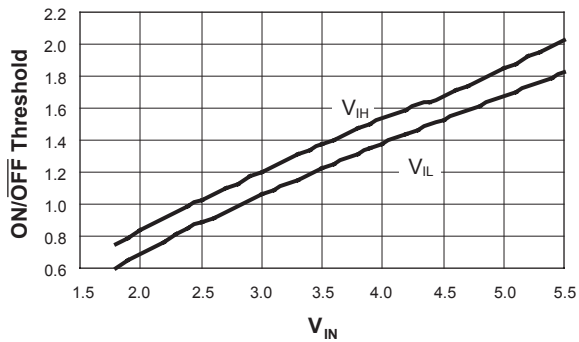
Functional Block Diagram
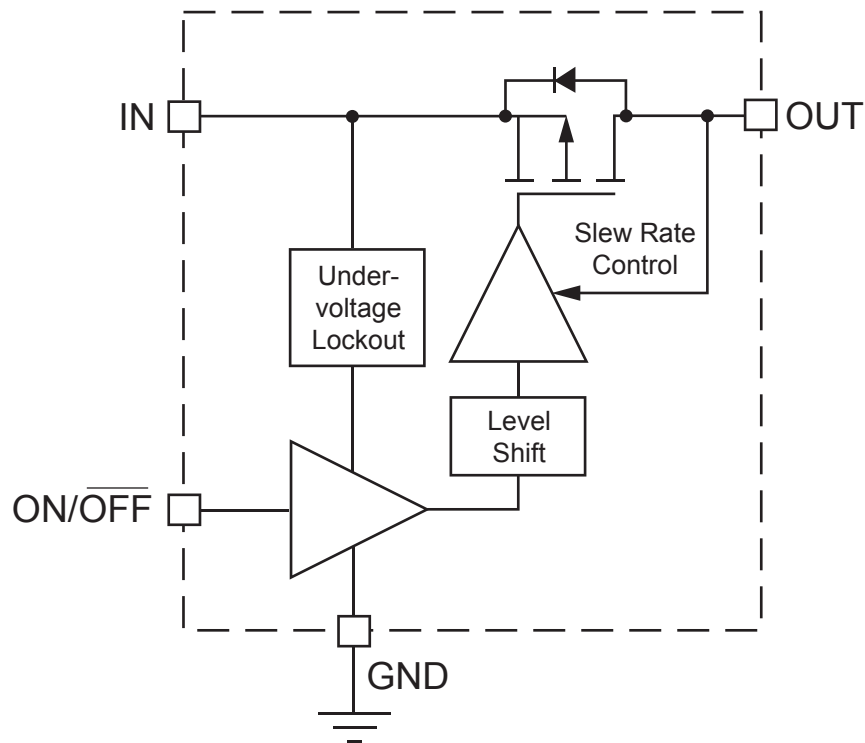
Functional Description

The AAT4250 is a slew rate controlled P-channel MOSFET power switch designed for high-side load-switching applications. It operates with input voltages ranging from 1.8V to 5.5V which, along with its extremely low operating current, makes it ideal for battery-powered applications. In cases where the input voltage drops below 1.8V, the AAT4250 MOSFET is protected from entering the saturated region of operation by automatically shutting down. In addition, the TTL compatible ON/$\overline{\text{OFF}}$ pin makes the AAT4250 an ideal level shifted load-switch. The slew rate controlling feature eliminates in-rush cur-

rent when the MOSFET is turned on, allowing the AAT4250 to be implemented with a small input capacitor, or no input capacitor at all. During slewing, the current ramps linearly until it reaches the level required for the output load condition. The proprietary control method works by careful control and monitoring of the MOSFET gate voltage. When the device is switched ON, the gate voltage is quickly increased to the threshold level of the MOSFET. Once at this level, the current begins to slew as the gate voltage is slowly increased until the MOSFET becomes fully enhanced. Once it has reached this point, the gate is quickly increased to the full input voltage and $R_{DS(ON)}$ is minimized.

Applications Information

Input Capacitor

Typically a 1µF or larger capacitor is recommended for C_{IN} in most applications. A C_{IN} capacitor is not required for basic operation, however, it is useful in preventing load transients from affecting up stream circuits. C_{IN} should be located as close to the device V_{IN} pin as practically possible. Ceramic, tantalum or aluminum electrolytic capacitors may be selected for C_{IN}. There is no specific capacitor ESR requirement for C_{IN}. However, for higher current operation, ceramic capacitors are recommended for C_{IN} due to their inherent capability over tantalum capacitors to withstand input current surges from low impedance sources such as batteries in portable devices.

Output Capacitor

For proper slew operation, a 0.1µF capacitor or greater between VOUT and GND is required.

Likewise, with the output capacitor, there is no specific capacitor ESR requirement. If desired, C_{OUT} maybe increased without limit to accommodate any load transient condition without adversely affecting the slew rate.

Enable Function

The AAT4250 features an enable / disable function. This pin (ON) is active high and is compatible with TTL or CMOS logic. To assure the load switch will turn on, the ON control level must be greater than 2.0 volts. The load switch will go into shutdown mode when the voltage on the ON pin falls below 0.8 volts. When the load switch is in shutdown mode, the OUT pin is tristated, and quiescent current drops to leakage levels below 1µA.

Reverse Output to Input Voltage Conditions and Protection

Under normal operating conditions a parasitic diode exists between the output and input of the load switch. The input voltage should always remain greater than the output load voltage maintaining a reverse bias on the internal parasitic diode. Conditions where V_{OUT} might exceed V_{IN} should be avoided since this would forward bias the internal parasitic diode and allow excessive current flow into the V_{OUT} pin and possibly damage the load switch.

In applications where there is a possibility of V_{OUT} exceeding V_{IN} for brief periods of time during normal operation, the use of a larger value C_{IN} capacitor is highly recommended. A larger value of C_{IN} with respect to C_{OUT} will effect a slower C_{IN} decay rate during shutdown, thus preventing V_{OUT} from exceeding V_{IN}. In applications where there is a greater danger of V_{OUT} exceeding V_{IN} for extended periods of time, it is recommended to place a schottky diode from V_{IN} to V_{OUT} (connecting the cathode to V_{IN} and anode to V_{OUT}). The Schottky diode forward voltage should be less then 0.45 volts.

Thermal Considerations and High Output Current Applications

The AAT4250 is designed to deliver a continuous output load current. The limiting characteristic for maximum safe operating output load current is package power dissipation. In order to obtain high operating currents, careful device layout and circuit operating conditions need to be taken into account.

The following discussions will assume the load switch is mounted on a printed circuit board utilizing the minimum recommended footprint as stated in the layout considerations section.

At any given ambient temperature (T_A) the maximum package power dissipation can be determined by the following equation:

$$P_{D(MAX)} = [T_{J(MAX)} - T_A] / \Theta_{JA}$$

Constants for the AAT4250 are maximum junction temperature, $T_{J(MAX)}$ = 125°C, and package thermal resistance, Θ_{JA} = 150°C/W. Worst case conditions are calculated at the maximum operating temperature where T_A = 85°C. Typical conditions are calculated under normal ambient conditions where T_A = 25°C. At T_A = 85°C, $P_{D(MAX)}$ = 267mW. At T_A = 25°C, $P_{D(MAX)}$ = 667mW.

The maximum continuous output current for the AAT4250 is a function of the package power dissipation and the R_{DS} of the MOSFET at $T_{J(MAX)}$. The maximum R_{DS} of the MOSFET at $T_{J(MAX)}$ is calculated by increasing the maximum room temperature R_{DS} by the R_{DS} temperature coefficient. The temperature coefficient (TC) is 2800ppm/°C. Therefore, at 125°C

$$R_{DS(MAX)} = R_{DS(25°C)} \times (1 + TC \times \Delta T)$$
$$R_{DS(MAX)} = 175m\Omega \times (1 + .002800 \times (125°C-25°C))$$
$$R_{DS(MAX)} = 224m\Omega$$

For maximum current, refer to the following equation:

$$I_{OUT(MAX)} < (P_{D(MAX)} / R_{DS})^{1/2}$$

For example, if V_{IN} = 5V, $R_{DS(MAX)}$=224mΩ and T_A = 25°C, $I_{OUT(MAX)}$ = 1.7A. If the output load current were to exceed 1.7A or if the ambient temperature were to increase, the internal die temperature will increase, and the device will be damaged.

Higher peak currents can be obtained with the AAT4250. To accomplish this, the device thermal resistance must be reduced by increasing the heat sink area or by operating the load switch in a duty cycle manner. Duty cycles with peaks less than 2ms in duration can be considered using the method below.

High Peak Output Current Applications

Some applications require the load switch to operate at a continuous nominal current level with short duration high current peaks. Refer to the I_{DM} specification in the Absolute Maximum table to ensure the AAT 4250's maximum pulsed current rating is not exceeded. The duty cycle for both output current levels must be taken into account. To do so, first calculate the power dissipation at the nominal continuous current level, and then add in the additional power dissipation due to the short duration high current peak scaled by the duty factor.

For example, a 4V system using an AAT4250 operates at a continuous 100mA load current level and has short 2A current peaks, as in a GSM application. The current peak occurs for 576μs out of a 4.61ms period.

First, the current duty cycle is calculated:

% Peak Duty Cycle: X/100 = 576μs/4.61ms
% Peak Duty Cycle = 12.5%

The load current is 100mA for 87.5% of the 4.61ms period and 2A for 12.5% of the period. Since the Electrical Characteristics do not report $R_{DS\,MAX}$ for 4 volts operation, it must be calculated approximated

by consulting the chart of R_{DSON} vs. V_{IN}. The Rds reported for 5 volt R_{DS} can be scaled by the ratio seen in the chart to derive the Rds for 4 volt V_{IN}: 175mΩ x 120mΩ/115mΩ = 183mΩ. Derated for temperature: 183mΩ x (1 + .002800 x (125°C - 25°C)) = 235mΩ. The power dissipation for a 100mA load is calculated as follows:

$P_{D(MAX)} = I^2_{OUT}$ x R_{DS}
$P_{D(100mA)} = (100mA)^2$ x 235mΩ
$P_{D(100mA)}$ = 2.35mW

$P_{D(87.5\%D/C)}$ = %DC x $P_{D(100mA)}$
$P_{D(87.5\%D/C)}$ = 0.875 x 2.35mW
$P_{D(87.5\%D/C)}$ = 2.1mW

The power dissipation for 100mA load at 87.5% duty cycle is 2.1mW. Now the power dissipation for the remaining 12.5% of the duty cycle at 2A is calculated:

$P_{D(MAX)} = I^2_{OUT}$ x R_{DS}
$P_{D(2A)} = (2A)^2$ x 235mΩ
$P_{D(2A)}$ = 940mW

$P_{D(12.5\%D/C)}$ = %DC x $P_{D(2A)}$
$P_{D(12.5\%D/C)}$ = 0.125 x 940mW
$P_{D(12.5\%D/C)}$ = 117.5mW

The power dissipation for 2A load at 12.5% duty cycle is 117mW. Finally, the two power figures are summed to determine the total true power dissipation under the varied load.

$P_{D(total)} = P_{D(100mA)} + P_{D(2A)}$
$P_{D(total)}$ = 2.1mW + 117.5mW
$P_{D(total)}$ = 120mW

The maximum power dissipation for the AAT4250 operating at an ambient temperature of 85°C is 267mW. The device in this example will have a total power dissipation of 120mW. This is well within the thermal limits for safe operation of the device, in fact, at 85°C, the AAT4250 will handle a 2A pulse for up to 28% duty cycle. At lower ambient temperatures the duty cycle can be further increased.

Printed Circuit Board Layout Recommendations

For proper thermal management, and to take advantage of the low R_{DSON} of the AAT4250, a few circuit board layout rules should be followed: Vin and Vout should be routed using wider than normal traces, and GND should be connected to a ground plane. For best performance, C_{IN} and C_{OUT} should be placed close to the package pins.

Evaluation Board Layout

The AAT4250 evaluation layout follows the printed circuit board layout recommendations, and can be used for good applications layout.

Note: Board layout shown is not to scale.
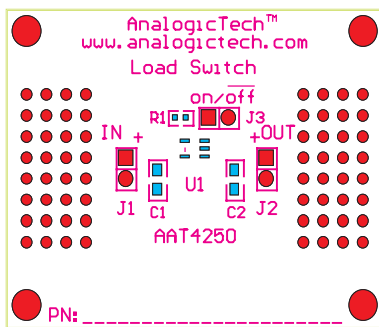
Figure 1: Evaluation board top side silk screen layout / assembly drawing
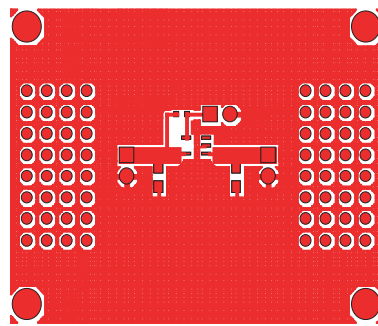
Figure 2: Evaluation board component side layout
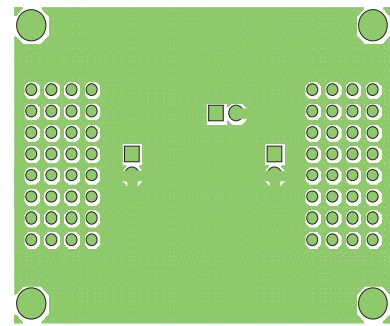
Figure 3: Evaluation board solder side layout

Ordering Information

Package	Marking[1]	Part Number (Tape and Reel)
SOT23-5 (SOT25)	ACXYY	**AAT4250IGV-T1**
SC70JW-8	ACXYY	**AAT4250IJS-T1**

Note: Sample stock is generally held on all part numbers listed in **BOLD**.
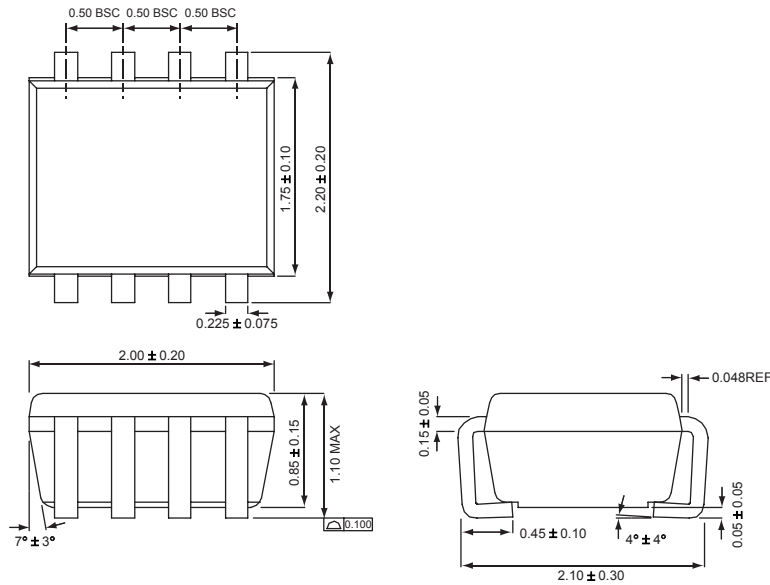Note 1: XYY = assembly and date code.

Package Information

SOT23-5 (SOT25)
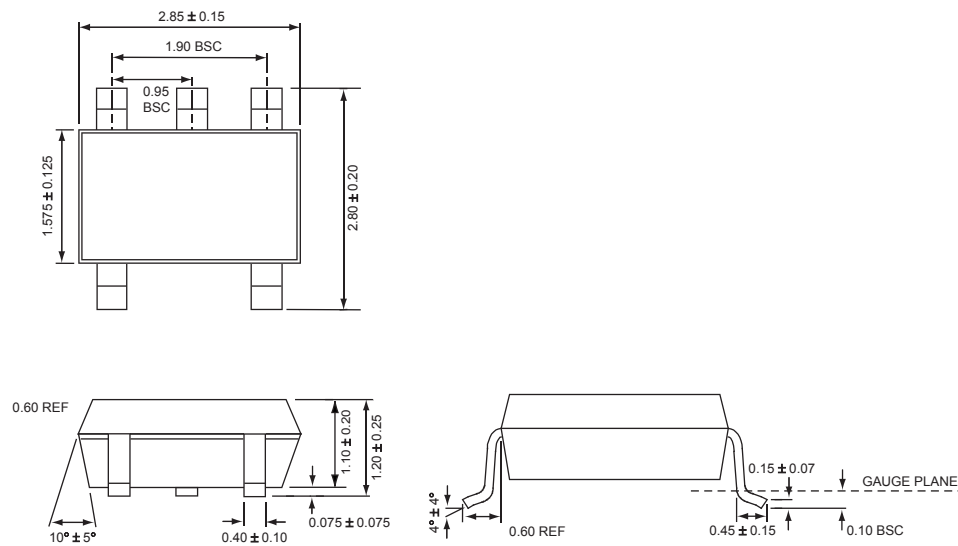
All dimensions in millimeters.

SC70JW-8

All dimensions in millimeters.

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