



March 1999

# LP2985

## Micropower SOT, 150 mA Low-Noise Ultra Low-Dropout Regulator

### Designed for Use with Very Low ESR Output Capacitors

#### General Description

The LP2985 is a 150 mA, fixed-output voltage regulator designed to provide ultra low-dropout and low noise in battery powered applications.

Using an optimized VIP™ (Vertically Integrated PNP) process, the LP2985 delivers unequalled performance in all specifications critical to battery-powered designs:

**Dropout Voltage:** Typically 300 mV @ 150 mA load, and 7 mV @ 1 mA load.

**Ground Pin Current:** Typically 850  $\mu$ A @ 150 mA load, and 75  $\mu$ A @ 1 mA load.

**Enhanced Stability:** The LP2985 is stable with output capacitor ESR as low as 5 m $\Omega$ , which allows the use of ceramic capacitors on the output.

**Sleep Mode:** Less than 1  $\mu$ A quiescent current when ON/OFF pin is pulled low.

**Smallest Possible Size:** SOT-23 package uses absolute minimum board space.

**Precision Output:** 1% tolerance output voltages available (A grade).

**Low Noise:** By adding a 10 nF bypass capacitor, output noise can be reduced to 30  $\mu$ V (typical).

Multiple voltage options, from 2.8V to 5.0V, are available as standard products. Consult factory for custom voltages.

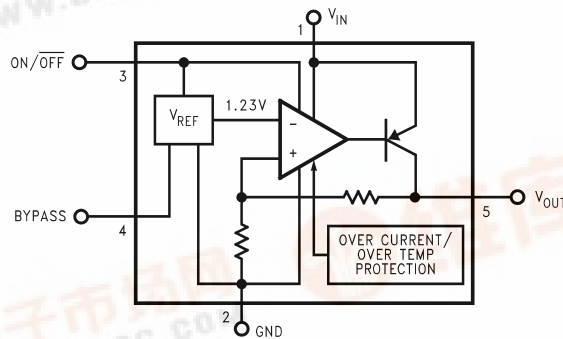
#### Features

- Ultra low dropout voltage
- Guaranteed 150 mA output current
- Smallest possible size (SOT-23 Package)
- Requires minimum external components
- Stable with low-ESR output capacitor
- <1  $\mu$ A quiescent current when shut down
- Low ground pin current at all loads
- Output voltage accuracy 1% (A Grade)
- High peak current capability
- Wide supply voltage range (16V max)
- Low  $Z_{OUT}$ : 0.3 $\Omega$  typical (10 Hz to 1 MHz)
- Overtemperature/overcurrent protection
- -40°C to +125°C junction temperature range
- Custom voltages available

#### Applications

- Cellular Phone
- Palmtop/Laptop Computer
- Personal Digital Assistant (PDA)
- Camcorder, Personal Stereo, Camera

#### Block Diagram



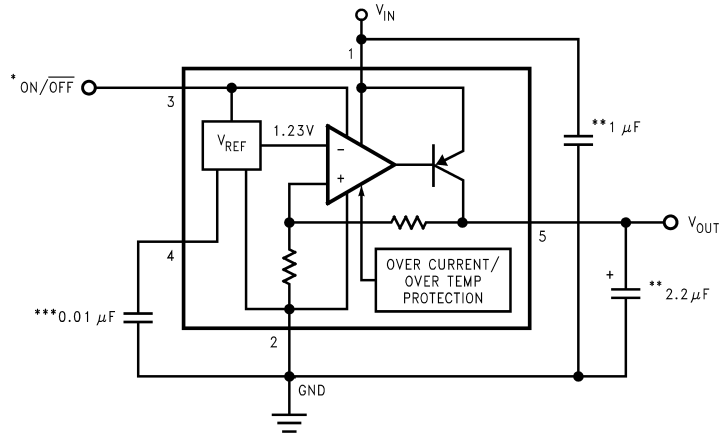
DS100140-1

VIP™ is a trademark of National Semiconductor Corporation.

LP2985 Micropower SOT, 150 mA Low-Noise Ultra Low-Dropout Regulator



## Basic Application Circuit



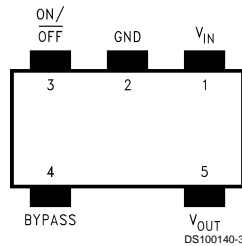
DS100140-2

\*ON/OFF input must be actively terminated. Tie to  $V_{IN}$  if this function is not to be used.

\*\*Minimum capacitance is shown to ensure stability (may be increased without limit). Ceramic capacitor required for output (see Application Hints).

\*\*\*Reduces output noise (may be omitted if application is not noise critical). Use ceramic or film type with very low leakage current (see Application Hints).

## Connection Diagram



See NS Package Number MA05B

## Ordering Information

TABLE 1. Package Marking and Ordering Information

Output Voltage (V)	Grade	Order Information	Package Marking	Supplied as:
2.8	A	LP2985AIM5X-2.8	L0KA	3k Units on Tape and Reel
2.8	A	LP2985AIM5-2.8	L0KA	250 Units on Tape and Reel
2.8	STD	LP2985IM5X-2.8	L0KB	3k Units on Tape and Reel
2.8	STD	LP2985IM5-2.8	L0KB	250 Units on Tape and Reel
3.0	A	LP2985AIM5X-3.0	L0OA	3k Units on Tape and Reel
3.0	A	LP2985AIM5-3.0	L0OA	250 Units on Tape and Reel
3.0	STD	LP2985IM5X-3.0	L0OB	3k Units on Tape and Reel
3.0	STD	LP2985IM5-3.0	L0OB	250 Units on Tape and Reel
3.1	A	LP2985AIM5X-3.1	L0PA	3k Units on Tape and Reel
3.1	A	LP2985AIM5-3.1	L0PA	250 Units on Tape and Reel
3.1	STD	LP2985IM5X-3.1	L0PB	3k Units on Tape and Reel
3.1	STD	LP2985IM5-3.1	L0PB	250 Units on Tape and Reel
3.2	A	LP2985AIM5X-3.2	L0QA	3k Units on Tape and Reel
3.2	A	LP2985AIM5-3.2	L0QA	250 Units on Tape and Reel

## Ordering Information (Continued)

TABLE 1. Package Marking and Ordering Information (Continued)

Output Voltage (V)	Grade	Order Information	Package Marking	Supplied as:
3.2	STD	LP2985IM5X-3.2	LOQB	3k Units on Tape and Reel
3.2	STD	LP2985IM5-3.2	LOQB	250 Units on Tape and Reel
3.3	A	LP2985AIM5X-3.3	LORA	3k Units on Tape and Reel
3.3	A	LP2985AIM5-3.3	LORA	250 Units on Tape and Reel
3.3	STD	LP2985IM5X-3.3	LORB	3k Units on Tape and Reel
3.3	STD	LP2985IM5-3.3	LORB	250 Units on Tape and Reel
3.5	A	LP2985AIM5X-3.5	LAIA	3k Units on Tape and Reel
3.5	A	LP2985AIM5-3.5	LAIA	250 Units on Tape and Reel
3.5	STD	LP2985IM5X-3.5	LAIB	3k Units on Tape and Reel
3.5	STD	LP2985IM5-3.5	LAIB	250 Units on Tape and Reel
3.6	A	LP2985AIM5X-3.6	LOSA	3k Units on Tape and Reel
3.6	A	LP2985AIM5-3.6	LOSA	250 Units on Tape and Reel
3.6	STD	LP2985IM5X-3.6	LOSB	3k Units on Tape and Reel
3.6	STD	LP2985IM5-3.6	LOSB	250 Units on Tape and Reel
4.0	A	LP2985AIM5X-4.0	LOTA	3k Units on Tape and Reel
4.0	A	LP2985AIM5-4.0	LOTA	250 Units on Tape and Reel
4.0	STD	LP2985IM5X-4.0	L0TB	3k Units on Tape and Reel
4.0	STD	LP2985IM5-4.0	L0TB	250 Units on Tape and Reel
5.0	A	LP2985AIM5X-5.0	LOUA	3k Units on Tape and Reel
5.0	A	LP2985AIM5-5.0	LOUA	250 Units on Tape and Reel
5.0	STD	LP2985IM5X-5.0	L0UB	3k Units on Tape and Reel
5.0	STD	LP2985IM5-5.0	L0UB	250 Units on Tape and Reel

## Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Storage Temperature Range	-65°C to +150°C
Operating Junction Temperature Range	-40°C to +125°C
Lead Temp. (Soldering, 5 sec.)	260°C
ESD Rating (Note 2)	2 kV
Power Dissipation (Note 3)	Internally Limited

Input Supply Voltage (Survival)	-0.3V to +16V
Input Supply Voltage (Operating)	2.5V to +16V
Shutdown Input Voltage (Survival)	-0.3V to +16V
Output Voltage (Survival, (Note 4))	-0.3V to +9V
I <sub>OUT</sub> (Survival)	Short Circuit Protected
Input-Output Voltage (Survival, (Note 5))	-0.3V to +16V

## Electrical Characteristics

Limits in standard typeface are for T<sub>J</sub> = 25°C, and limits in **boldface type** apply over the full operating temperature range. Unless otherwise specified: V<sub>IN</sub> = V<sub>O</sub>(NOM) + 1V, I<sub>L</sub> = 1 mA, C<sub>IN</sub> = 1 μF, C<sub>OUT</sub> = 4.7 μF, V<sub>ON/OFF</sub> = 2V.

Symbol	Parameter	Conditions	Typ	LP2985AI-X.X (Note 6)		LP2985I-X.X (Note 6)		Units
				Min	Max	Min	Max	
ΔV <sub>O</sub>	Output Voltage Tolerance	I <sub>L</sub> = 1 mA		-1.0	1.0	-1.5	1.5	%V <sub>NOM</sub>
		1 mA ≤ I <sub>L</sub> ≤ 50 mA		-1.5	1.5	-2.5	2.5	
		1 mA ≤ I <sub>L</sub> ≤ 150 mA		-2.5	2.5	-3.0	3.0	
				<b>-3.5</b>	<b>3.5</b>	<b>-4.0</b>	<b>4.0</b>	
$\frac{\Delta V_O}{\Delta V_{IN}}$	Output Voltage Line Regulation	V <sub>O</sub> (NOM)+1V ≤ V <sub>IN</sub> ≤ 16V	0.007		0.014		0.014	%/V
				<b>0.032</b>		<b>0.032</b>		
V <sub>IN</sub> -V <sub>O</sub>	Dropout Voltage (Note 7)	I <sub>L</sub> = 0	1		3		3	mV
		I <sub>L</sub> = 1 mA	7		10		10	
		I <sub>L</sub> = 10 mA	40		60		60	
		I <sub>L</sub> = 50 mA	120		150		150	
		I <sub>L</sub> = 150 mA	280		350		350	
					<b>575</b>		<b>575</b>	
I <sub>GND</sub>	Ground Pin Current	I <sub>L</sub> = 0	65		95		95	μA
		I <sub>L</sub> = 1 mA	75		110		110	
		I <sub>L</sub> = 10 mA	120		220		220	
		I <sub>L</sub> = 50 mA	350		600		600	
		I <sub>L</sub> = 150 mA	850		1500		1500	
		V <sub>ON/OFF</sub> < 0.3V	0.01		0.8		0.8	
		V <sub>ON/OFF</sub> < 0.15V	<b>0.05</b>		<b>2</b>		<b>2</b>	
V <sub>ON/OFF</sub>	ON/OFF Input Voltage (Note 8)	High = O/P ON	1.4	<b>1.6</b>		<b>1.6</b>		V
		Low = O/P OFF	0.55		<b>0.15</b>		<b>0.15</b>	
I <sub>ON/OFF</sub>	ON/OFF Input Current	V <sub>ON/OFF</sub> = 0	0.01		<b>-2</b>		<b>-2</b>	μA
		V <sub>ON/OFF</sub> = 5V	5		<b>15</b>		<b>15</b>	
e <sub>n</sub>	Output Noise Voltage (RMS)	BW = 300 Hz to 50 kHz, C <sub>OUT</sub> = 10 μF C <sub>BYPASS</sub> = 10 nF	30					μV

## Electrical Characteristics (Continued)

Limits in standard typeface are for  $T_J = 25^\circ\text{C}$ , and limits in **boldface type** apply over the full operating temperature range. Unless otherwise specified:  $V_{IN} = V_O(\text{NOM}) + 1\text{V}$ ,  $I_L = 1\text{ mA}$ ,  $C_{IN} = 1\ \mu\text{F}$ ,  $C_{OUT} = 4.7\ \mu\text{F}$ ,  $V_{ON/OFF} = 2\text{V}$ .

Symbol	Parameter	Conditions	Typ	LP2985AI-X.X (Note 6)		LP2985I-X.X (Note 6)		Units
				Min	Max	Min	Max	
$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	Ripple Rejection	$f = 1\text{ kHz}$ , $C_{BYPASS} = 10\text{ nF}$ $C_{OUT} = 10\ \mu\text{F}$	45					dB
$I_O(\text{SC})$	Short Circuit Current	$R_L = 0$ (Steady State) (Note 9)	400					mA
$I_O(\text{PK})$	Peak Output Current	$V_{OUT} \geq V_O(\text{NOM}) - 5\%$	350					mA

**Note 1:** "Absolute Maximum Ratings" indicate limits beyond which damage to the component may occur. Electrical specifications do not apply when operating the device outside of its rated operating conditions.

**Note 2:** The ESD rating of pins 3 and 4 is 1 kV.

**Note 3:** The maximum allowable power dissipation is a function of the maximum junction temperature,  $T_J(\text{MAX})$ , the junction-to-ambient thermal resistance,  $\theta_{J-A}$ , and the ambient temperature,  $T_A$ . The maximum allowable power dissipation at any ambient temperature is calculated using:

$$P(\text{MAX}) = \frac{T_J(\text{MAX}) - T_A}{\theta_{J-A}}$$

Where the value of  $\theta_{J-A}$  for the SOT-23 package is  $220^\circ\text{C/W}$  in a typical PC board mounting. Exceeding the maximum allowable dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown.

**Note 4:** If used in a dual-supply system where the regulator load is returned to a negative supply, the LP2985 output must be diode-clamped to ground.

**Note 5:** The output PNP structure contains a diode between the  $V_{IN}$  to  $V_{OUT}$  terminals that is normally reverse-biased. Reversing the polarity from  $V_{IN}$  to  $V_{OUT}$  will turn on this diode.

**Note 6:** Limits are 100% production tested at  $25^\circ\text{C}$ . Limits over the operating temperature range are guaranteed through correlation using Statistical Quality Control (SQC) methods. The limits are used to calculate National's Average Outgoing Quality Level (AOQL).

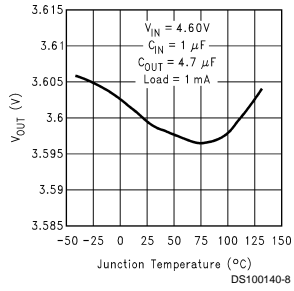
**Note 7:** Dropout voltage is defined as the input to output differential at which the output voltage drops 100 mV below the value measured with a 1V differential.

**Note 8:** The ON/OFF input must be properly driven to prevent possible misoperation. For details, refer to Application Hints.

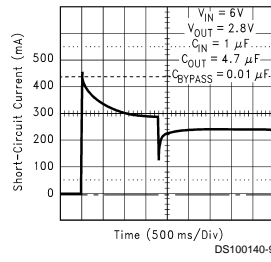
**Note 9:** The LP2985 has foldback current limiting which allows a high peak current when  $V_{OUT} > 0.5\text{V}$ , and then reduces the maximum output current as  $V_{OUT}$  is forced to ground (see Typical Performance Characteristics curves).

**Typical Performance Characteristics** Unless otherwise specified:  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 4.7\mu F$ ,  $V_{IN} = V_{OUT(NOM)} + 1$ ,  $T_A = 25^\circ C$ , ON/OFF pin is tied to  $V_{IN}$ .

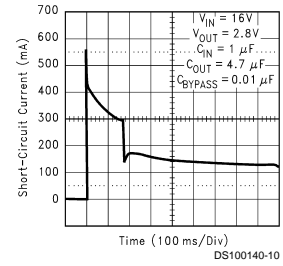
**$V_{OUT}$  vs Temperature**



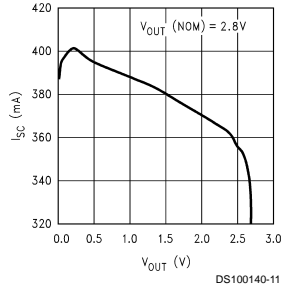
**Short-Circuit Current**



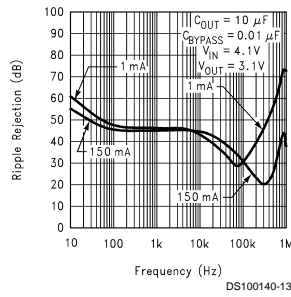
**Short-Circuit Current**



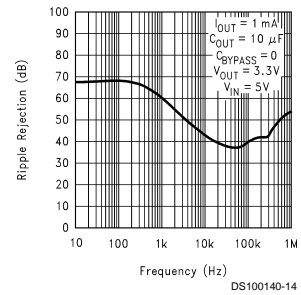
**Short Circuit Current vs Output Voltage**



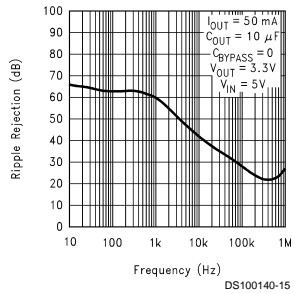
**Ripple Rejection**



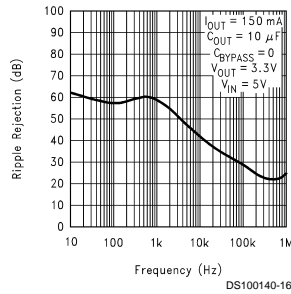
**Ripple Rejection**



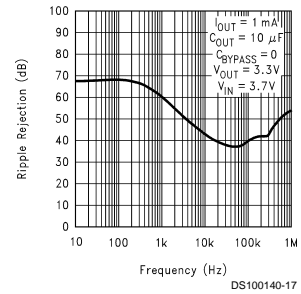
**Ripple Rejection**



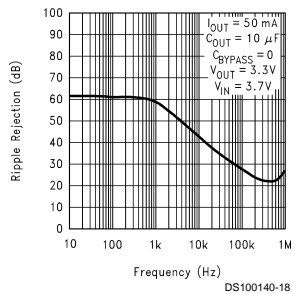
**Ripple Rejection**



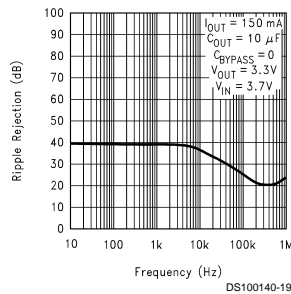
**Ripple Rejection**



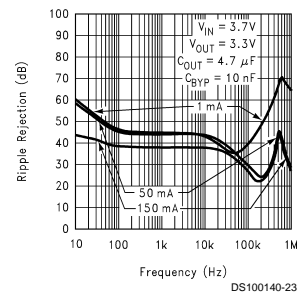
**Ripple Rejection**



**Ripple Rejection**

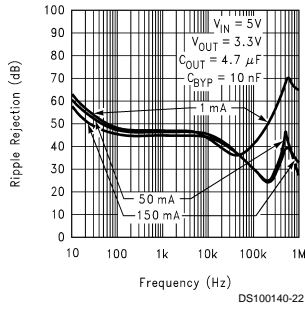


**Ripple Rejection**

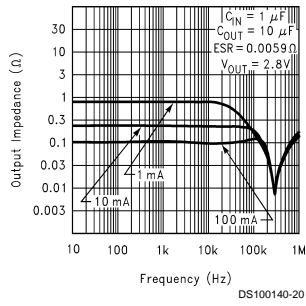


**Typical Performance Characteristics** Unless otherwise specified:  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 4.7\mu F$ ,  $V_{IN} = V_{OUT(NOM)} + 1$ ,  $T_A = 25^\circ C$ , ON/OFF pin is tied to  $V_{IN}$ . (Continued)

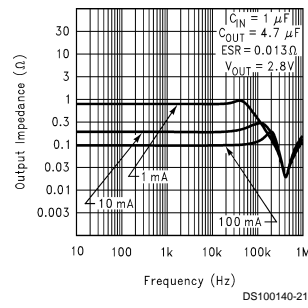
**Ripple Rejection**



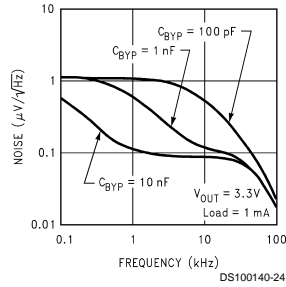
**Output Impedance vs Frequency**



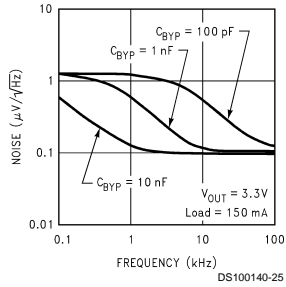
**Output Impedance vs Frequency**



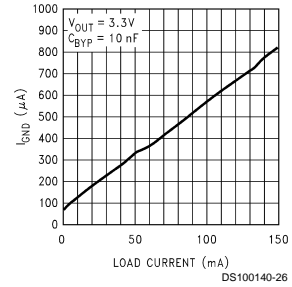
**Output Noise Density**



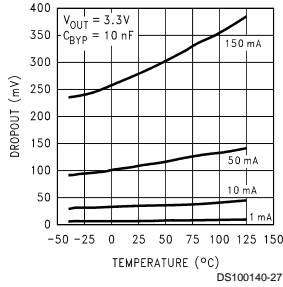
**Output Noise Density**



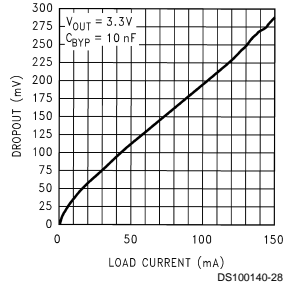
**Ground Pin vs Load Current**



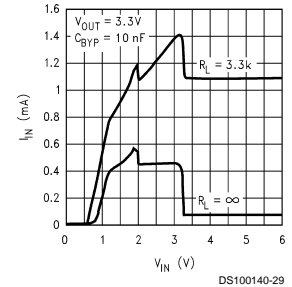
**Dropout Voltage vs Temperature**



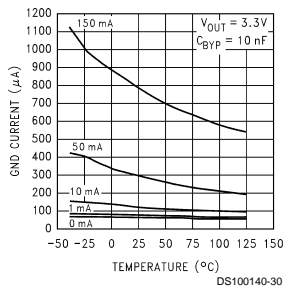
**Dropout Voltage vs Load Current**



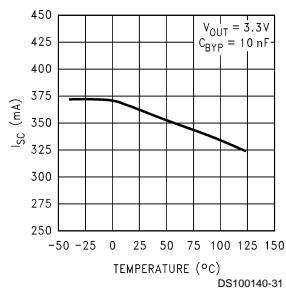
**Input Current vs Pin**



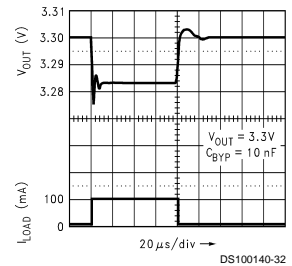
**GND Pin Current vs Temperature**



**Instantaneous Short Circuit Current**

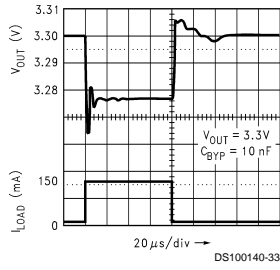


**Load Transient Response**

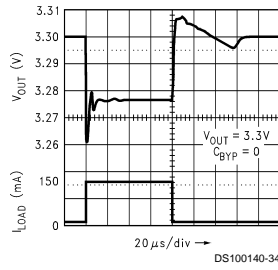


**Typical Performance Characteristics** Unless otherwise specified:  $C_{IN} = 1\mu\text{F}$ ,  $C_{OUT} = 4.7\mu\text{F}$ ,  $V_{IN} = V_{OUT(NOM)} + 1$ ,  $T_A = 25^\circ\text{C}$ , ON/OFF pin is tied to  $V_{IN}$ . (Continued)

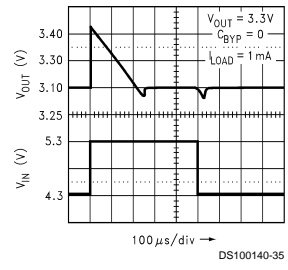
**Load Transient Response**



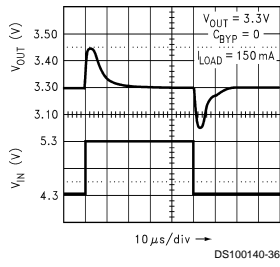
**Load Transient Response**



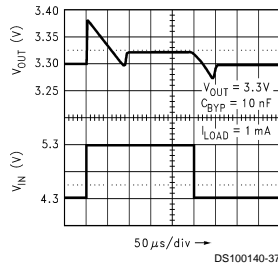
**Line Transient Response**



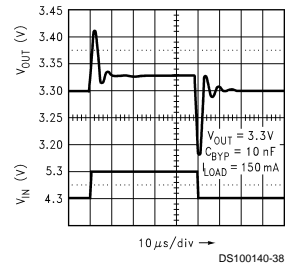
**Line Transient Response**



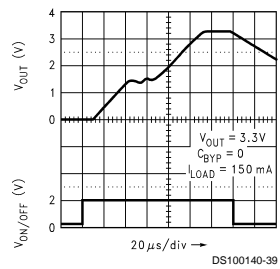
**Line Transient Response**



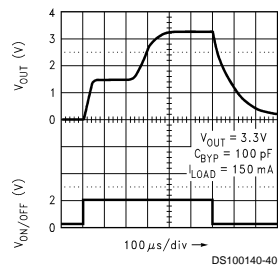
**Line Transient Response**



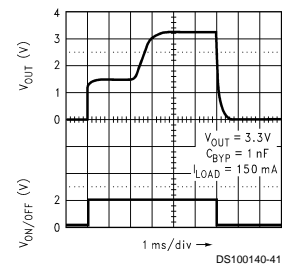
**Turn-On Time**



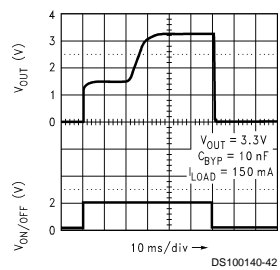
**Turn-On Time**



**Turn-On Time**



**Turn-On Time**





## Application Hints

### EXTERNAL CAPACITORS

Like any low-dropout regulator, the LP2985 requires external capacitors for regulator stability. These capacitors must be correctly selected for good performance.

#### Input Capacitor

An input capacitor whose capacitance is  $\geq 1 \mu\text{F}$  is required between the LP2985 input and ground (the amount of capacitance may be increased without limit).

This capacitor must be located a distance of not more than 1 cm from the input pin and returned to a clean analog ground. Any good quality ceramic, tantalum, or film capacitor may be used at the input.

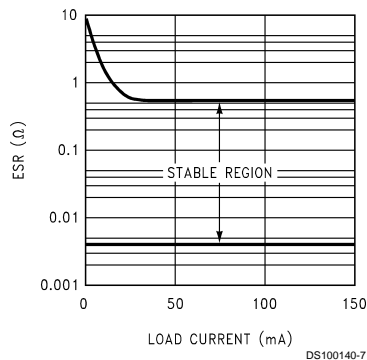
**Important:** Tantalum capacitors can suffer catastrophic failure due to surge current when connected to a low-impedance source of power (like a battery or very large capacitor). If a Tantalum capacitor is used at the input, it must be guaranteed by the manufacturer to have a surge current rating sufficient for the application.

There are no requirements for ESR on the input capacitor, but tolerance and temperature coefficient must be considered when selecting the capacitor to ensure the capacitance will be  $\geq 1 \mu\text{F}$  over the entire operating temperature range.

#### Output Capacitor:

The LP2985 is designed specifically to work with ceramic output capacitors, utilizing circuitry which allows the regulator to be stable across the entire range of output current with an output capacitor whose ESR is as low as  $5 \text{ m}\Omega$ . It may also be possible to use Tantalum or film capacitors at the output, but these are not as attractive for reasons of size and cost (see next section Capacitor Characteristics).

The output capacitor must meet the requirement for minimum amount of capacitance and also have an ESR (equivalent series resistance) value which is within the stable range. Curves are provided which show the stable ESR range as a function of load current (see ESR graph below).



**Important:** The output capacitor must maintain its ESR within the stable region over the full operating temperature range of the application to assure stability.

The LP2985 requires a minimum of  $2.2 \mu\text{F}$  on the output (output capacitor size can be increased without limit).

It is important to remember that capacitor tolerance and variation with temperature must be taken into consideration when selecting an output capacitor so that the minimum required amount of output capacitance is provided over the full

operating temperature range. It should be noted that ceramic capacitors can exhibit large changes in capacitance with temperature (see next section, *Capacitor Characteristics*).

The output capacitor must be located not more than 1 cm from the output pin and returned to a clean analog ground.

#### Noise Bypass Capacitor:

Connecting a  $10 \text{ nF}$  capacitor to the Bypass pin significantly reduces noise on the regulator output. It should be noted that the capacitor is connected directly to a high-impedance circuit in the bandgap reference.

Because this circuit has only a few microamperes flowing in it, any significant loading on this node will cause a change in the regulated output voltage. For this reason, DC leakage current through the noise bypass capacitor must never exceed  $100 \text{ nA}$ , and should be kept as low as possible for best output voltage accuracy.

The types of capacitors best suited for the noise bypass capacitor are ceramic and film. High-quality ceramic capacitors with either NPO or COG dielectric typically have very low leakage.  $10 \text{ nF}$  polypropylene and polycarbonate film capacitors are available in small surface-mount packages and typically have extremely low leakage current.

### CAPACITOR CHARACTERISTICS

The LP2985 was designed to work with ceramic capacitors on the output to take advantage of the benefits they offer: for capacitance values in the  $2.2 \mu\text{F}$  to  $4.7 \mu\text{F}$  range, ceramics are the least expensive and also have the lowest ESR values (which makes them best for eliminating high-frequency noise). The ESR of a typical  $2.2 \mu\text{F}$  ceramic capacitor is in the range of  $10 \text{ m}\Omega$  to  $20 \text{ m}\Omega$ , which easily meets the ESR limits required for stability by the LP2985.

One disadvantage of ceramic capacitors is that their capacitance can vary with temperature. Most large value ceramic capacitors ( $\geq 2.2 \mu\text{F}$ ) are manufactured with the Z5U or Y5V temperature characteristic, which results in the capacitance dropping by more than 50% as the temperature goes from  $25^\circ\text{C}$  to  $85^\circ\text{C}$ .

This could cause problems if a  $2.2 \mu\text{F}$  capacitor were used on the output since it will drop down to approximately  $1 \mu\text{F}$  at high ambient temperatures (which could cause the LM2985 to oscillate). If Z5U or Y5V capacitors are used on the output, a minimum capacitance value of  $4.7 \mu\text{F}$  must be observed.

A better choice for temperature coefficient in ceramic capacitors is X7R, which holds the capacitance within  $\pm 15\%$ . Unfortunately, the larger values of capacitance are not offered by all manufacturers in the X7R dielectric.

#### Tantalum:

Tantalum capacitors are less desirable than ceramics for use as output capacitors because they are more expensive when comparing equivalent capacitance and voltage ratings in the  $1 \mu\text{F}$  to  $4.7 \mu\text{F}$  range.

Another important consideration is that Tantalum capacitors have higher ESR values than equivalent size ceramics. This means that while it may be possible to find a Tantalum capacitor with an ESR value within the stable range, it would have to be larger in capacitance (which means bigger and more costly) than a ceramic capacitor with the same ESR value.

It should also be noted that the ESR of a typical Tantalum will increase about 2:1 as the temperature goes from  $25^\circ\text{C}$  down to  $-40^\circ\text{C}$ , so some guard band must be allowed.

## Application Hints (Continued)

### ON/OFF INPUT OPERATION

The LP2985 is shut off by driving the ON/OFF input low, and turned on by pulling it high. If this feature is not to be used, the ON/OFF input should be tied to  $V_{IN}$  to keep the regulator output on at all times.

To assure proper operation, the signal source used to drive the ON/OFF input must be able to swing above and below the specified turn-on/turn-off voltage thresholds listed in the Electrical Characteristics section under  $V_{ON/OFF}$ . To prevent mis-operation, the turn-on (and turn-off) voltage signals applied to the ON/OFF input must have a slew rate which is  $\geq 40$  mV/ $\mu$ s.

**Caution:** the regulator output voltage can not be guaranteed if a slow-moving AC (or DC) signal is applied that is in the range between the specified turn-on and turn-off voltages listed under the electrical specification  $V_{ON/OFF}$  (see Electrical Characteristics).

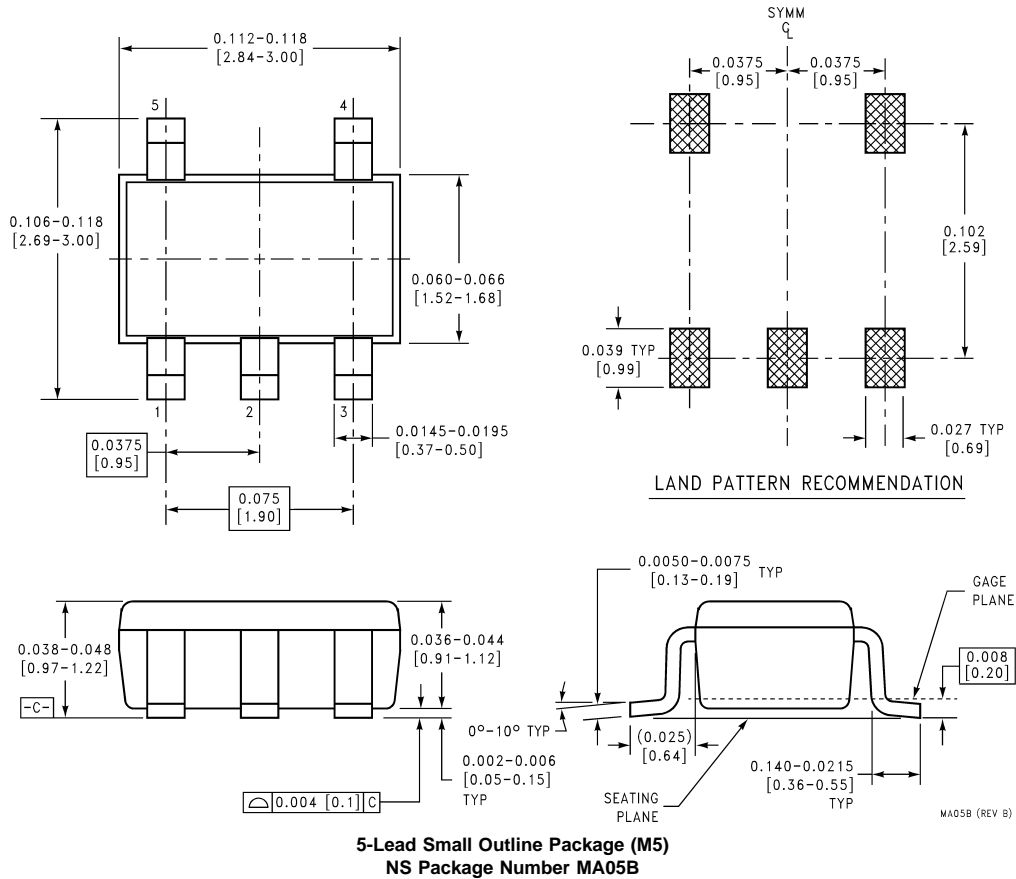
### REVERSE INPUT-OUTPUT VOLTAGE

The PNP power transistor used as the pass element in the LP2985 has an inherent diode connected between the regulator output and input. During normal operation (where the input voltage is higher than the output) this diode is reverse-biased.

However, if the output is pulled above the input, this diode will turn ON and current will flow into the regulator output. In such cases, a parasitic SCR can latch which will allow a high current to flow into  $V_{IN}$  (and out the ground pin), which can damage the part.

In any application where the output may be pulled above the input, an external Schottky diode must be connected from  $V_{IN}$  to  $V_{OUT}$  (cathode on  $V_{IN}$ , anode on  $V_{OUT}$ ), to limit the reverse voltage across the LP2985 to 0.3V (see Absolute Maximum Ratings).

**Physical Dimensions** inches (millimeters) unless otherwise noted



**LIFE SUPPORT POLICY**

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

**National Semiconductor Corporation**  
Americas  
Tel: 1-800-272-9959  
Fax: 1-800-737-7018  
Email: support@nsc.com

**National Semiconductor Europe**  
Fax: +49 (0) 1 80-530 85 86  
Email: europe.support@nsc.com  
Deutsch Tel: +49 (0) 1 80-530 85 85  
English Tel: +49 (0) 1 80-532 78 32  
Français Tel: +49 (0) 1 80-532 93 58  
Italiano Tel: +49 (0) 1 80-534 16 80

**National Semiconductor Asia Pacific Customer Response Group**  
Tel: 65-2544466  
Fax: 65-2504466  
Email: sea.support@nsc.com

**National Semiconductor Japan Ltd.**  
Tel: 81-3-5639-7560  
Fax: 81-3-5639-7507

www.national.com