

# **TDA7233S**

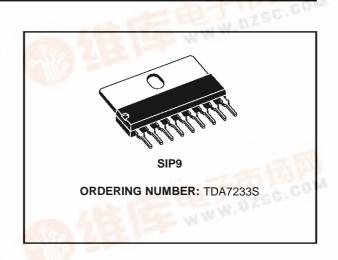
# **1W AUDIO AMPLIFIER WITH MUTE**

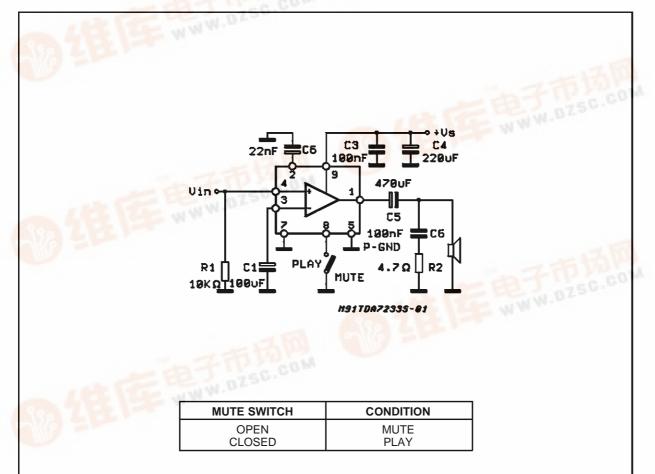
- OPERATING VOLTAGE 1.8 TO 15V
- EXTERNAL MUTE OR POWER DOWN FUNCTION
- IMPROVED SUPPLY VOLTAGE REJECTION
- LOW QUIESCENT CURRENT
- HIGH POWER CAPABILITY
- LOW CROSSOVER DISTORTION

#### DESCRIPTION

The TDA7233S is a monolithic integrated circuit in SIP 9, intended for use as class AB power amplifier with a wide range of supply voltage from 1.8V to 15V in portable radios, cassette recorders and players.

#### **TEST AND APPLICATION CIRCUIT**

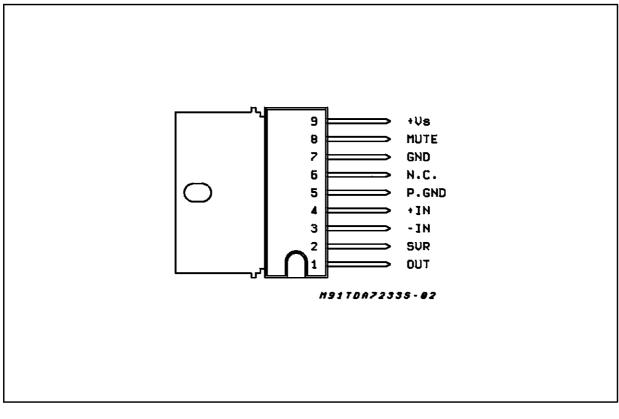






### **TDA7233S**

# PIN CONNECTION (Top view)



#### **ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter	Value	Unit
Vs	Supply Voltage	16	V
Ιo	Output Peak Current	1	А
P <sub>tot</sub>	Total Power Dissipation T <sub>amb</sub> = 50°C	1	W
T <sub>stg</sub> , T <sub>j</sub>	Storage and Junction Temperature	-40 to 150	°C

## THERMAL DATA

Symbol	Description	Value	Unit	
R <sub>th j-amb</sub>	Thermal Resistance Junction-ambient	Max	70 10	°C/W
Rth j-case	Thermal Resistance Junction-pins	Max	10	°C/W



Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Unit
Vs	Supply Voltage		1.8		15	V
Vo	Quiescent Output Voltage			27		V
		$V_{S} = 3V$ $V_{S} = 9V$		1.2 4.2		V V
l <sub>d</sub>	Quiescent Drain Current	PLAY		3.6	9	mA
		MUTE		0.4		mA
l <sub>b</sub>	Input Bias Current			100		nA
Po	Output Power		0.8 0.45	1.9 1.6 1 0.4 0.7 110 70		≥≥≥≥≥ E
d	Distortion	$ \begin{array}{ll} P_{O} = 0.5W & R_{L} = 8\Omega \\ f = 1KHz & V_{S} = 9V \end{array} $		0.3		%
Gv	Closed Loop Voltage Gain	f = 1KHz		39		dB
R <sub>IN</sub>	Input Resistance	f = 1KHz	100			KΩ
e <sub>N</sub>	Total Input Noise ( $R_S = 10K\Omega$ )	B = Curve A		2		μV
		B = 22Hz to 22KHz		3		μV
SVR	Supply Voltage Rejection	$R_g = 10K\Omega$ f = 100Hz	40	45		dB
	MUTE Attenuation	$V_0 = 1V$ , f = 100Hz to 10KHz		70		dB
	MUTE Threshold			0.6		V
I <sub>M</sub>	MUTE Current	V <sub>S</sub> = 15V		0.4	2	mA

# **ELECTRICAL CHARACTERISTICS** (V<sub>S</sub> = 6V, $T_{amb}$ = 25°C, unless otherwise specified)

Figure 1: Output Power vs. Supply Voltage

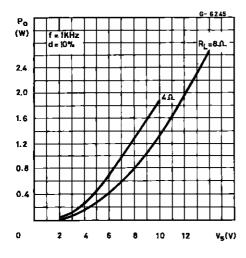
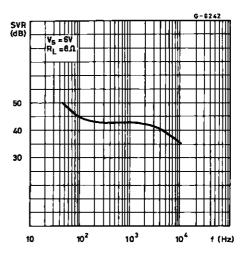


Figure 2: Supply Voltage Rejection vs. Frequency





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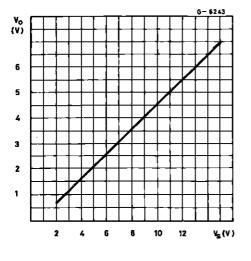
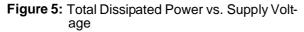


Figure 3: DC Output Voltage vs. Supply Voltage



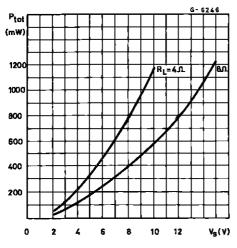
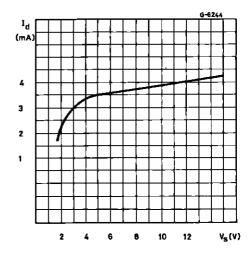
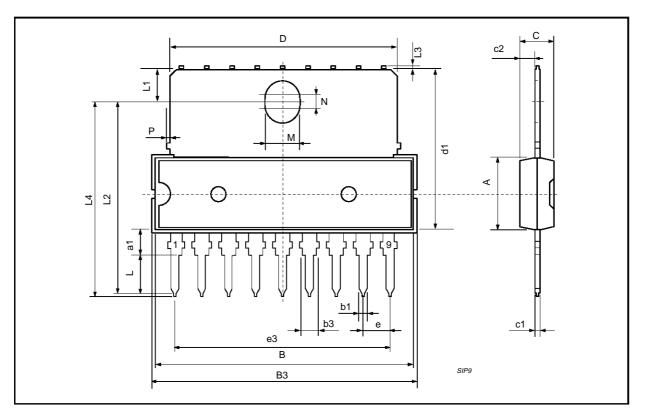


Figure 4: Quiescent Current vs. Supply Voltage



#### SIP9 PACKAGE MECHANICAL DATA

DIM.	mm				inch		
DINI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
А			7.1			0.280	
a1	2.7		3	0.106		0.118	
В			23			0.90	
B3			24.8			0.976	
b1		0.5			0.020		
b3	0.85		1.6	0.033		0.063	
С		3.3			0.130		
c1		0.43			0.017		
c2		1.32			0.052		
D			21.2			0.835	
d1		14.5			0.571		
е		2.54			0.100		
e3		20.32			0.800		
L	3.1			0.122			
L1		3			0.118		
L2		17.6			0.693		
L3			0.25			0.010	
L4	17.4		17.85	0.685		0,702	
М		3.2			0.126		
Ν		1			0.039		
Р			0.15			0.006	





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