

<b>SANYO</b>	No.1463C	<b>Monolithic Linear IC</b>
		<b>LA4265</b>
<b>3.5W MONAURAL POWER AMP</b>		

**Features**

- Minimum number of external parts required (No input capacitor, bootstrap capacitor required)
- High output: 3.5 W typ ( $V_{CC} = 16\text{ V}$ ,  $R_L = 8\ \Omega$ , THD = 10 %)
- Soft clip, causing little harmonic disturbance to radios
- Small pop noise at the time of power switch ON/OFF
- Built-in protector against abnormal modes (Thermal shutdown, overvoltage)

**Maximum Ratings/ $T_a = 25^\circ\text{C}$**

Maximum supply voltage	$V_{CC}$ max	25	V	unit
Maximum output current	$I_O$ peak	2	A	
Allowable power dissipation	$P_D$ max	7.5*	W	
Operating temperature	$T_{opr}$	-20 ~ +75	$^\circ\text{C}$	
Storage temperature	$T_{stg}$	-40 ~ +150	$^\circ\text{C}$	

\* With 100 x 120 x 1.5 mm<sup>3</sup> A1 heat sink

**Operating Conditions/ $T_a = 25^\circ\text{C}$**

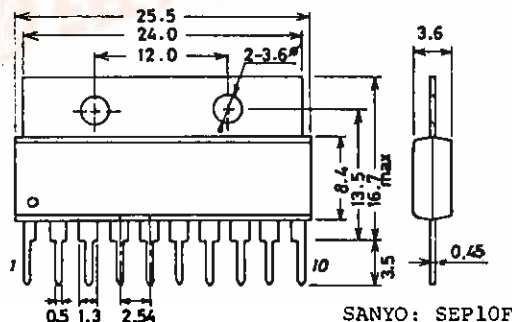
Recommended supply voltage	$V_{CC}$	16	V	unit
Recommended load resistance	$R_L$	8	$\Omega$	
Operating supply voltage range	$V_{CC}$	9 ~ 24	V	

**Operating Characteristics/ $T_a = 25^\circ\text{C}$ ,  $V_{CC} = 16\text{ V}$ ,  $R_L = 8\ \Omega$ ,  $f = 1\ \text{kHz}$ ,  $R_g = 600\ \Omega$ , See specified test circuit (based on sample application circuit).**

			min	typ	max	unit
Quiescent current	$I_{CCO}$			35	50	mA
Voltage gain	VG		48	50	52	dB
Output power	$P_O$	THD = 10 %	3.0	3.5		W
Total harmonic distortion	THD	$P_O = 0.5\ \text{W}$		0.3	1.0	%
Output noise voltage	VNO	$R_g = 10\ \text{k}\Omega$ , BPF = 20 Hz to 20 kHz		0.65	1.5	mV
Ripple rejection	SVRR	$R_g = 0$ , $f_R = 100\ \text{Hz}$ , $V_R = 0.5\ \text{V}$	40	50		dB

**Package Dimensions (unit: mm)**

3018A-S10FIC

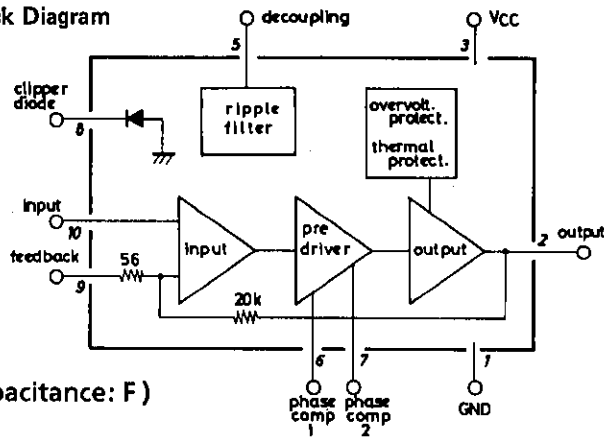


SANYO: SEP10F



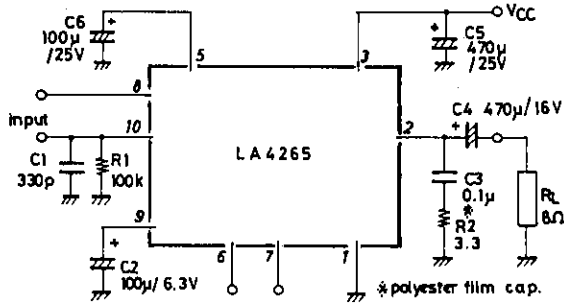
# LA4265

## Equivalent Circuit Block Diagram

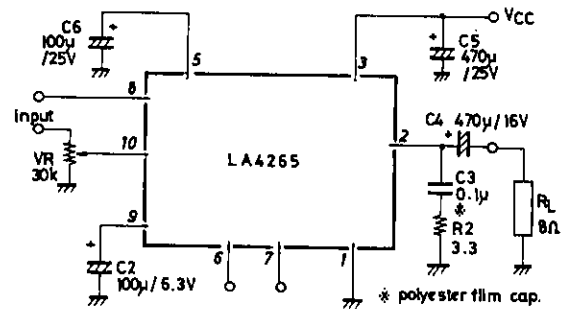


Unit (resistance:  $\Omega$ , capacitance: F)

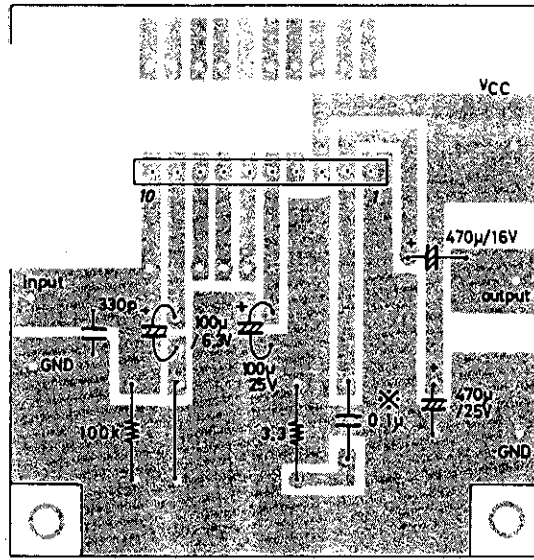
### Sample application circuit 1 (Recommended circuit)



### Sample application circuit 2 (Circuit with minimum number of external parts)

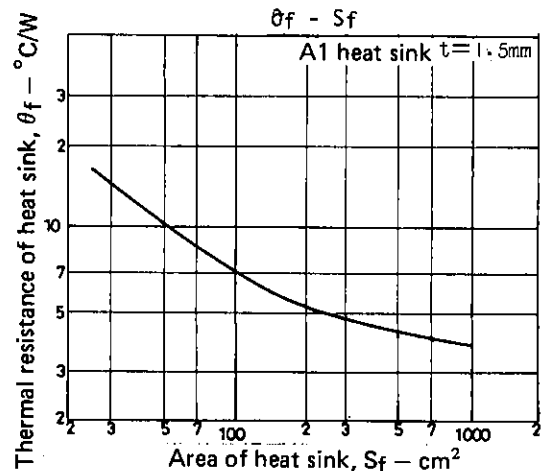
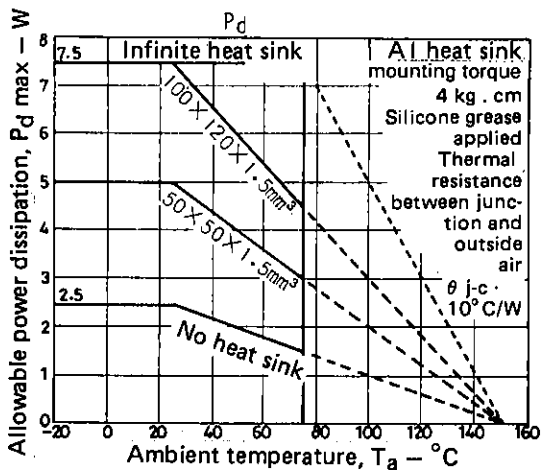


### Sample printed circuit pattern



Cu-foiled area  
55×57.5 mm<sup>2</sup>

\* Polyester film capacitor



**Description of External Parts**

- C<sub>1</sub> (330 pF)** : Input short capacitor  
Reduces the high frequency noise when the input impedance is increased. Not required when the input impedance is decreased.
- C<sub>2</sub> (100  $\mu$ F)** : Feedback capacitor  
Decreasing the capacitance value lowers the low frequency response. Increasing the capacitance value makes the starting time later.
- C<sub>3</sub> (0.1  $\mu$ F polyester film capacitor)** : Oscillation blocking capacitor  
Decreasing the capacitance value causes oscillation to occur easily. Use a polyester film capacitor that is good in high frequency response and temperature characteristic. The use of an electrolytic capacitor may cause oscillation to occur at low temperatures.
- C<sub>4</sub> (470  $\mu$ F)** : Output capacitor  
Decreasing the capacitance value causes insufficient power at low frequencies.
- C<sub>5</sub> (470  $\mu$ F)** : Power capacitor  
Decreasing the capacitance value causes ripple to occur easily. Locating at a distance from the IC or removing this capacitor may cause oscillation to occur.
- C<sub>6</sub> (100  $\mu$ F)** : Ripple filter capacitor  
Decreasing the capacitance value excessively or removing this capacitor causes ripple to occur. However, increasing the capacitance value does not always cause ripple to be reduced. Decreasing the capacitance value makes the starting time earlier.
- R<sub>1</sub> (100 k $\Omega$ )** : Input bias resistor  
Determines the bias (bias of zero potential) to be applied to the input pin and the input impedance. Not required if a variable resistor is also used as this resistor.
- R<sub>2</sub> (3.3  $\Omega$ )** : Resistor connected in series with oscillation blocking capacitor  
Prevents phase shift attributable to the oscillation blocking capacitor so that oscillation is hard to occur.

**Note for Changing Voltage Gain**

The voltage gain can be reduced by adding an external resistor ( $R_{NF}$ ) in series with the feedback capacitor. (See  $V_G$ - $R_{NF}$  characteristic curve.) However, it should be noted that various characteristics are also changed (THD- $V_G$ ,  $V_{NO}$ - $V_G$ ,  $V_{ro}$ - $V_G$ ). The voltage gain must not be reduced to be less than 30 dB. Since the frequency response is extended and oscillation is liable to occur when the voltage gain is reduced, high-cut must be made as required. (High-cut is made by connecting a capacitor of approximately 30 pF across pins (6) and (7).)

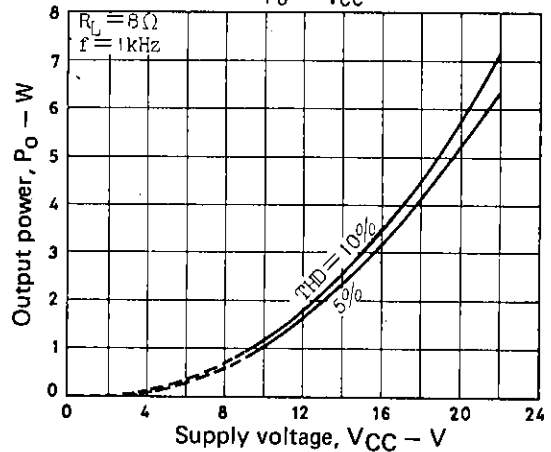
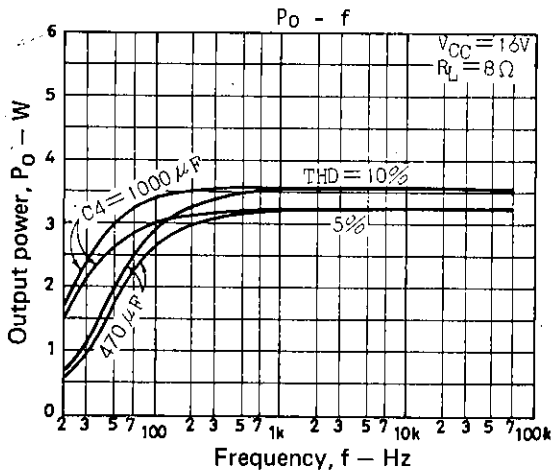
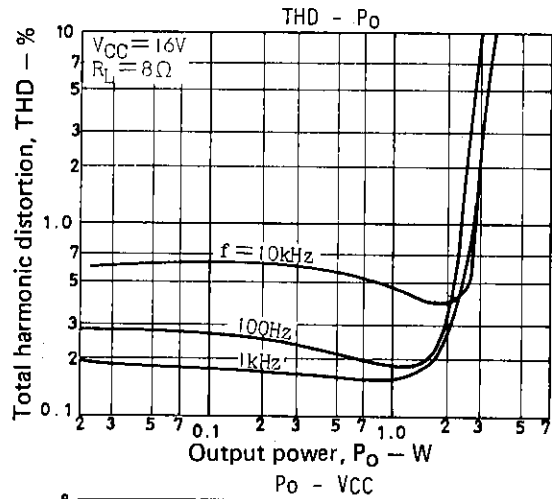
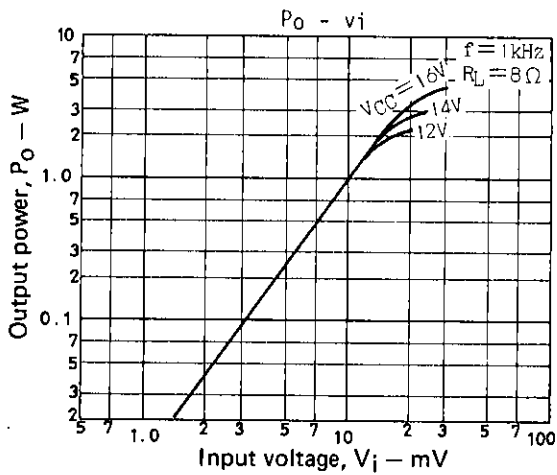
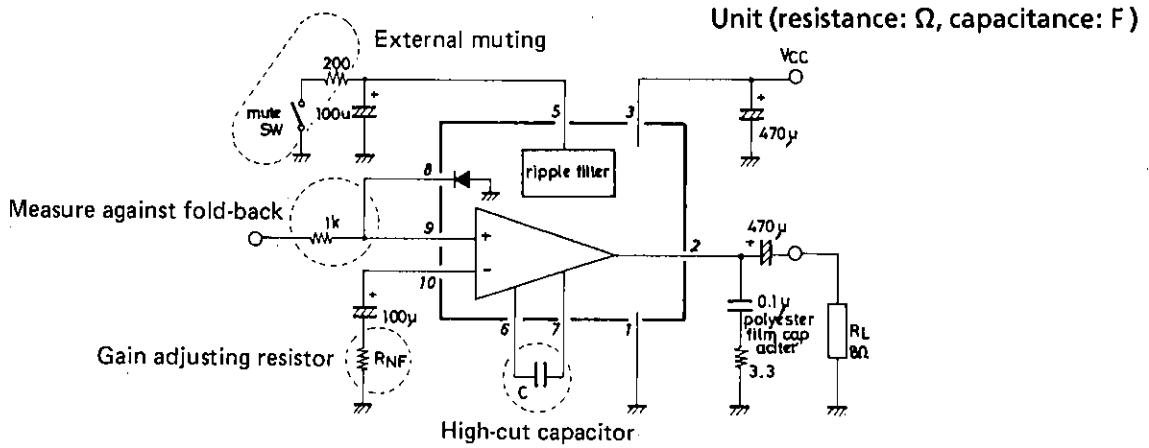
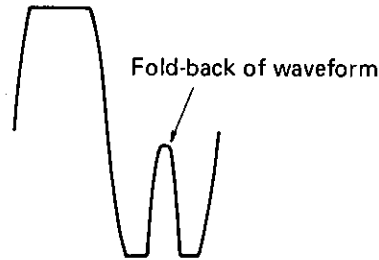
**External Muting**

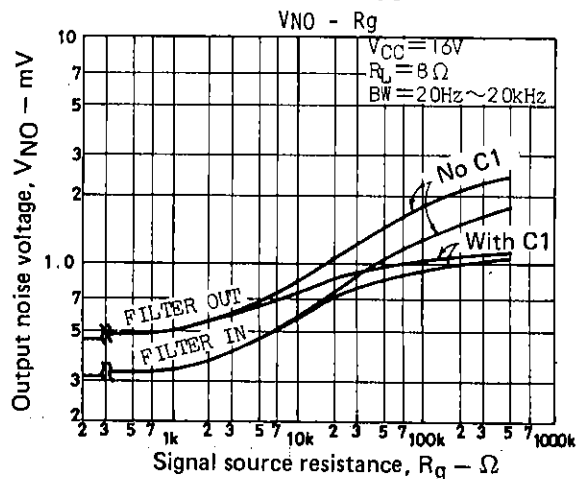
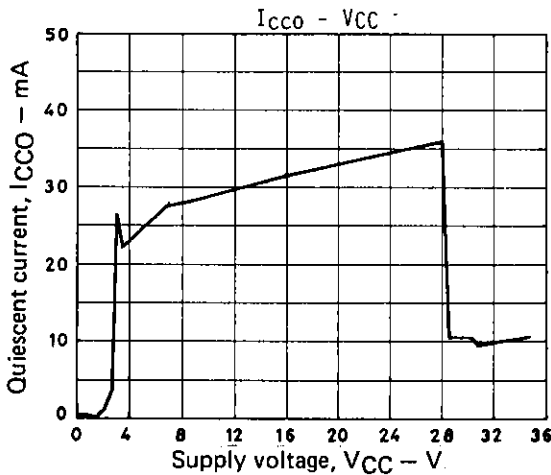
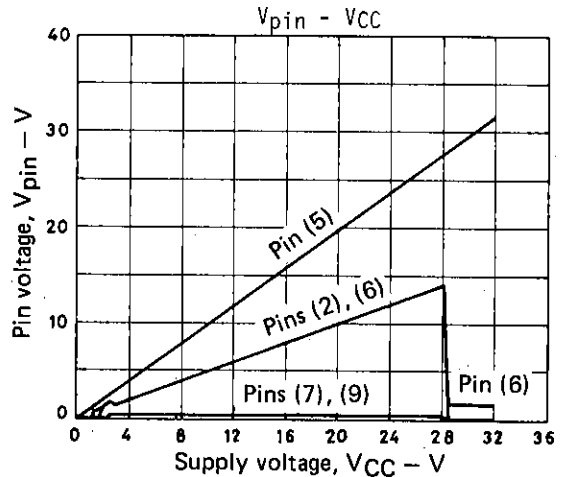
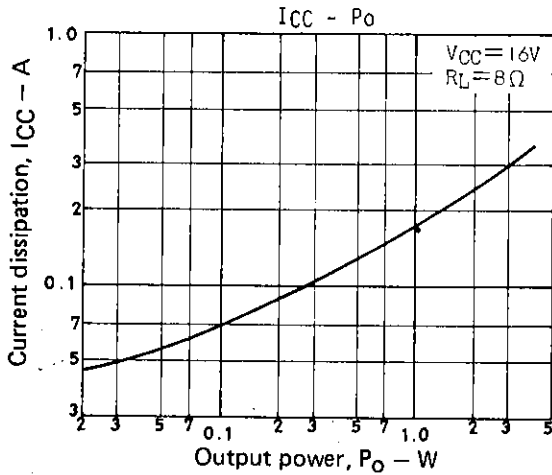
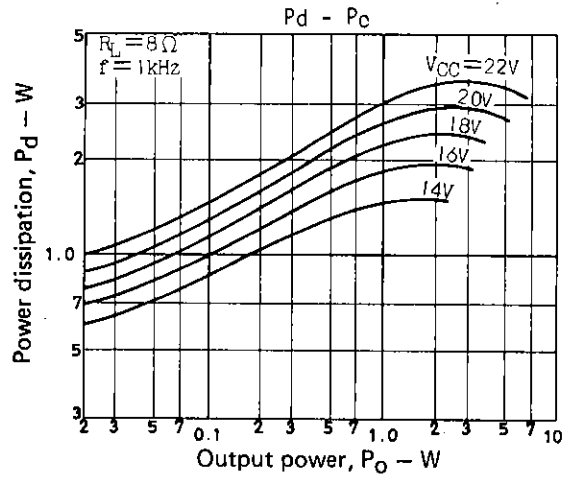
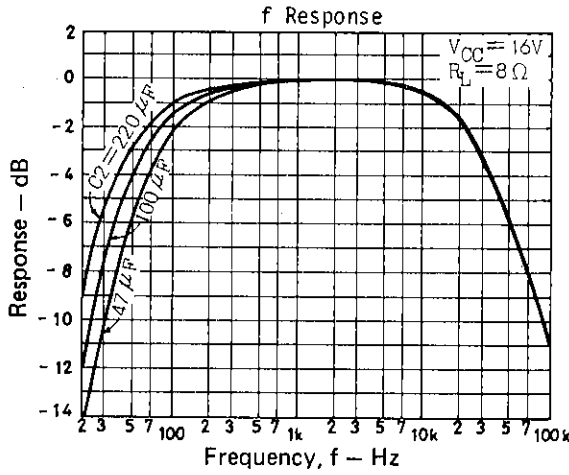
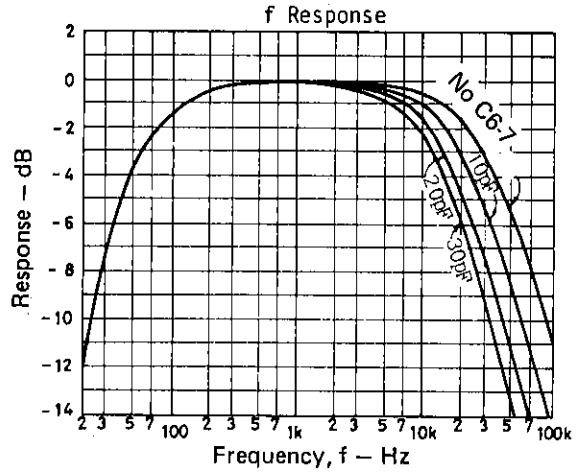
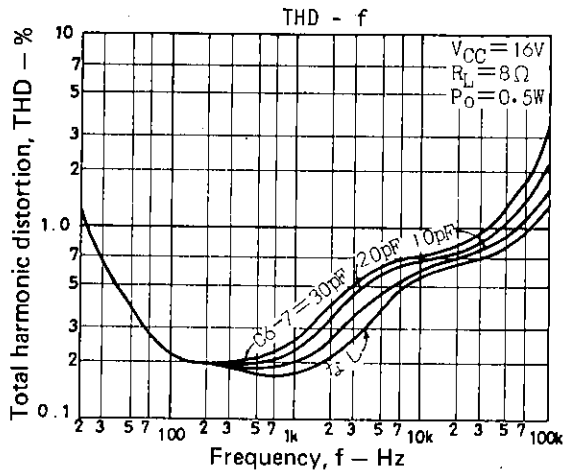
If external muting is required, make the circuit as shown on next page. In this case, the pop noise is similar to that which occurs at the time of power switch ON/OFF. If the value of the series resistor is decreased, more pop noise is heard at the time of attack; if increased, muting is hard to work.

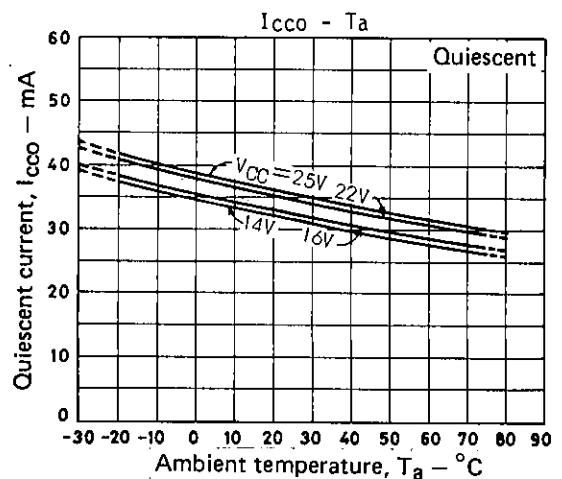
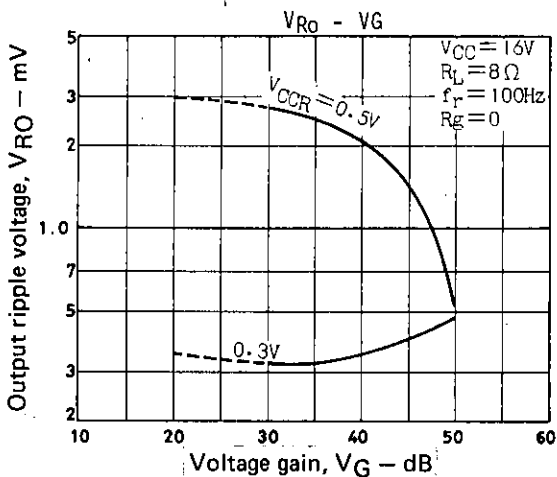
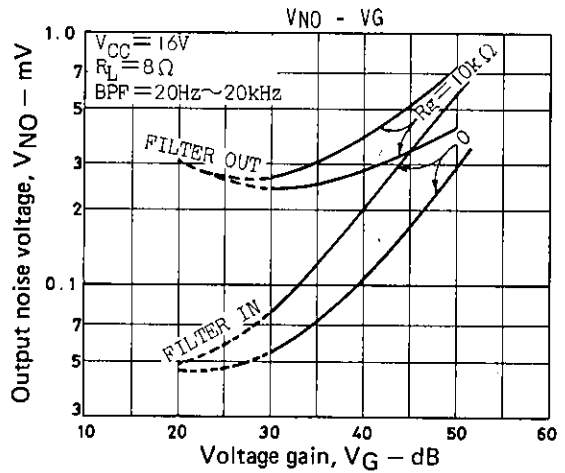
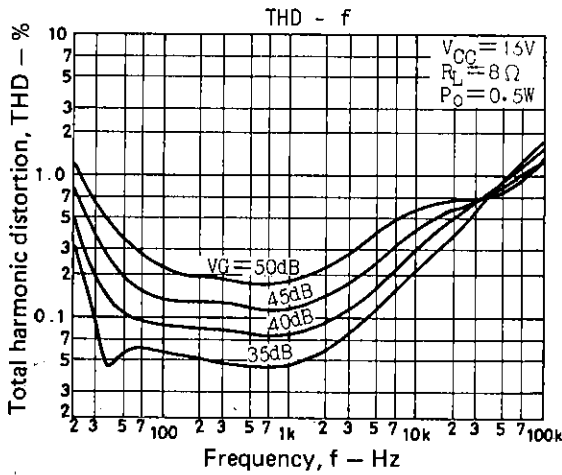
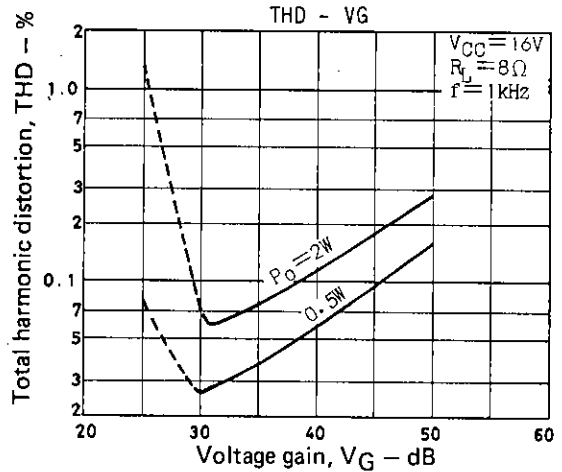
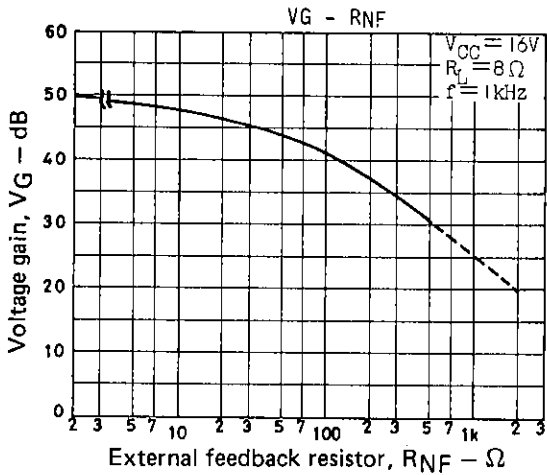
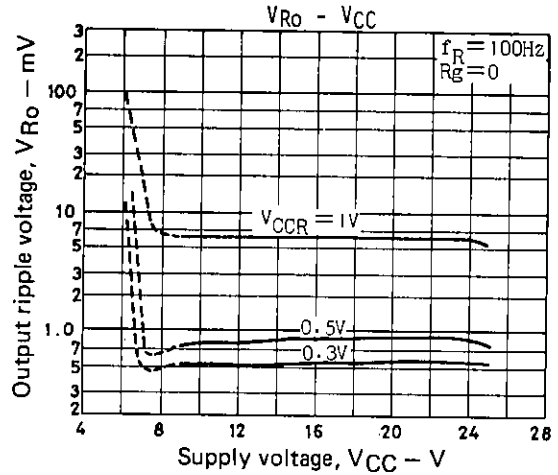
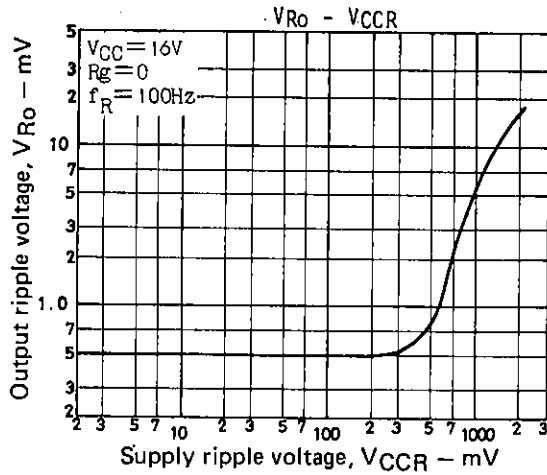
**Measure against Fold-back of Output Waveform**

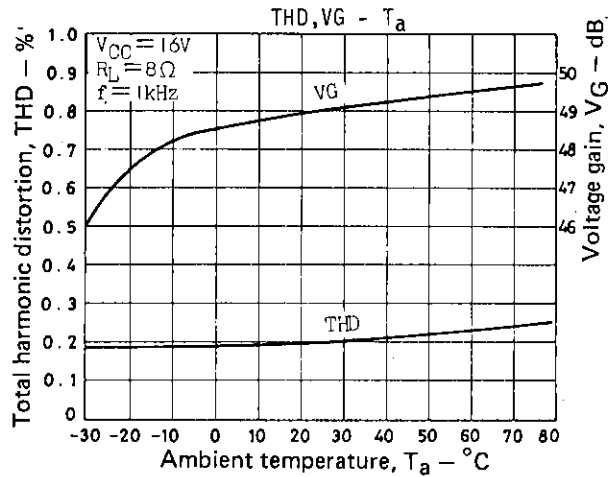
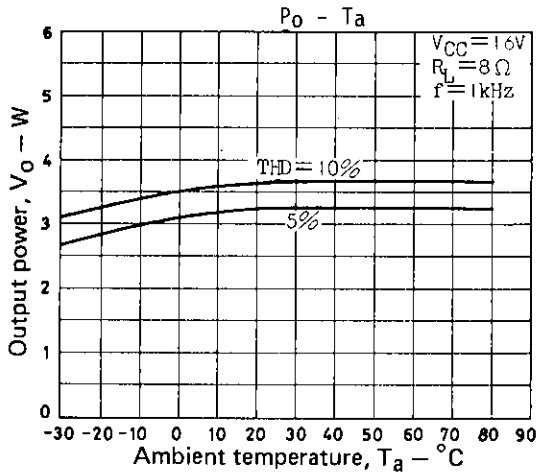
Since the input pin is zero-biased, the circuit may be saturated at an overinput, causing a part of the output waveform to be folded back (e.g. when the peak input voltage exceeds 600 mV). In such a case, the fold-back of the waveform can be prevented by using the built-in diode (also can be prevented by using an external diode). When the built-in diode is used, a resistor must be connected in series with the input pin to cause the diode to conduct no overcurrent (10 mA or less).

Fold-back of Output Waveform

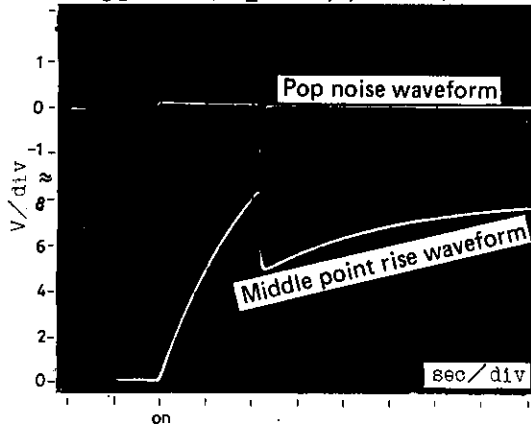




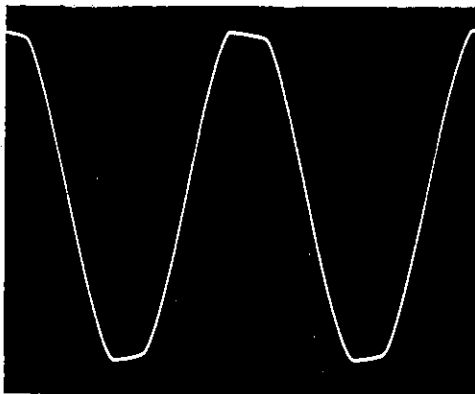




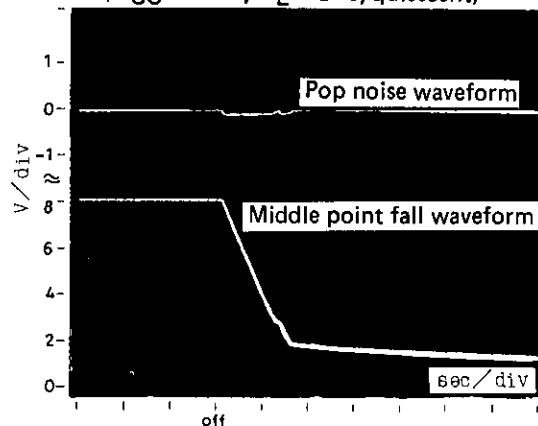
Pop noise waveform at the time of power switch ON ( $V_{CC} = 16V, R_L = 8\Omega$ , quiescent)



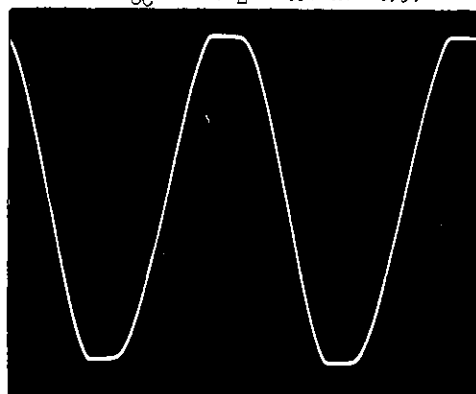
f = 1 kHz clip waveform  
 ( $V_{CC} = 16V, R_L = 8\Omega, THD = 5\%$ )



Pop noise waveform at the time of power switch OFF ( $V_{CC} = 16V, R_L = 8\Omega$ , quiescent)



f = 10 kHz clip waveform  
 ( $V_{CC} = 16V, R_L = 8\Omega, THD = 5\%$ )



**Proper Cares in Using IC**

- **Maximum ratings**  
 If the IC is used in the vicinity of the maximum ratings, even a slight variation in conditions may cause the maximum ratings to be exceeded, thereby leading to breakdown. Allow an ample margin of variation for supply voltage, etc. and use the IC in the range where the maximum ratings are not exceeded.
- **Pin-to-pin short**  
 If power is applied when the space between pins is shorted, breakdown or deterioration may occur. When mounting the IC on the board or applying power, make sure that the space between pins is not shorted with solder, etc.

- **When used in radio applications**  
When using in radios, allow a sufficient space between IC and bar antenna.
- **Printed circuit pattern**  
When designing the printed circuit pattern, make the power supply, output, and ground lines thick and short and arrange the pattern and parts so that no feedback loop is formed between input and output. Place power capacitor C5, oscillation blocking capacitor C3 as close to IC pins as possible to prevent oscillation from occurring. Refer to the sample printed circuit pattern.
- **Some plug jacks to be used for connecting to the external speaker are such that both poles are short-circuited once when connecting. In this case, the load is short-circuited, which may break down the IC.**

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