M1MA141KT1, M1MA142KT1

Preferred Device

Single Silicon Switching Diode

This Silicon Epitaxial Planar Diode is designed for use in ultra high speed switching applications. This device is housed in the SC-70 package which is designed for low power surface mount applications.

Features

- Fast t_{rr} , < 3.0 ns
- Low C_D, < 2.0 pF
- Pb–Free Packages are Available

MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

Rating	Symbol	Value	Unit
Reverse Voltage M1MA141KT M1MA142KT		40 80	Vdc
Peak Reverse Voltage M1MA141KT M1MA142KT		40 80	Vdc
Forward Current	١ _F	100	mAdc
Peak Forward Current	I _{FM}	225	mAdc
Peak Forward Surge Current	I _{FSM} (Note 1)	500	mAdc

THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Power Dissipation	PD	150	mW
Junction Temperature	TJ	150	°C
Storage Temperature	T _{stg}	-55 ~ +150	°C

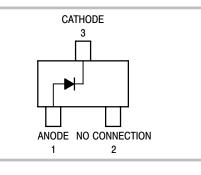
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. t = 1 sec



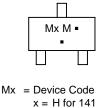
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM





M = Date Code*

= Pb–Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING	INFORMATION
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Device	Package	Shipping [†]
M1MA141KT1	SC-70	3000/Tape & Reel
M1MA141KT1G	SC-70 (Pb-Free)	3000/Tape & Reel
M1MA142KT1	SC-70	3000/Tape & Reel
M1MA142KT1G	SC-70 (Pb-Free)	3000/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

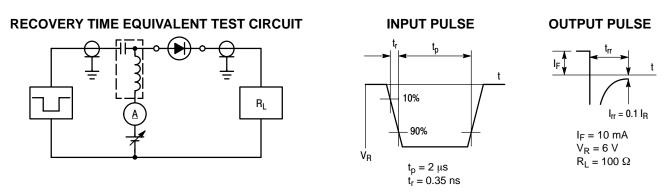
M1MA141KT1, M1MA142KT1

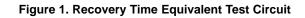
ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$)

Characteristic		Condition	Symbol	Min	Max	Unit
Reverse Voltage Leakage Current	M1MA141KT1 M1MA142KT1	V _R = 35 V V _R = 75 V	I _R	-	0.1	μAdc
Forward Voltage		I _F = 100 mA	V _F	-	1.2	Vdc
Reverse Breakdown Voltage	M1MA141KT1 M1MA142KT1	I _R = 100 μA	V _R	40 80	_	Vdc
Diode Capacitance		V _R = 0, f = 1.0 MHz	CD	-	2.0	pF
Reverse Recovery Time (Figure 1)		$I_{F} = 10 \text{ mA}, V_{R} = 6.0 \text{ V}, R_{L} = 100 \Omega, I_{rr} = 0.1 I_{R}$	t _{rr} (Note 2)	-	3.0	ns

2. t_{rr} Test Circuit

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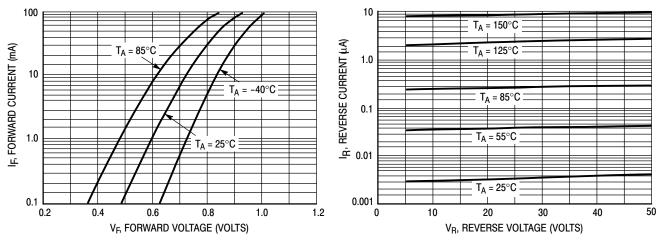


Figure 2. Forward Voltage

Figure 3. Reverse Current

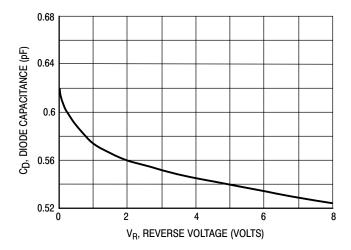
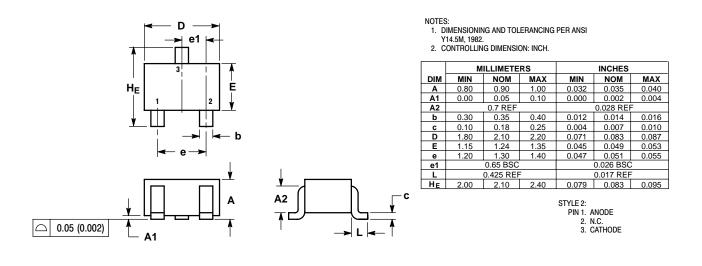


Figure 4. Diode Capacitance

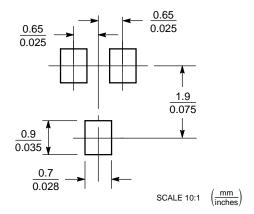
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PACKAGE DIMENSIONS

SC-70 (SOT-323) CASE 419-04 ISSUE M



SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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