RSQ030P03

Transistor

DC-DC Converter (-30V, -3A)

RSQ030P03

Features

- 1) Low On-resistance.(90mΩ at 4.5V)
- 2) High Power Package.
- 4) Low voltage drive.(4.5V)

Applications

DC-DC converter

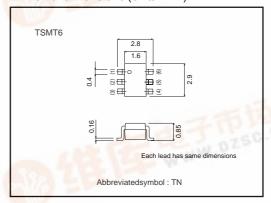
Structure

Silicon P-channel MOSFET

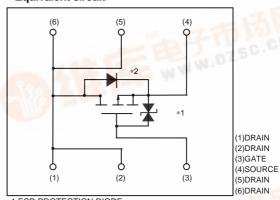
Packaging specifications

Туре	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
RSQ030P03		0

●External dimensions (Units : mm)



Equivalent circuit



*1 ESD PROTECTION DIODE





●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-source voltage		Voss	-30	V	
Gate-source voltage		Vgss	±20	V	
Drain current	Continuous	lσ	±3	A	
	Pulsed	IDP	±12	A *1	
Source current (Body diode)	Continuous	Is	-1	A	
	Pulsed	Isp	-4	A *1	
Total power dissipation		Po	1.25	W*2	
Channel temperature		Tch	150	°C	
Range of Storage temperature		Tstg	−55~+150	°C	

^{*1} Pw≦10μs, Duty cycle≦1%

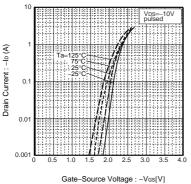
●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
Gate-source leakage	Igss	-	-	±10	μΑ	Vgs=±20V, Vds=0V		
Drain-source breakdown voltage	V(BR)DSS	-30	-	_	V	I _D =-1mA, V _G S=0V		
Zero gate voltage drain current	IDSS	ı	-	-1	μΑ	V _{DS} =-30V, V _{GS} =0V		
Gate threshold voltage	VGS(th)	-1.0	-	-2.5	V	V _{DS} =-10V, I _D =-1mA		
Static drain-source on-state resistance	RDS(on)	ı	60	80	mΩ	In=-3A, Vgs=-10V		
		ı	90	125	mΩ	ID=-3A, VGS=-4.5V		
		-	100	140	mΩ	In=-1.5A, Vgs=-4.0V		
Foward transfer admittance	Y _{fs} *	1.5	1	_	S	VDS=-10V, ID=-1.5A		
Input capacitance	Ciss	-	440	_	pF	V _{DS} =-10V,V _{GS} =0V f=1MHz		
Output capacitance	Coss	ı	110	_	pF			
Reverse transfer capacitance	Crss	-	80	_	pF			
Turn-on delay time	td(on) *	ı	10	-	ns	I _D =-1.5A V _{DD} =-15V V _{GS} =-10V R _L =10Ω R _{GS} =10Ω		
Rise time	tr *	ı	13	_	ns			
Turn-off delay time	td(off) *	ı	40	_	ns			
Fall time	t _f *	_	12	_	ns			
Total gate charge	Qg	_	6.0	-	nC	- V _{DD} ≒−15V V _{GS} =−5V I _D =−3A		
Gate-source charge	Qgs	-	1.6	_	nC			
Gate-drain charge	Qgd	1	2.0	-	nC			
*PULSED Body diode characteristics (source-drain characteristics)								
Forward voltage	VSD	_	-	-1.2	V	Is=-1A, Vgs=0V		



^{*2} Mounted on a ceramic board

•Electrical characteristic curves



Static Drain Source On-State

Static Drain-Source On-State

Static Drain-Source On-State

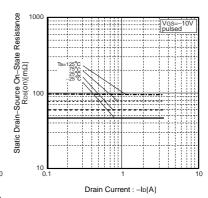
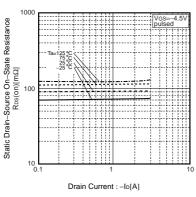
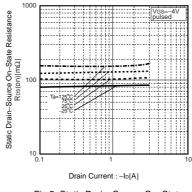


Fig.1 Typical Transfer Characteristics

Fig.2 Static Drain–Source On–State Resistance vs. Drain Current

Fig.3 Static Drain–Source On–State Resistance vs.Drain Current





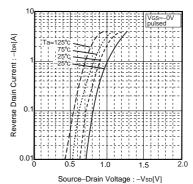
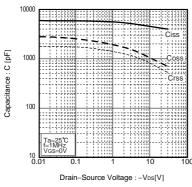
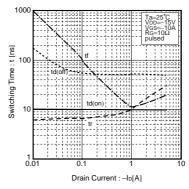


Fig.4 Static Drain–Source On–State Resistance vs.Drain–Current

Fig.5 Static Drain-Source On-State Resistance vs.Drain-Current

Fig.6 Reverse Drain Current Source-Drain Current





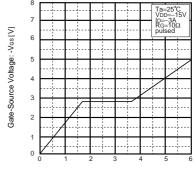


Fig.7 Typical Capactitance vs.Drain-Source Voltage

Fig.8 Switching Characteristics

Total Gate Charge : Qg[nC]
Fig.9 Dynamic Input Characteristics

Measurement circuits

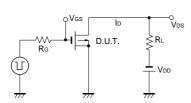


Fig.10 Switching Time Measurement Circuit

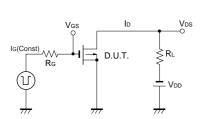


Fig.12 Gate Charge Measurement Circuit

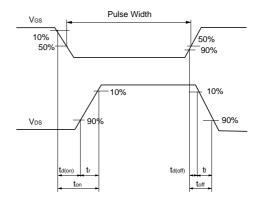


Fig.11 Switching Waveforms

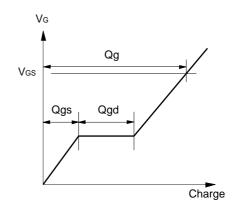


Fig.13 Gate Charge Waveforms

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