

## Features

**SmartSwitch™** 

## **General Description**

The AAT4610A SmartSwitch™ is a member of AnalogicTech™'s Application Specific Power MOSFET™ (ASPM™) product family. It is a Current Limited P-channel MOSFET power switch designed for high-side load-switching applications. This switch operates with inputs ranging from 2.4V to 5.5V, making it ideal for both 3V and 5V systems. An integrated current-limiting circuit protects the input supply against large currents which may cause the supply to fall out of regulation. The AAT4610A is also protected from thermal overload which limits power dissipation and junction temperatures. It can be used to control loads that require up to 1 A. Current limit threshold is programmed with a resistor from SET to ground. The guiescent supply current is typically a low 9µA. In shutdown mode, the supply current decreases to less than 1µA.

The AAT4610A is available in a 5 pin SOT-23 or 8 pin SC70JW specified over -40 to 85°C.

The AAT4600 Series is a family of adjustable and fixed SmartSwitch™ with a range of current handling capabilities. Available are single versions in adj. current limit (AAT4600, AAT4601) as well as in fixed current limit (AAT4602, AAT4625). Dual versions (AAT4620, AAT4626) are also available.

- 2.4V to 5.5V Input voltage range
- Programmable over current threshold
- · Fast transient response:
  - 400ns response to short circuit
- Low quiescent current
  - 9µA typical
  - 1µA max with Switch off
- 145m $\Omega$  typical R<sub>DS(ON)</sub>
- Only 2.5V needed for ON/OFF Control
- Undervoltage Lockout
- Thermal shutdown
- Temp range -40 to 85°C
- 4kV ESD Rating
- UL Approved—File No. E217765
- 5 pin SOT-23 or 8-pin SC70JW package

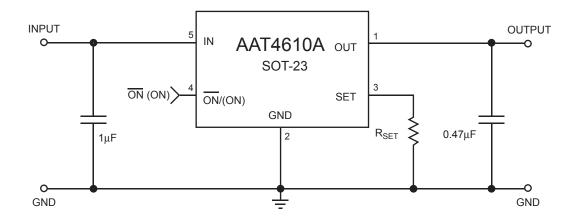
### **Applications**

- Peripheral ports
- Notebook computers
- Personal communication devices
- Hot swap supplies



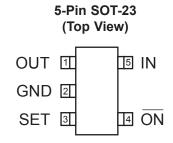
**UL Recognized Component** 

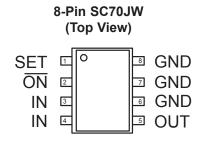
## **Typical Application**





# **Pin Configuration**





## **Pin Descriptions**

Pin #			
SOT23-5	SC70JW-8	Symbol	Function
1	5	OUT	P-channel MOSFET drain. Connect 0.47µF capacitor from OUT to GND.
2	6, 7, 8	GND	Ground connection
3	1	SET	Current-Limit Set Input. A resistor from SET to ground sets the current limit for the switch.
4	2	ŌN	Enable Input. Two versions are available, active-high and active-low. See ordering information for details.
5	3, 4	IN	P-channel MOSFET source. Connect 1µF capacitor from IN to GND.



## **Absolute Maximum Ratings** (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Description	Value	Units
$V_{IN}$	IN to GND	-0.3 to 6	V
$V_{ON}$	ON(ON) to GND	-0.3 to V <sub>IN</sub> +0.3	V
V <sub>SET,</sub> V <sub>OUT</sub>	SET, OUT to GND	-0.3 to V <sub>IN</sub> +0.3	V
I <sub>MAX</sub>	Maximum Continuous Switch Current	2	А
TJ	Operating Junction Temperature Range	-40 to 150	°C
T <sub>LEAD</sub>	Maximum Soldering Temperature (at Leads)	300	°C
V <sub>ESD</sub>	ESD Rating <sup>1</sup> - HBM	4000	V

Note: Stresses above those listed in Absolute Maximum Ratings may cause permanent damage to the device. Functional operation at conditions other than the operating conditions specified is not implied. Only one Absolute Maximum rating should be applied at any one time.

Note 1: Human body model is a 100pF capacitor discharged through a 1.5k $\Omega$  resistor into each pin.

## **Thermal Characteristics**

Symbol	Description	Value	Units
$\Theta_{JA}$	Thermal Resistance <sup>2</sup> (SOT23-5 or SC70JW-8)	150	°C/W
P <sub>D</sub>	Power Dissipation <sup>2</sup> (SOT23-5 or SC70JW-8)	667	mW

Note 2: Mounted on a demo board.

# $\frac{\textbf{Electrical Characteristics}}{\text{are at T}_{A}=25^{\circ}\text{C})} \text{ (V}_{IN} = 5\text{V}, \text{ T}_{A} = -40 \text{ to } 85^{\circ}\text{C} \text{ unless otherwise noted.} \text{ Typical values}$

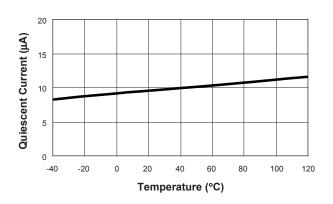
Symbol	Description	Conditions	Min	Тур	Max	Units
V <sub>IN</sub>	Operation Voltage		2.4		5.5	V
IQ	Quiescent Current	$V_{IN} = 5V$ , ON $(\overline{ON}) = active$ , $I_{OUT} = 0$		9	25	μΑ
I <sub>Q(OFF)</sub>	Off Supply Current	ON $(\overline{ON})$ =inactive, $V_{IN}$ = 5.5V			1	μΑ
I <sub>SD(OFF)</sub>	Off Switch Current	ON $(\overline{ON})$ = inactive, $V_{IN}$ = 5.5V, $V_{OUT}$ = 0		.01	1	μA
$V_{UVLO}$	Undervoltage Lockout	Rising edge, 1% hysteresis,		1.8	2.4	V
	On-Resistance	V <sub>IN</sub> =5.0V, T <sub>A</sub> =25°C		145	180	mΩ
R <sub>DS(ON)</sub>		V <sub>IN</sub> =4.5V, T <sub>A</sub> =25°C		150		mΩ
, ,		V <sub>IN</sub> =3.0V, T <sub>A</sub> =25°C		190	230	mΩ
TC <sub>RDS</sub>	On-Resistance Temp-co			2800		ppm/°C
I <sub>LIM</sub>	Current Limit	$R_{SET} = 6.8k\Omega$	.75	1	1.25	Α
I <sub>LIM(MIN)</sub>	Minimum Current Limit			130		mA
$V_{ON(L)}$	ON (ON) Input Low Voltage	V <sub>IN</sub> =2.7V to 5.5V <sup>3</sup>			0.8	V
\/	ON (ON) Input High Voltage	V <sub>IN</sub> =2.7V to <4.2V <sup>3</sup>	2.0			V
V <sub>ON(H)</sub>		V <sub>IN</sub> ≥4.2V to 5.0V <sup>3</sup>	2.4			V
I <sub>ON(SINK)</sub>	ON (ON) Input leakage	V <sub>ON</sub> = 5.5V		0.01	1	μΑ
T <sub>RESP</sub>	Current Limit Response Time	V <sub>IN</sub> =5V		0.4		μs
T <sub>OFF</sub>	Turn-Off Time	$V_{IN}$ =5V, $R_L$ =10 $\Omega$		4	12	μs
T <sub>ON</sub>	Turn-On Time	$V_{IN}$ =5V, $R_L$ =10 $\Omega$		12	200	μs

Note 3: For  $V_{IN}$  outside this range consult Typical ON  $(\overline{ON})$  Threshold curve..

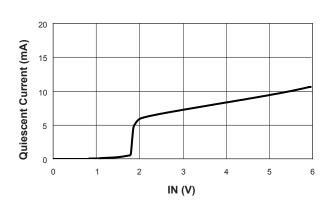


# $\frac{\textbf{Typical Characteristics}}{(\text{Unless otherwise noted, V}_{\text{IN}} = 5\text{V}, T_{\text{A}} = 25^{\circ}\text{C})}$

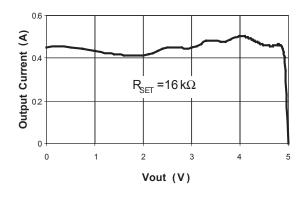
#### **Quiescent Current vs. Temperature**



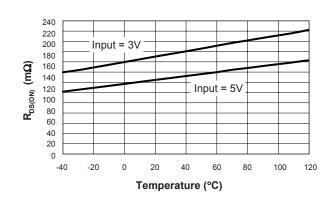
#### Quiescent Current vs. Input Voltage



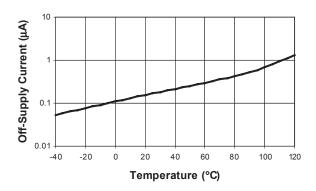
Output Current vs Vout



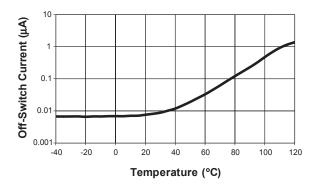
R<sub>DS(ON)</sub> vs. Temperature



Off-Supply Current vs. Temperature



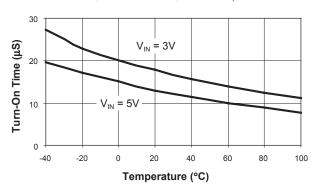
Off-Switch Current vs. Temperature



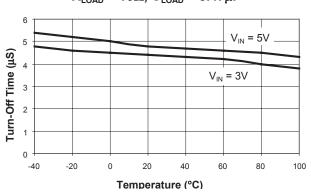


(Unless otherwise noted,  $V_{IN}$  = 5V,  $T_A$  = 25°C)

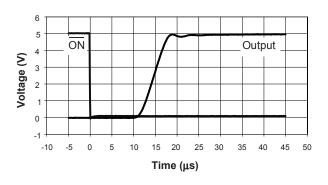
Turn-On vs. Temperature  $R_{LOAD} = 10\Omega$ ,  $C_{LOAD} = 0.47\mu F$ 



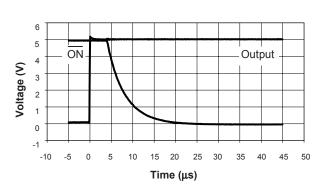
Turn-Off vs. Temperature  $R_{LOAD} = 10\Omega$ ,  $C_{LOAD} = 0.47\mu$ F



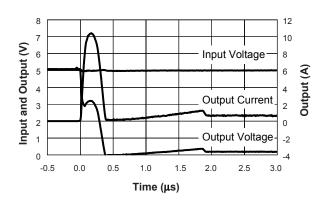
 $Turn\text{-On} \\ R_L = 10\Omega, \ C_L = 0.47 \mu F$ 



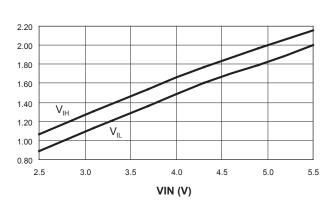
Turn-Off  $R_L = 10\Omega, \ C_L = 0.47\mu\text{F}$ 



Short Circuit through  $0.3\Omega$ 



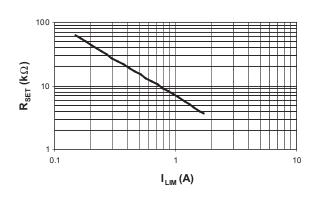
 $V_{\text{IH}}$  and  $V_{\text{IL}}$  vs. VIN



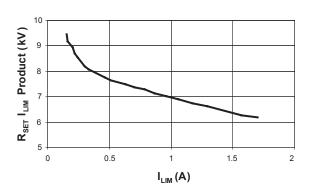


(Unless otherwise noted,  $V_{IN}$  = 5V,  $T_A$  = 25°C)

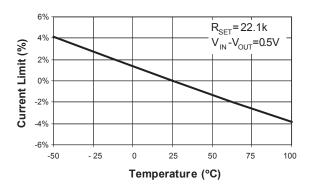
R<sub>SET</sub> vs I<sub>LIM</sub>



 $R_{\text{SET}}$  coefficent vs  $I_{\text{LIM}}$ 



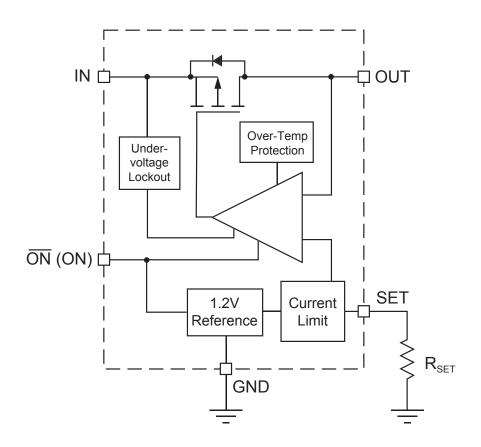
#### **Current Limit v. Temperature**



6 4610A.2003.05.0.92



## **Functional Block Diagram**



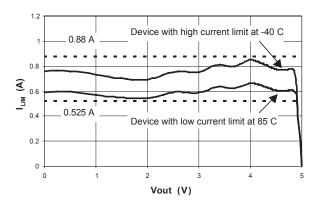


## **Applications Information**

#### **Setting Current Limit**

In most applications, the variation in I<sub>IIM</sub> must be taken into account when determining R<sub>SFT</sub> The I<sub>LIM</sub> variation is due to processing variations from part to part, as well as variations in the voltages at IN (pin 5) and OUT (pin 3), plus the operating temperature. See charts "Current Limit vs. Temperature", and "Output Current vs. VOUT". Together these three factors add up to a ±25% tolerance (see ILIM specification in Electrical Characteristics section). In the figure below, a cold device with a statistically higher current limit, and a hot device with a statistically lower current limit, both with R<sub>SFT</sub> equal to 10.5k $\Omega$  are shown. While the chart, "R<sub>SET</sub> vs. I<sub>LIM</sub>" indicates an  $I_{LIM}$  of 0.7A with an  $R_{SFT}$  of 10.5k $\Omega$ , this figure shows that the actual current limit will be at least 0.525A, and no greater than 0.880A.

#### Current Limit Using 10.5 k $\Omega$



To determine  $R_{SET}$ , start with the maximum current drawn by the load, and multiply it by 1.33. (typical\_I<sub>LIM</sub> = minimum\_I<sub>LIM</sub> / 0.75) This is the typical current limit value. Next, refer to " $R_{SET}$  vs. I<sub>LIM</sub>" and find the  $R_{SET}$  that corresponds to the typical current limit value. Choose the largest resistor available

that is less than or equal to it. For greater precision, the value of  $R_{\text{SET}}$  may also be calculated using the  $I_{\text{LIM}}$   $R_{\text{SET}}$  product found in the chart " $R_{\text{SET}}$  coefficient vs.  $I_{\text{LIM}}$ ". The maximum current is derived by multiplying the typical current for the chosen  $R_{\text{SET}}$  in the chart by 1.25. A few standard resistor values are listed in the table "Current Limit  $R_{\text{SET}}$  Values".

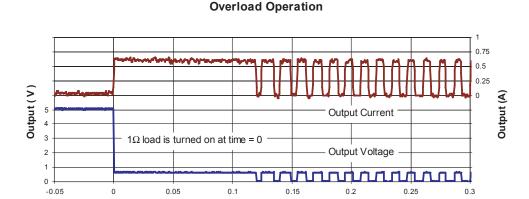
### **Current Limit R<sub>SET</sub> Values**

$R_{SET}$ (k $\Omega$ )	Current Limit typ (mA)	Device will not current limit below: (mA)	Device always current limits below: (mA)
40.2	200	150	250
30.9	250	188	313
24.9	300	225	375
22.1	350	263	438
19.6	400	300	500
17.8	450	338	563
16.2	500	375	625
14.7	550	413	688
13.0	600	450	750
10.5	700	525	875
8.87	800	600	1000
7.50	900	675	1125
6.81	1000	750	1250
6.04	1100	825	1375
5.49	1200	900	1500
4.99	1300	975	1625
4.64	1400	1050	1750

Example: A USB port requires 0.5A. 0.5A multiplied by 1.33 is 0.665A. From the chart named " $R_{SET}$  vs.  $I_{LIM}$ ",  $R_{SET}$  should be less than 11k $\Omega$ . 10.5 k $\Omega$  is a standard value that is a little less than 11k $\Omega$  but very close. The chart reads approximately 0.700A as a typical  $I_{LIM}$  value for 10.5k $\Omega$ . Multiplying 0.700A by 0.75 and 1.25 shows that the AAT4610 will limit the load current to greater than 0.525A but less than 0.875A.

8 4610A.2003.05.0.92





Time (s)

#### **Operation in Current Limit**

When a heavy load is applied to the output of the AAT4610A, the load current is limited to the value of  $I_{LIM}$  determined by  $R_{SET}$ . See the figure "Overload Operation". Since the load is demanding more current than  $I_{LIM}$ , the voltage at the output drops. This causes the AAT4610A to dissipate a larger than normal quantity of power, and its die temperature to increase. When the die temperature exceeds an over temperature limit, the AAT4610A will shut down until is has cooled sufficiently, at which point it will startup again. The AAT4610A will continue to cycle on and off until the load is removed, power is removed, or until a logic high level is applied to ON (pin 4).

#### **Enable Input**

In many systems, power planes are controlled by integrated circuits which run at lower voltages than the power plane itself. The enable input ON (pin 4) of the AAT4610A has low and high threshold voltages that accommodate this condition. The threshold voltages are compatible with 5 volt TTL, and 2.5 volt to 5 volt CMOS.

#### **Reverse Voltage**

The AAT4610A is designed to control current flowing from IN to OUT. If a voltage is applied to OUT which is greater than the voltage on IN, large currents may flow. This could cause damage to the AAT4610A.

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## **Ordering Information**

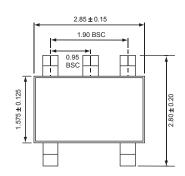
Package	Enable	Marking <sup>1</sup>	Part Number (Tape and Reel)
SOT23-5	ON (active low)	ERXYY	AAT4610AIGV-T1
SOT23-5	ON (active high)	HXXYY	AAT4610AIGV-1-T1
SC70JW-8	ON (active low)	FKXYY	AAT4610AIJS-T1
SC70JW-8	ON (active high)	HXXYY	AAT4610AIJS-1-T1

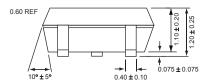
Note: Sample stock is generally held on all part numbers listed in **BOLD**.

Note 1: XYY = assembly and date code.

## **Package Information**

#### SOT23-5



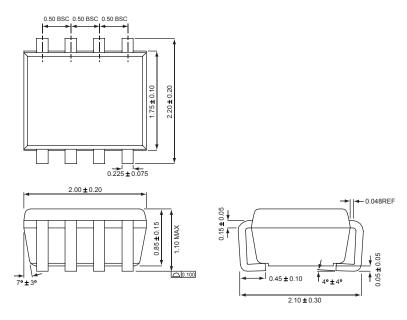




All dimensions in millimeters.



#### SC70JW-8



All dimensions in millimeters.





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12 *4610A.2003.05.0.92* 

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