

# MMBTA42LT1, MMBTA43LT1

MMBTA42LT1 is a Preferred Device

## High Voltage Transistors NPN Silicon



ON Semiconductor®

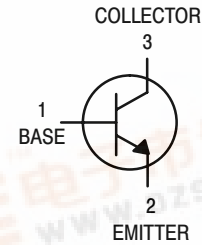
<http://onsemi.com>

### Features

- Pb-Free Package May be Available. The G-Suffix Denotes a Pb-Free Lead Finish

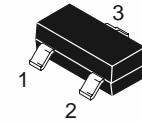
### MAXIMUM RATINGS

| Rating                       | Symbol    | MMBTA42 | MMBTA43 | Unit |
|------------------------------|-----------|---------|---------|------|
| Collector-Emitter Voltage    | $V_{CEO}$ | 300     | 200     | Vdc  |
| Collector-Base Voltage       | $V_{CBO}$ | 300     | 200     | Vdc  |
| Emitter-Base Voltage         | $V_{EBO}$ | 6.0     | 6.0     | Vdc  |
| Collector Current-Continuous | $I_C$     | 500     |         | mAdc |



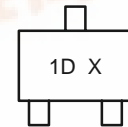
### THERMAL CHARACTERISTICS

| Characteristic  | Symbol          | Max         | Unit                      |
|---|-----------------|-------------|---------------------------|
| Total Device Dissipation FR-5 Board (Note 1)<br>$T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$           | $P_D$           | 225         | mW                        |
|   |                 | 1.8         | mW/ $^\circ\text{C}$      |
| Thermal Resistance, Junction-to-Ambient   | $R_{\theta JA}$ | 556         | $^\circ\text{C}/\text{W}$ |
| Total Device Dissipation<br>Alumina Substrate (Note 2)<br>$T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$ | $P_D$           | 300         | mW                        |
|   |                 | 2.4         | mW/ $^\circ\text{C}$      |
| Thermal Resistance, Junction-to-Ambient   | $R_{\theta JA}$ | 417         | $^\circ\text{C}/\text{W}$ |
| Junction and Storage Temperature  | $T_J, T_{stg}$  | -55 to +150 | $^\circ\text{C}$          |

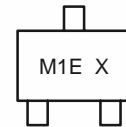


SOT-23 (TO-236)  
CASE 318  
STYLE 6

### MARKING DIAGRAMS



MMBTA42LT1



MMBTA43LT1

1D, M1E = Specific Device Code  
X = Date Code

### ORDERING INFORMATION

| Device      | Package | Shipping†         |
|-------------|---------|-------------------|
| MMBTA42LT1  | SOT-23  | 3000/Tape & Reel  |
| MMBTA42LT1G | SOT-23  | 3000/Tape & Reel  |
| MMBTA43LT1  | SOT-23  | 3000/Tape & Reel  |
| MMBTA43LT3  | SOT-23  | 10000/Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.



## MMBTA42LT1, MMBTA43LT1

### ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

| Characteristic  |  | Symbol        | Min                  | Max              | Unit            |
|---|--|---------------|----------------------|------------------|-----------------|
| <b>OFF CHARACTERISTICS</b>  |  |               |                      |                  |                 |
| Collector–Emitter Breakdown Voltage (Note 3)<br>( $I_C = 1.0\text{ mAdc}$ , $I_B = 0$ )   | MMBTA42<br>MMBTA43                             | $V_{(BR)CEO}$ | 300<br>200           | –<br>–           | Vdc             |
| Collector–Base Breakdown Voltage<br>( $I_C = 100\text{ }\mu\text{Adc}$ , $I_E = 0$ )  | MMBTA42<br>MMBTA43                             | $V_{(BR)CBO}$ | 300<br>200           | –<br>–           | Vdc             |
| Emitter–Base Breakdown Voltage<br>( $I_E = 100\text{ }\mu\text{Adc}$ , $I_C = 0$ )  |  | $V_{(BR)EBO}$ | 6.0                  | –                | Vdc             |
| Collector Cutoff Current<br>( $V_{CB} = 200\text{ Vdc}$ , $I_E = 0$ )<br>( $V_{CB} = 160\text{ Vdc}$ , $I_E = 0$ )  | MMBTA42<br>MMBTA43                             | $I_{CBO}$     | –<br>–               | 0.1<br>0.1       | $\mu\text{Adc}$ |
| Emitter Cutoff Current<br>( $V_{EB} = 6.0\text{ Vdc}$ , $I_C = 0$ )<br>( $V_{EB} = 4.0\text{ Vdc}$ , $I_C = 0$ )  | MMBTA42<br>MMBTA43                             | $I_{EBO}$     | –<br>–               | 0.1<br>0.1       | $\mu\text{Adc}$ |
| <b>ON CHARACTERISTICS</b> (Note 3)  |  |               |                      |                  |                 |
| DC Current Gain<br>( $I_C = 1.0\text{ mAdc}$ , $V_{CE} = 10\text{ Vdc}$ )<br>( $I_C = 10\text{ mAdc}$ , $V_{CE} = 10\text{ Vdc}$ )<br><br>( $I_C = 30\text{ mAdc}$ , $V_{CE} = 10\text{ Vdc}$ ) | Both Types<br>Both Types<br>MMBTA42<br>MMBTA43 | $h_{FE}$      | 25<br>40<br>40<br>40 | –<br>–<br>–<br>– | –               |
| Collector–Emitter Saturation Voltage<br>( $I_C = 20\text{ mAdc}$ , $I_B = 2.0\text{ mAdc}$ )  | MMBTA42<br>MMBTA43                             | $V_{CE(sat)}$ | –<br>–               | 0.5<br>0.5       | Vdc             |
| Base–Emitter Saturation Voltage<br>( $I_C = 20\text{ mAdc}$ , $I_B = 2.0\text{ mAdc}$ )   |  | $V_{BE(sat)}$ | –                    | 0.9              | Vdc             |
| <b>SMALL–SIGNAL CHARACTERISTICS</b>   |  |               |                      |                  |                 |
| Current–Gain – Bandwidth Product<br>( $I_C = 10\text{ mAdc}$ , $V_{CE} = 20\text{ Vdc}$ , $f = 100\text{ MHz}$ )  |  | $f_T$         | 50                   | –                | MHz             |
| Collector–Base Capacitance<br>( $V_{CB} = 20\text{ Vdc}$ , $I_E = 0$ , $f = 1.0\text{ MHz}$ )   | MMBTA42<br>MMBTA43                             | $C_{cb}$      | –<br>–               | 3.0<br>4.0       | pF              |

3. Pulse Test: Pulse Width  $\leq 300\text{ }\mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .

# MMBTA42LT1, MMBTA43LT1

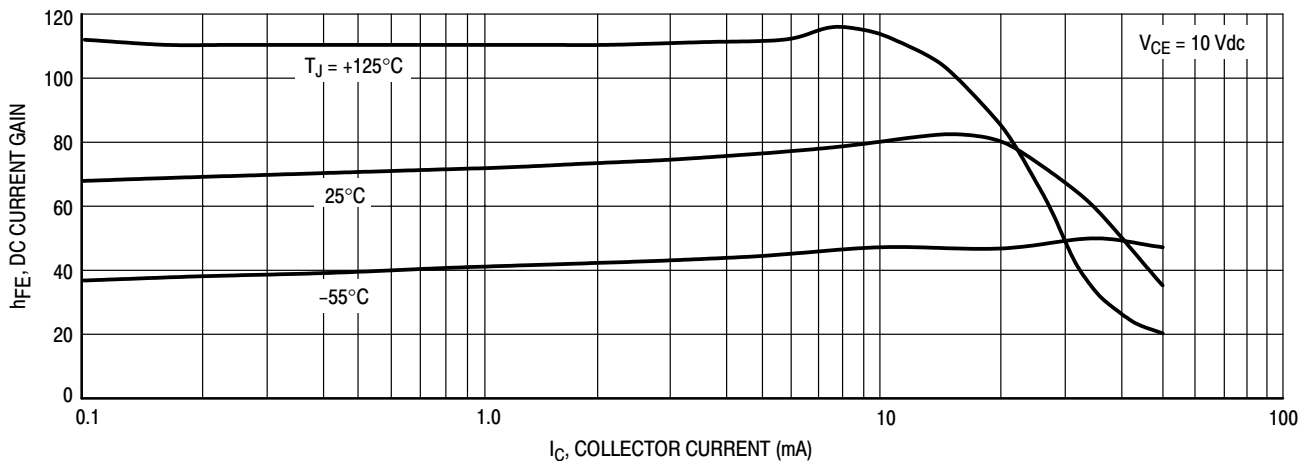


Figure 1. DC Current Gain

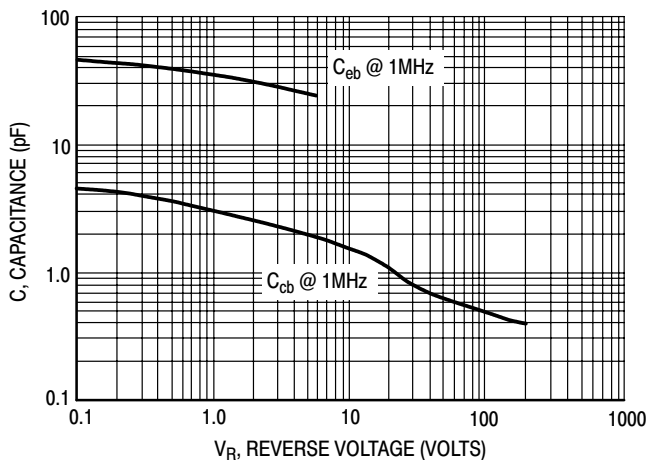


Figure 2. Capacitance

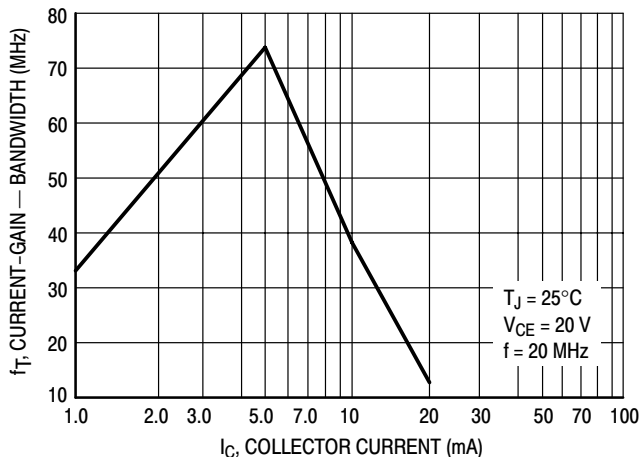


Figure 3. Current-Gain - Bandwidth

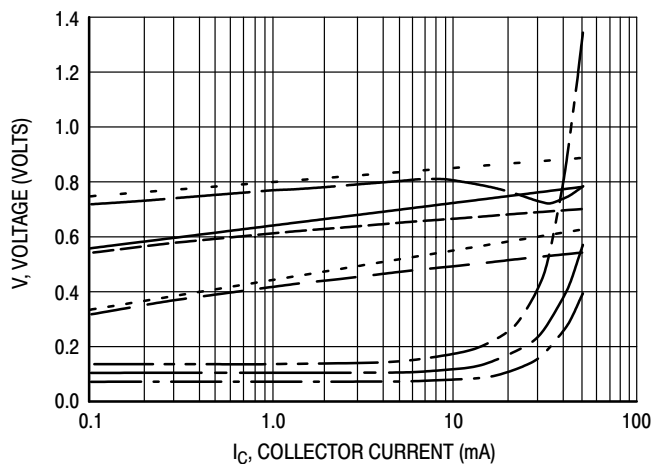
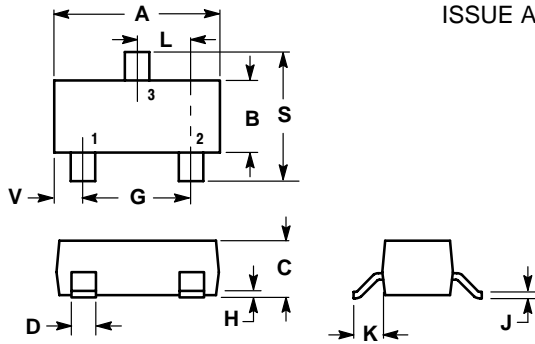


Figure 4. "ON" Voltages

# MMBTA42LT1, MMBTA43LT1

## PACKAGE DIMENSIONS

SOT-23 (TO-236)  
CASE 318-08  
ISSUE AH



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. 318-03 AND -07 OBSOLETE, NEW STANDARD 318-08.

| DIM | INCHES |        | MILLIMETERS |       |
|-----|--------|--------|-------------|-------|
|     | MIN    | MAX    | MIN         | MAX   |
| A   | 0.1102 | 0.1197 | 2.80        | 3.04  |
| B   | 0.0472 | 0.0551 | 1.20        | 1.40  |
| C   | 0.0350 | 0.0440 | 0.89        | 1.11  |
| D   | 0.0150 | 0.0200 | 0.37        | 0.50  |
| G   | 0.0701 | 0.0807 | 1.78        | 2.04  |
| H   | 0.0005 | 0.0040 | 0.013       | 0.100 |
| J   | 0.0034 | 0.0070 | 0.085       | 0.177 |
| K   | 0.0140 | 0.0285 | 0.35        | 0.69  |
| L   | 0.0350 | 0.0401 | 0.89        | 1.02  |
| S   | 0.0830 | 0.1039 | 2.10        | 2.64  |
| V   | 0.0177 | 0.0236 | 0.45        | 0.60  |

STYLE 6:

- PIN 1. BASE
- EMITTER
- COLLECTOR

### SOLDERING FOOTPRINT\*

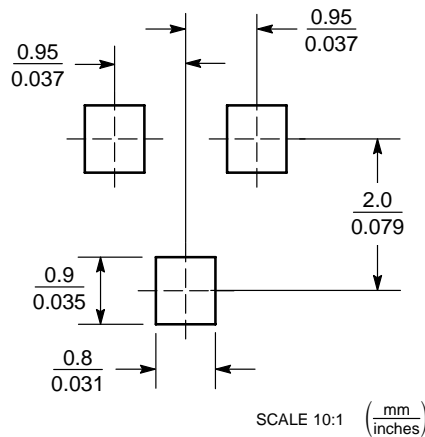


Figure 5. SOT-23

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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