

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

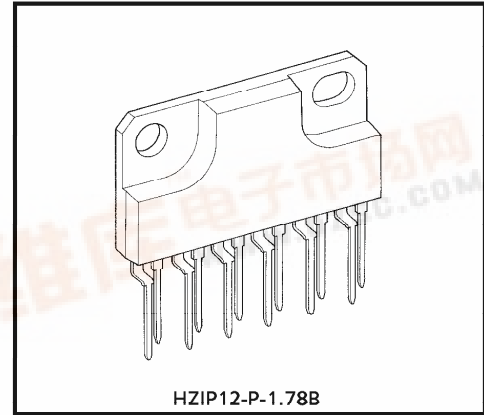
# TA8429H

## 3.0A FULL BRIDGE DRIVER

The TA8429H is full bridge driver IC for brush motor rotation control that has current capability of up to 3.0A (AVE.). Thermal shutdown and short current protector are provided. And also stand-by function available.

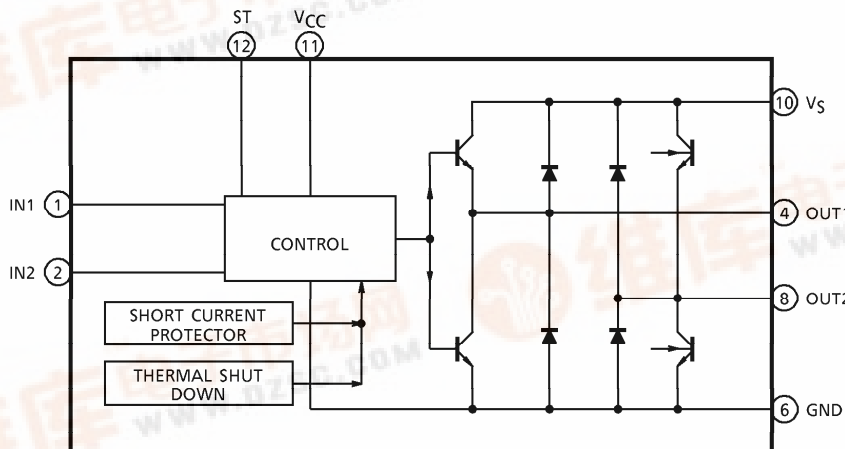
### FEATURES

- Output current is as large as 3.0A (AVE.) and 4.5A (PEAK.)
- Stand-by mode available :  $I_{ST} \leq 100\mu A$  (MAX.)
- Thermal shutdown and short circuit protector circuit are provided.
- 4 modes (Forward / reverse / short brake and stop) are available with 2 low active TTL compatible inputs control.
- Free wheeling diodes are equipped.
- HZIP power package sealed.
- Wide range of operating voltage :  $V_{CC} = 7 \sim 27V$   
 $V_S$  (opr.) =  $0 \sim 27V$



Weight : 4.04g (Typ.)

### BLOCK DIAGRAM



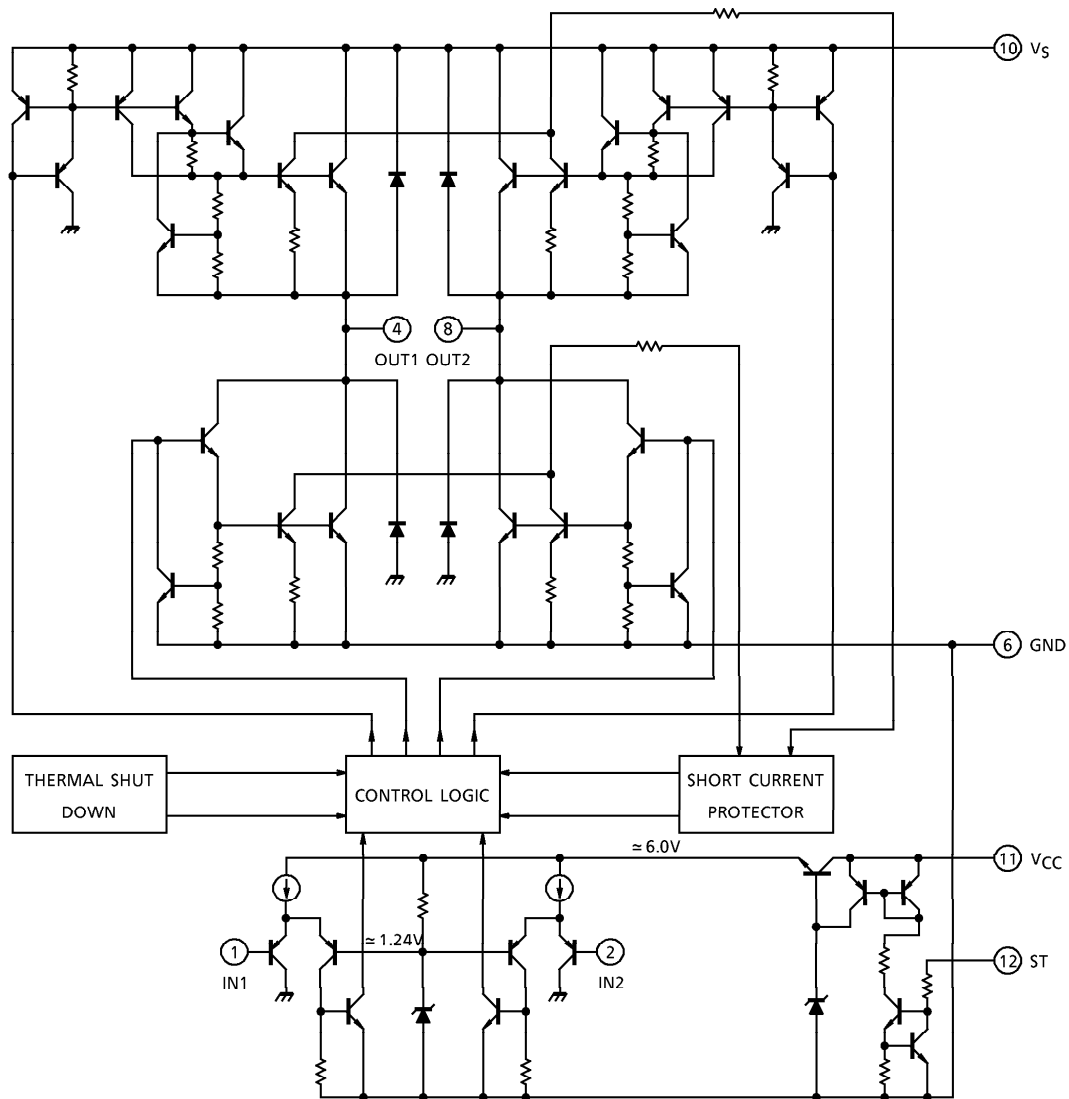
(Note 1) Pin③, ⑤, ⑦, and ⑨ are non connection.

(Note 2) Heat fin is connected with GND with low impedance.

## PIN FUNCTION

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION
1	IN1	TTL compatible control inputs
2	IN2	(PNP type low active comparator inputs)
3	N.C	Non connection
4	OUT1	Output terminals, free wheeling diodes are connected between each output with GND and $V_S$ .
5	N.C	Non connection
6	GND	GND terminal
7	N.C	Non connection
8	OUT2	Output terminals, free wheeling diodes are connected between each output with GND and $V_S$ .
9	N.C	Non Connection
10	$V_S$	Supply voltage terminal for Motor Drive
11	$V_{CC}$	Supply voltage terminal for control circuit
12	ST	Stand-by terminal. Stand-by state is obtained with this terminal connected with GND (or Open).

**INTERNAL CIRCUIT**



**FUNCTION**

INPUT			OUTPUT		MODE
IN1	IN2	ST	OUT1	OUT2	MOTOR
H	H	H	L	L	Short brake
L	H	H	L	H	CW / CCW
H	L	H	H	L	CCW / CW
L	L	H	OFF (high impedance)		Stop
H/L	H/L	L	OFF (high impedance)		Stand-by

**MAXIMUM RATINGS (Ta = 25°C)**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V <sub>CC</sub> , V <sub>S</sub>	30	V
Input Voltage	V <sub>IN</sub>	-0.3~V <sub>CC</sub>	V
Output Current	AVE.	I <sub>O</sub> (AVE.)	3.0
	PEAK	I <sub>O</sub> (PEAK)	4.5 (Note 1)
Power Dissipation	P <sub>D</sub>	2.25 (Note 2)	W
		21.6 (Note 3)	
Operating Temperature	T <sub>opr</sub>	-30~85	°C
Storage Temperature	T <sub>stg</sub>	-55~150	°C

(Note 1) t = 100ms

(Note 2) No heat sink

(Note 3) T<sub>c</sub> = 85°C

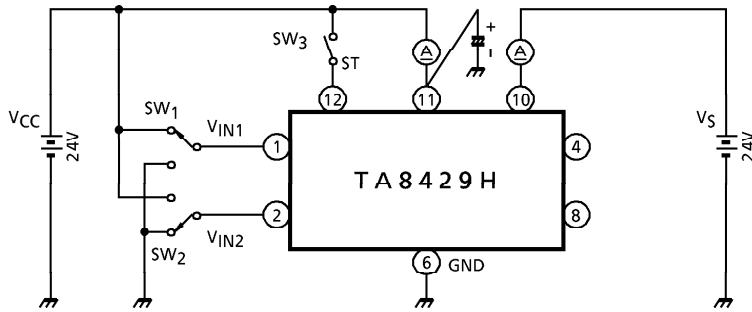
**ELECTRICAL CHARACTERISTICS (V<sub>CC</sub> = 24V, V<sub>S</sub> = 24V, Ta = 25°C)**

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Quiescent Current (I) (V <sub>CC</sub> Line)	I <sub>CC1</sub>	1	Stop mode	—	6	12	mA
	I <sub>CC2</sub>		Forward / reverse mode	—	20	40	
	I <sub>CC3</sub>		Brake mode	—	20	40	
Quiescent Current (II) (V <sub>S</sub> Line)	I <sub>S1</sub>	1	Stop mode	—	3	8	mA
	I <sub>S2</sub>		Forward / reverse mode	—	16	40	
	I <sub>S3</sub>		Brake mode	—	3	8	
Input Voltage	V <sub>INL</sub>	2	—	—	—	0.8	V
	V <sub>INH</sub>		—	2.0	—	—	
Input Current	I <sub>INL</sub>	2	V <sub>IN</sub> = GND	—	—	12	μA
	I <sub>INH</sub>		V <sub>IN</sub> = V <sub>CC</sub>	—	—	10	
Output Saturation Voltage (Note)	V <sub>sat1</sub>	3	I <sub>O</sub> = 1.5A	—	2.1	2.8	V
	V <sub>sat2</sub>		I <sub>O</sub> = 3.0A	—	3.3	4.1	
Output Leakage Current	I <sub>LU</sub>	4	V <sub>L</sub> = 25V	—	—	50	μA
	I <sub>LL</sub>		V <sub>L</sub> = 25V	—	—	50	
Diode Forward Voltage	V <sub>FU</sub>	5	I <sub>F</sub> = 3.0A	—	5.0	—	V
	V <sub>FL</sub>		I <sub>F</sub> = 3.0A	—	1.5	—	
Limiting Current	I <sub>SD</sub>	—	—	—	5	—	A
Thermal Shutdown Operating Temperature	T <sub>SD</sub>	—	—	—	150	—	°C
Stand-by Current	I <sub>ST</sub>	1	—	—	—	100	μA
Propagation Delay Time	t <sub>pLH</sub>	2	—	—	1	10	μs
	t <sub>pHL</sub>	2	—	—	1	10	

(Note) Upper and lower side total

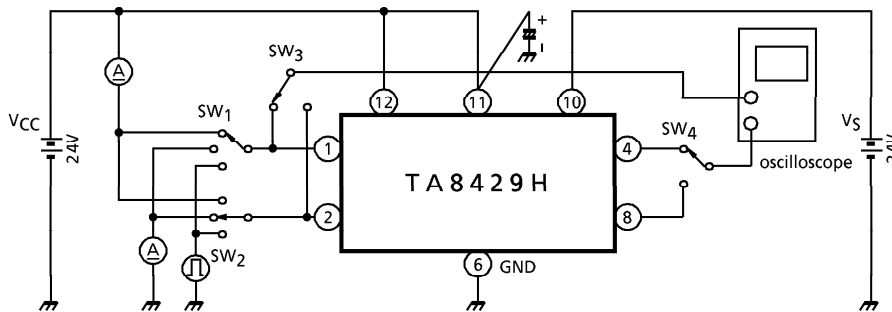
**TEST CIRCUIT 1.**

$I_{S1}, I_{S2}, I_{S3}, I_{CC1}, I_{CC2}, I_{CC3}, I_{ST}$



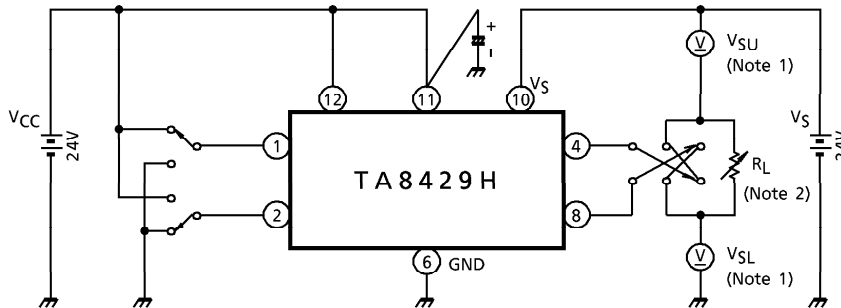
**TEST CIRCUIT 2.**

$V_{INH}, V_{INL}, I_{INH}, I_{INL}, t_{pHL}, t_{pLH}$



**TEST CIRCUIT 3.**

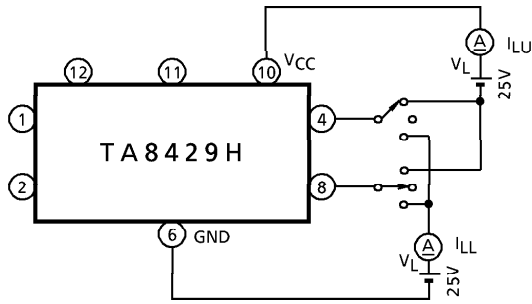
$V_{sat}$



(Note 1)  $V_{sat} = V_{SU} + V_{SL}$   
 (Note 2) Calibrate  $I_O$  to 1.5/3.0A by  $R_L$

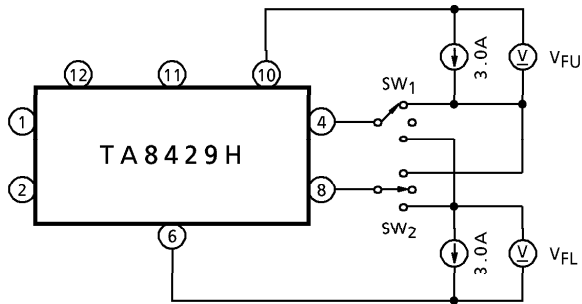
**TEST CIRCUIT 4.**

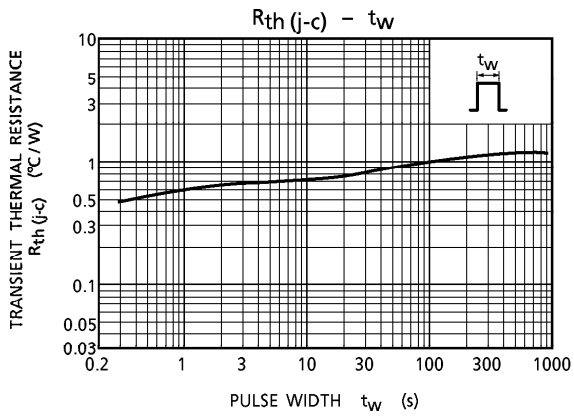
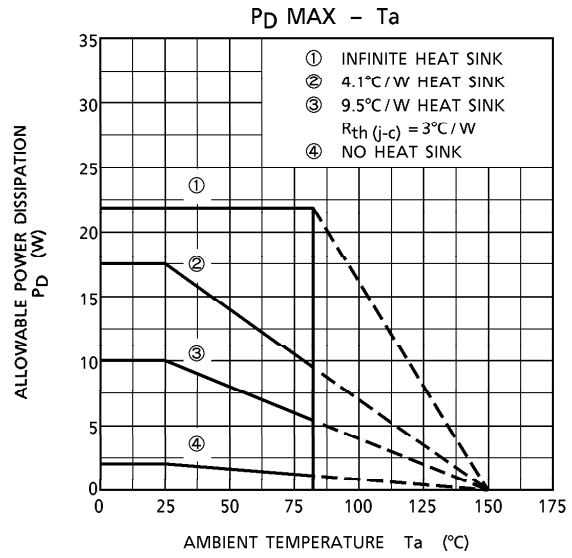
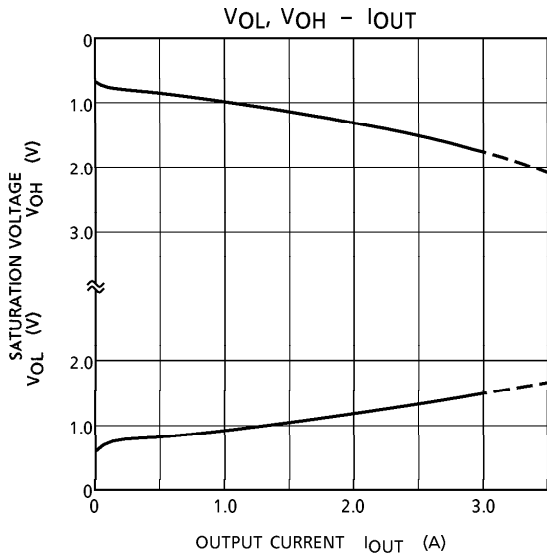
$I_{LU}$ ,  $I_{LL}$



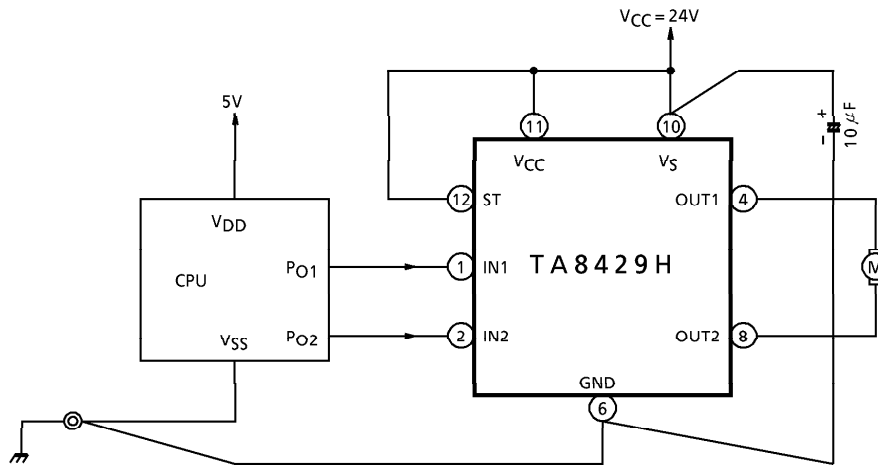
**TEST CIRCUIT 5.**

$V_{FU}$ ,  $V_{FL}$

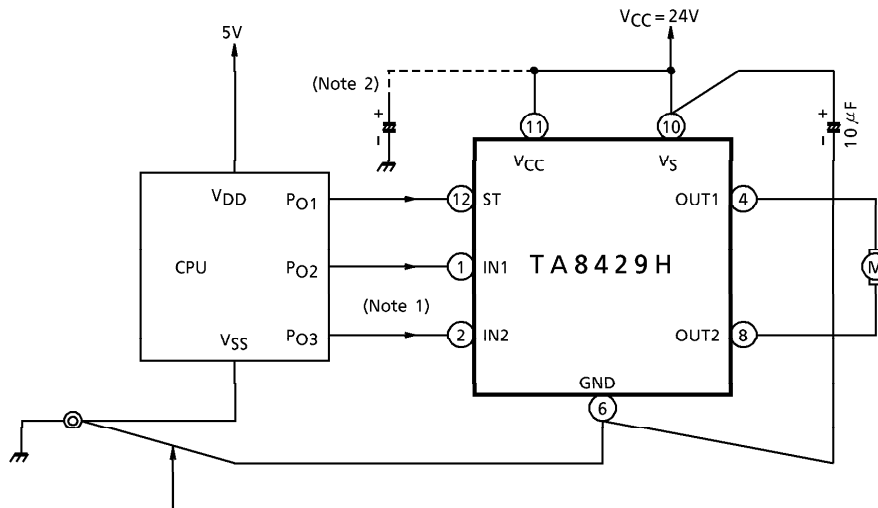




APPLICATION CIRCUIT 1. (Single power supply operation)

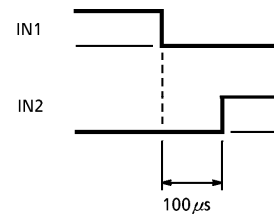


APPLICATION CIRCUIT 2. (Dual power supply (Control and Motor) operation)



Not to have a common impedance with other lines and use low impedance wire.

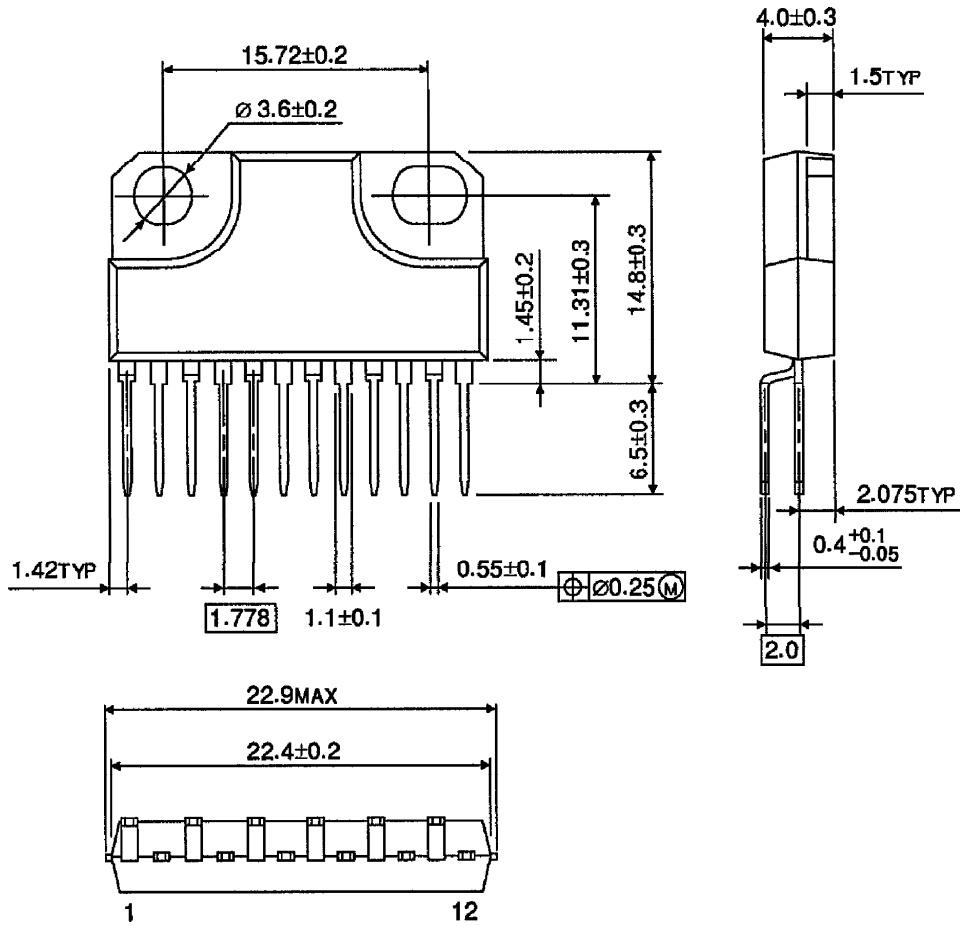
- (Note 1) Recommend to take approximately 100µs of input dead time for reliable operations.
- (Note 2) Connect if required.
- (Note 3) Utmost care is necessary in the design of the output line, VS and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.





OUTLINE DRAWING  
HZIP12-P-1.78B

Unit : mm



Weight : 4.04g (Typ.)