

## **Li-Ion Battery Charger**

#### **PRODUCTION DATA SHEET**

## DESCRIPTION

The LX2203 is a tiny linear battery current and voltage regulation, reverse deeply discharged batteries. The accurate programmable charge threshold. The LX2203 enters a low current programming make it easy to power is removed. logically reprogram the charge current limit at any time.

The LX2203 charges the battery in charger for Lithium Ion or Lithium two phases: constant current and Polymer batteries. The LX2203 constant voltage. A reduced current includes built in MOSFET, accurate conditioning mode is provided for The blocking protection, over temperature LX2203 automatically restarts if the control, and charge status indicator. battery voltage falls below the top-off current and independent termination quiescent current sleep mode when

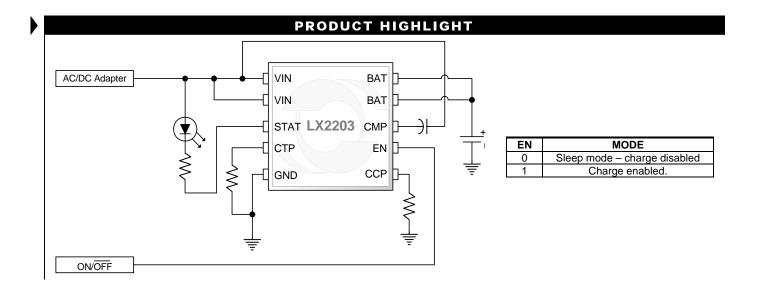
IMPORTANT: For the most current data, consult MICROSEM's website: http://www.microsemi.com

### **KEY FEATURES**

- Up to 1A charge current
- . Low drop out design
- . Reverse leakage protection
- Linear thermal control loop
- Pre-charge conditioning . **Programmable Termination**
- Current
- Short Circuit protected
- Small 3x3mm MLP package

## APPLICATIONS

- PDAs, MP3 Players
- Cell Phones .
- Charging cradles .
- **Digital Cameras**



	PACKAGE ORDER INFO					
	T <sub>A</sub> (°C)	LD Plastic MLP 10-Pin				
		RoHS Compliant / Pb-free				
	-40 to 85	LX2203CLD				

Note: Available in Tape & Reel. Append the letters "TR" to the part number. (i.e. LX2203CLD-TR)



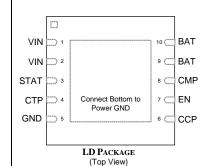
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ABSOLUTE MAXIMUM RATINGS
Supply Voltage (VIN)

Note: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of specified terminal.

## THERMAL DATA



PACKAGE PIN OUT

RoHS / Pb-free 100% Matte Tin Lead Finish

Plastic Micro Leadframe Package 10-Pin

THERMAL RESISTANCE-JUNCTION TO AMBIENT,  $\theta_{\mathsf{JA}}$ 

49°C/W

Junction Temperature Calculation:  $T_J = T_A + (P_D \ x \ \theta_{JA})$ .

The  $\theta_{JA}$  numbers are guidelines for the thermal performance of the device/pc-board system. All of the above assume no ambient airflow.

	FUNCTIONAL PIN DESCRIPTION						
Name	Description						
BAT	Charging Output - This pin is wired to the positive terminal of the battery. (The negative battery terminal is wired to GND.)						
CCP	Charge Current Programming Pin - A resistor (Rccp) is connected between this pin and GND. See application section for programming information.						
CMP	Compensation Pin – Apply a 0.01µF capacitor between CMP and VIN pins.						
CTP	Charge Termination Programming Pin – A resistor (Rctp) is connected between this pin and GND See application section for programming information.						
EN	Enable - Applying a TTL compatible Hi signal enables the charger, a Low signal disables the charger and puts it in sleep mode.						
GND	Common Ground.						
STAT	Status - This pin is a logic low level when the battery is being charged. Pin can sink up to 5mA.						
VIN	Voltage Input – Supply Voltage. Must be greater than VBAT to charge.						



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## ELECTRICAL CHARACTERISTICS

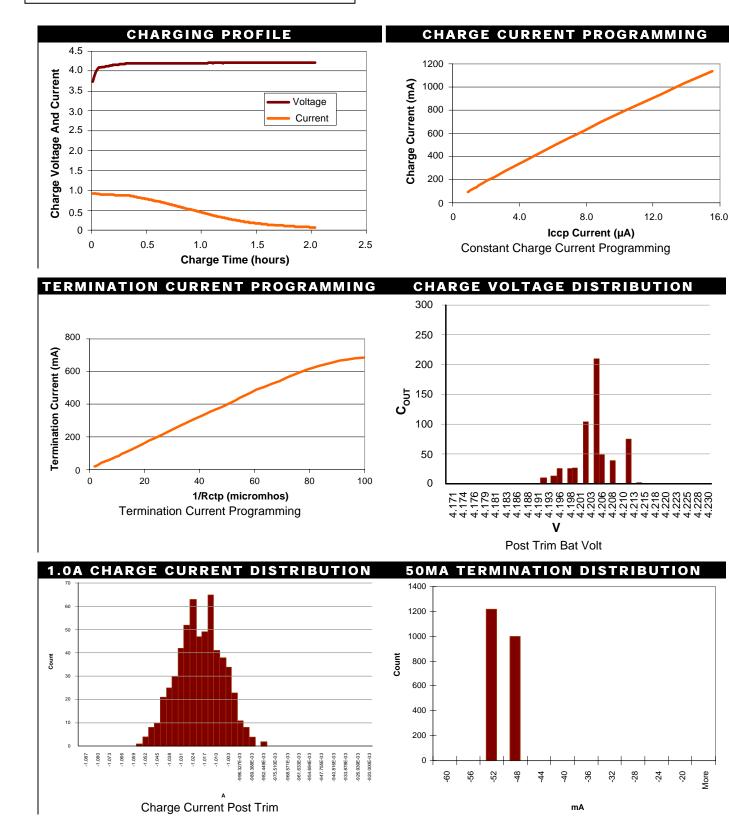
Unless otherwise specified, the following specifications apply over the ambient temperature  $0^{\circ}C \le T_A \le 70^{\circ}C$  except where otherwise noted and the following test conditions:  $V_{VIN} = 5V$ ,  $V_{BAT} = 3.8V$ , Rccp = 90.9k, Rctp = 150k.

Parameter	Symbol	Test Conditions		LX2203		Unit
Farameter		lest Conditions	Min	Тур	Max	Units
MAIN CIRCUITRY						
Input Voltage	$V_{\text{VIN}}$		4.35		6	V
Quiescent Current		V <sub>EN</sub> = Hi		3	5	mA
Quescent Current	I <sub>GND</sub>	V <sub>EN</sub> = Lo		7		μA
CTP Bias Voltage	V <sub>CTP</sub>			1.25		V
CCP Bias Voltage	V <sub>CCP</sub>			1.25		V
CONSTANT VOLTAGE MODE	-					•
BAT Output Voltage	V <sub>BAT(MAX)</sub>		4.16	4.2	4.24	V
Top Off Charge Droop Threshold	V <sub>DRP</sub>	V <sub>DRP</sub> /V <sub>BAT(MAX)</sub>	96	97	98	%
Charge Termination Taper Current		$V_{BAT} = V_{BAT(MAX)}$	42	51	60	mA
CONSTANT CURRENT MODE	•					
BAT Constant Current			0.9	1.0	1.1	A
Conditioning Current	I <sub>COND</sub>	$V_{BAT} < V_{BAT(COND)}$	4%	5%	6%	I <sub>CON</sub>
Conditioning Current Mode Threshold Voltage	V <sub>BAT(COND)</sub>	V <sub>BAT(COND)</sub> /V <sub>BAT(MAX)</sub>	60	63	66	%
LOGIC						
STAT Logic High Output	V <sub>STAT</sub>	I <sub>STAT</sub> = -100uA	4.5			V
STAT Logic Low Output	V <sub>STAT</sub>	I <sub>STAT</sub> = 5mA			0.5	V
EN Select Threshold	V <sub>EN</sub>	Logic Hi	2.0			
		Logic Lo			0.8	
EN bias current	I <sub>EN</sub>	V <sub>EN</sub> = Hi		8		μA
THERMAL SHUTDOWN	•					
Maximum Junction Temperature	TJ			150		°C
PASS ELEMENT	•		I			
Reverse mode current	-IBAT	V <sub>VIN</sub> < VBAT		5		μA
UNDER VOLTAGE LOCKOUT			I			
Rising Threshold Voltage	VUVLO+			4.2		V
Falling Threshold Voltage	Vuvlo-			3.9		V



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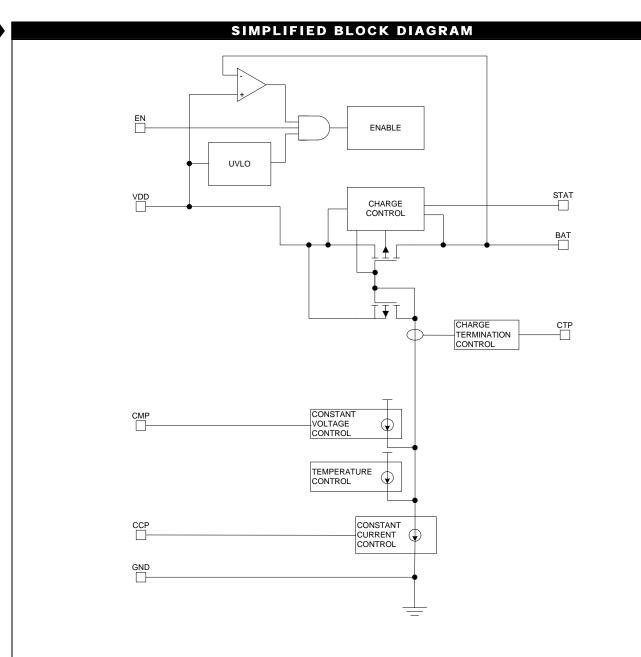
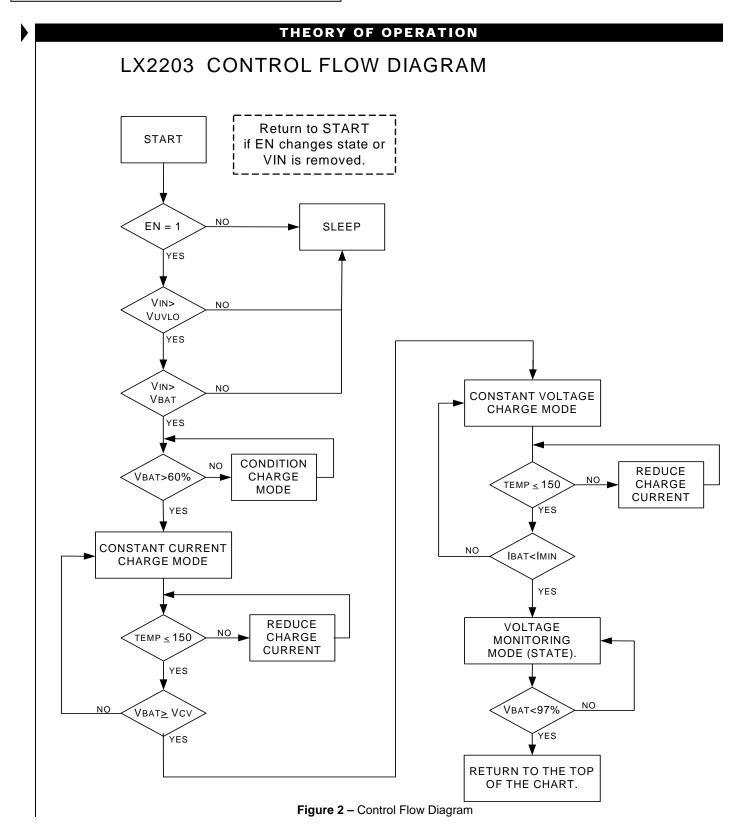


Figure 1 – Simplified Block Diagram



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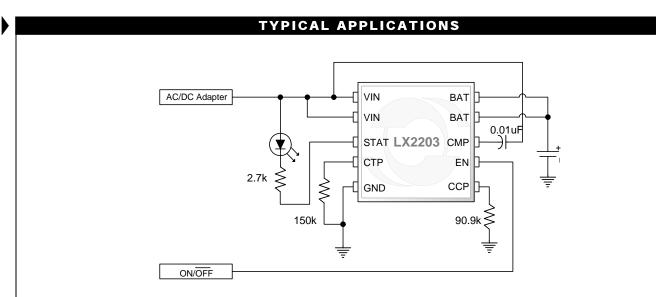


Figure 3 - Basic charger from Wall Adapter with 1A constant current and 50mA termination current

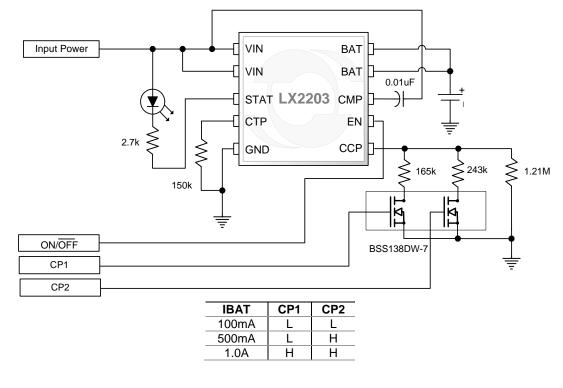


Figure 4 – Wall adapter and USB Charger (1A, 500mA, 100mA, selectable)



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## **APPLICATION NOTE**

#### GENERAL DESCRIPTION

The LX2203 is designed to charge a single cell Lithium Ion (Li-ion) battery using two modes: a constant current mode, where the charge current is held constant and the battery terminal voltage rises to 4.2V; this is followed by constant voltage mode, where the battery voltage is held at 4.2V and the charge current starts to taper off. Once the taper current reaches the charge termination current level the charge cycle is ended and the STAT indicator turns off. If the fully charged battery terminal voltage drops to 4.07V, the battery charge cycle will be restarted.

#### **PROTECTION FEATURES**

<u>Conditioning Current Mode</u> – If the battery terminal voltage is less than 2.65V, the battery charger will reduce the charge current to 5%. This also protects the appliance from overheating by trying to drive the full charging current into a damaged battery.

<u>Under Voltage Lockout</u> – The charge cycle will not start until the VIN voltage rises above 4.2V. Hysteresis prevents chattering on and off.

<u>Thermal Control loop</u> – If the power dissipation of the charger becomes excessive, the charge current will be reduced to prevent the die temperature from getting above  $150^{\circ}$ C. This does not cause the charge cycle to stop.

<u>Reverse current blocking</u> – If Vin is grounded, current will not flow from the battery through the charger. No external blocking diode is required on the input.

<u>Sleep Mode</u> – If the EN pin is a logic low or if Vin is removed, the charger enters a sleep mode where a very low quiescent current prevents drain from the battery.

#### LAYOUT GUIDELINES

• It is important when laying out the LX2203 to place  $10\mu F$  ceramic capacitors close to the VIN and  $V_{BAT}$  IC terminals to filter switching transients.

• It is important to provide a low thermal impedance path from the thermal pad on the bottom of the LX2203 package to the ground plane of the circuit board to maximize the heat dissipation. To minimize charge time it is best not to rely on the thermal control feature as this feature will extend the charging time when activated.

#### **TERMINATION CURRENT PROGRAMMING**

The charge termination current (or minimum taper current) is set by selecting a value for the CTP resistor using the following formula:

$$R_{CTP} = \frac{7500}{I_{TERM}}$$

For example, for a termination current of 50mA set Rctp = 150k. This formula applies to termination currents ranging from 20mA to 500mA.

It is possible to change the termination current to different levels for different charge rates. For a "quick charge", the termination current can be set to about 50% of the constant current level; this charges the battery to about 75% capacity. For a full charge (taking several hours) the termination current can be set to 5% of the constant current level. The circuit below allows switching between the two levels:

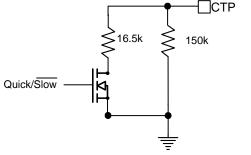


Figure 5 – 500mA or 50mA termination current

#### CONSTANT CHARGE CURRENT PROGRAMMING

The charge current during the constant current charge mode is programmable by controlling the current flowing from the CCP pin. A graph showing this relationship is included in this specification. The CCP pin is regulated to 1.25V when the charger is active. Connecting a resistor from the CTP to ground will produce a CTP current of:

$$I_{CTP} = \frac{1.25}{R_{CCP}}$$

(Continued on next page)

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## **APPLICATION NOTE (CONTINUED)**

CONSTANT CHARGE CURRENT PROGRAMMING (CONTINUED)

The table below lists some popular Constant Current Settings along with the associated CCP pin current and programming resistor:

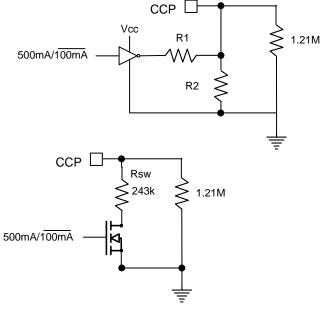
Charge Current	Iccp Current	Rccp Value	
1.0A	13.75µA	90.9K	
500mA	6.22µA	200K	
100mA	1.00µA	1200K	

It is possible to change the constant current setting by changing the Rccp resistor while in charge mode. Since the termination current is independent of the charge current, lowering the constant charge current will increase the charge time, but will not reduce the stored charge in the battery at the charge termination point.

The circuit in **Figure 4** is an example of a battery charger configured to charge at 1A, 500mA or 100mA. The switches are logically controlled and reduce the resistance at the CCP pin when switched in. It is possible to eliminate the MOSFET devices if open drain logic is available.

The logic for the CP1 and CP2 would normally come from the appliance processor which would need to have the capability to communicate over the USB interface. If there is only one power connector and the USB interface is active, the logic could assume the power was coming from the USB bus and not the wall adapter. If the USB interface is active, the USB application will know if the appliance has been enumerated as a high or low load and would set CP1 and CP2 appropriately.

It is possible to change the current programming circuit to drive it directly with CMOS logic, but this requires that the CMOS logic power supply be well regulated (+/-2%). Each switch resistor leg in Figure 4 can be replaced with a two resistor network tied to the output of a CMOS gate.



**Figure 6** – Circuits to provide 100mA and 500mA constant charge currents.

The values of R1 and R2 are selected such that:

 $V_{CC} \times \frac{R_2}{R_1 + R_2} = 1.25$  &  $\frac{R_1 \times R_2}{R_1 + R_2} = R_{SW}$ 

Solving these equations:

$$\mathbf{R}_1 = \frac{\mathbf{V}_{\mathrm{CC}} \times \mathbf{R}_{\mathrm{SW}}}{1.25} \quad \mathbf{\&} \quad \mathbf{R}_2 = \frac{\mathbf{R}_1 \times \mathbf{R}_{\mathrm{SW}}}{\mathbf{R}_1 - \mathbf{R}_{\mathrm{SW}}}$$

For Vcc = 3.3V and Rsw = 243k; R1 = 649k; R2 = 392k. These values should provide charge currents of 100mA and 500mA.

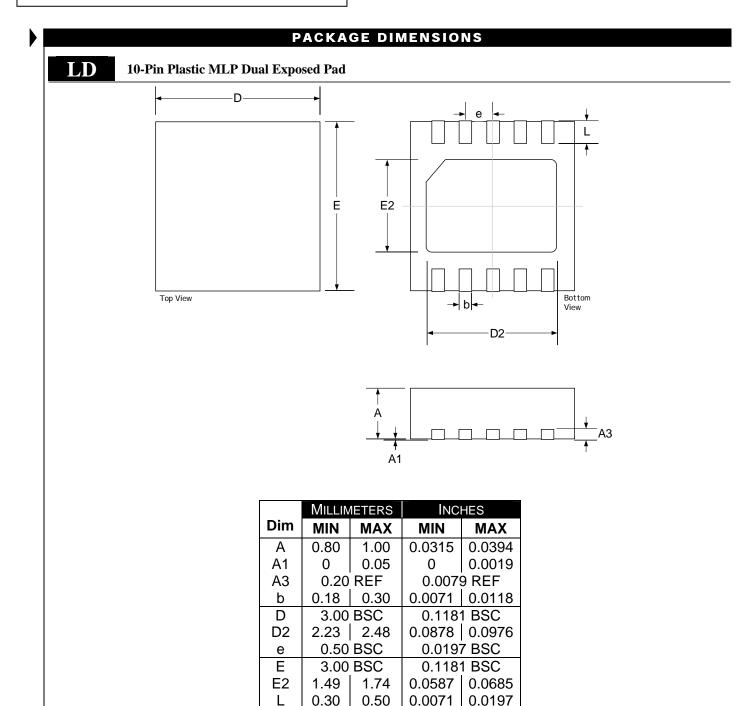
#### **COMPENSATION CAPACITOR**

A compensation capacitor of value 0.01 uF is required between the CMP pin and VIN.



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#### Note:

1. Dimensions do not include mold flash or protrusions; these shall not exceed 0.155mm(.006") on any side. Lead dimension shall not include solder coverage.



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NOTES

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