19-1444; Rev 1; 7/99

NIXIV

MIXIM

+3V to +5.5V, 1.5Mbps RS-232 Receivers in SOT23-5

General Description

The MAX3180-MAX3183 are single RS-232 receivers in a SOT23-5 package for space- and cost-constrained applications requiring minimal RS-232 communications. These devices minimize power and heat dissipation by consuming only 0.5µA supply current from a +3.0V to +5.5V supply voltage. They guarantee true RS-232 performance up to a 1.5Mbps data rate.

The MAX3180/MAX3182 feature a three-state TTL/ CMOS receiver output that is controlled by an EN logic input. The MAX3181/MAX3183 feature an INVALID output that indicates valid RS-232 signals at the receiver input for applications requiring automatic system wakeup. The MAX3180/MAX3181 receivers have a standard inverting output, while the MAX3182/MAX3183 receivers have a noninverting output.

Applications

Set-Top Boxes Diagnostic Ports **Telecommunications** Digital Cameras Networking Equipment Hand-Held Equipment

Features

- ♦ Tiny SOT23-5 Package
- ♦ 0.5µA Supply Current
- ♦ 1.5Mbps Guaranteed Data Rate
- ♦ Meet EIA/TIA-232 and V.28/V.24 Specifications Down to $V_{CC} = +3.0V$
- ♦ INVALID Output Indicates Valid RS-232 Signal at Receiver Input (MAX3181/MAX3183)
- ♦ Three-State TTL/CMOS Receiver Output (MAX3180/MAX3182)
- Noninverting RS-232 Output (MAX3182/MAX3183)

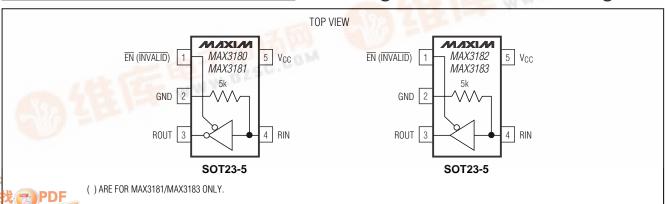
Ordering Information

PART	TEMP. RANGE	PIN- PACKAGE	TOP MARK
MAX3180EUK-T	-40°C to +85°C	5 SOT23-5	ADKF
MAX3181EUK-T	-40°C to +85°C	5 SOT23-5	ADKG
MAX3182EUK-T	-40°C to +85°C	5 SOT23-5	ADKH
MAX3183EUK-T	-40°C to +85°C	5 SOT23-5	ADKI

Selector Guide

PART	PACKAGE	SUPPLY CURRENT (µA)	EN INPUT	INVALID OUTPUT	INVERTING ROUT	NONINVERTING ROUT
MAX3180	SOT23-5	0.5	1	_	✓	_
MAX3181	SOT23-5	0.5	_	1	✓	_
MAX3182	SOT23-5	0.5	✓	_	_	1
MAX3183	SOT23-5	0.5	_	1		

Pin Configurations/Functional Diagrams



Maxim Integrated Products 1

ABSOLUTE MAXIMUM RATINGS

V _{CC} to GND	0.3V to +6V
RIN to GND	±25V
EN, ROUT, INVALID to GND	
Continuous Power Dissipation (TA = +	+70°C)
SOT23-5 (derate 7.1mW/°C above -	+70°C)571mW

Operating Temperature Range	40°C to +85°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10sec)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{CC} = +3.0V \text{ to } +5.5V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +5.0V \text{ and } T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
DC CHARACTERISTICS	<u> </u>		1				
Supply Voltage	Vcc		3.0		5.5	V	
Supply Current	Icc	V _{CC} = 3.3V or 5V, RIN = V _{CC} or GND, no load		0.5	5	μΑ	
LOGIC INPUT (EN)	'						
Logic Threshold Low	VIL				0.8	V	
Logic Threshold High	VIH	V _{CC} = 3.3V	2.0			V	
Logic Tilleshold High	VIH	VCC = 5.0V	2.4			V	
Leakage Current	IEN			±0.01	±1.0	μΑ	
LOGIC OUTPUT	<u>'</u>						
INVALID Output Voltage Low	VIOL	I _{SINK} = 1.6mA			0.4	V	
INVALID Output Voltage High	Vioh	ISOURCE = 1.0mA	VCC - 0.6			V	
RECEIVER INPUT			L			I.	
Input Voltage Range	V _{RIN}		-25		25	V	
Input Threshold Low	V _{ITL}	VCC = 3.3V	0.6	1.2		V	
		V _{CC} = 5.0V	0.8	1.5		v	
Input Threshold High	V _{ITH}	V _{CC} = 3.3V		1.5	2.4	V	
input mresnoid riign		VCC = 5.0V		1.8	2.7		
Input Hysteresis	VHYST			300		mV	
RIN Threshold to INVALID	VITOH	Positive threshold			2.7	V	
Output High		Negative threshold	-2.7			v	
RIN Threshold to INVALID Output Low	VITOL		-0.3		0.3	V	
Input Resistance	RRIN		3	5	7	kΩ	
RECEIVER OUTPUT	•		•			•	
Output Leakage Current	IROUT	Receiver disabled		±0.05	±10	μΑ	
Output Voltage Low	VoL	ISINK = 1.6mA			0.4	V	
Output Voltage High	VoH	ISOURCE = 1.0mA	Vcc - 0.6	Vcc - 0.1		V	

ELECTRICAL CHARACTERISTICS (continued)

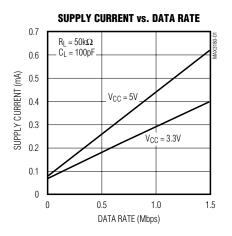
 $(V_{CC} = +3.0 \text{V to } +5.5 \text{V}, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted.}$ Typical values are at $V_{CC} = +5.0 \text{V}$ and $T_A = +25 ^{\circ}\text{C}$.) (Note 1)

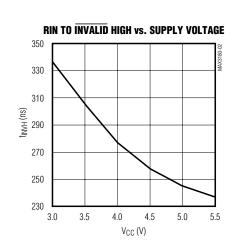
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
TIMING CHARACTERISTICS						
Maximum Data Rate		$C_L = 50pF$	1.5			Mbps
Receiver Propagation Delay, High-to-Low	t _{PHL}	RIN to ROUT; C _L = 150pF	0.15			μs
Receiver Propagation Delay, Low-to-High		RIN to ROUT; C _L = 150pF	0.15		μs	
Receiver Skew t _{RS}		tphL - tpLh , Figure 1		50		ns
Receiver Output Enable Time tROE				200		ns
Receiver Output Disable Time tROD				200		ns
Receiver Positive or Negative Threshold to INVALID High				250		ns
Receiver Positive or Negative Threshold to NVALID Low				30		μs

Note 1: Specifications are 100% tested at $T_A = +25$ °C. Limits over temperature are guaranteed by design.

Typical Operating Characteristics

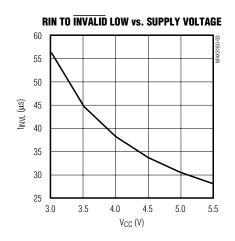
($V_{CC} = +5V$, $T_A = +25$ °C, unless otherwise noted.)

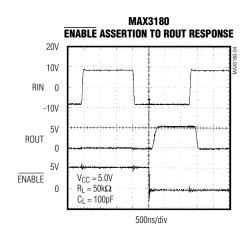




Typical Operating Characteristics (continued)

(VCC = +5V, TA = +25°C, unless otherwise noted.)





Pin Description

PIN		NA	ME		FUNCTION
	MAX3180	MAX3181	MAX3182	MAX3183	FUNCTION
1	ĒN	-	ĒN	-	Receiver Output Enable
'	_	ĪNVALID	-	ĪNVALID	Output of the Valid Input Detector
2	GND	GND	GND	GND	Ground
3	ROUT	ROUT	-	-	Inverting Receiver Output
3	_	-	ROUT	ROUT	Noninverting Receiver Output
4	RIN	RIN	RIN	RIN	Receiver Input
5	V _C C	V _C C	Vcc	Vcc	Supply Voltage

Detailed Description

The MAX3180–MAX3183 are EIA/TIA-232 and V.28/V.24 communications receivers that convert RS-232 signals to CMOS logic levels. The devices operate on a supply voltage of +3V to +5.5V and have a 1.5Mbps data rate capability. They achieve a 0.5µA typical supply current. The MAX3180/MAX3182 have a receiver enable control (EN), while the MAX3181/MAX3183 contain a signal invalid output (INVALID). The MAX3180/MAX3181 invert the ROUT signal relative to RIN. The MAX3182/MAX3183 are not inverted. The devices come in SOT23-5 packages.

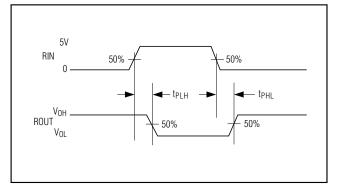


Figure 1. Receiver Propagation-Delay Timing

Signal Invalid Detector

If no valid signal levels appear on RIN for 30µs (typ), INVALID goes low. This event typically occurs if the RS-232 cable is disconnected or if the connected peripheral transmitter is turned off. INVALID goes high when a valid level is applied to the RS-232 receiver input. Figure 2 shows the input levels and timing diagram for INVALID operation.

Enable Input

The MAX3180/MAX3182 feature an enable input. Drive \overline{EN} high to force ROUT into a high-impedance state. In this state, the devices ignore incoming RS-232 signals. Drive \overline{EN} low for normal operation.

Power-Supply Decoupling

In most circumstances, a $0.1\mu F$ V_{CC} bypass capacitor is adequate for power-supply decoupling. Connect the bypass capacitor as close to the IC as possible.

Chip Information

TRANSISTOR COUNT: 41

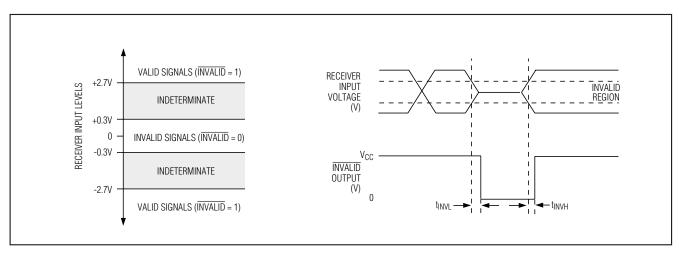
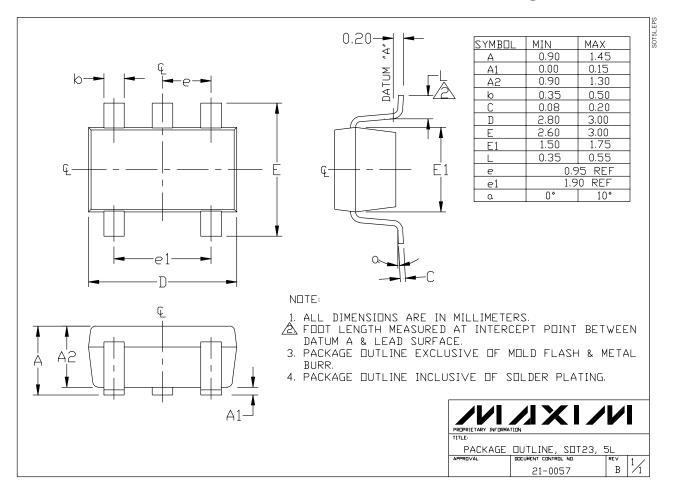


Figure 2. Input Levels and INVALID Timing

Package Information



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