Triple 2-Input Exclusive OR/ Exclusive NOR Gate

The MC10H107 is a triple 2–input exclusive OR/NOR gate. This MECL 10H part is a functional/pinout duplication of the standard MECL 10K family part, with 100% improvement in propagation delay, and no increase in power–supply current.

- Propagation Delay, 1.0 ns Typical
- Power Dissipation 35 mW/Gate Typical (same as MECL 10K)
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10K-Compatible

MAXIMUM RATINGS

| Characteristic | Symbol | Rating | Unit |
|--|------------------|----------------------------|---------|
| Power Supply ($V_{CC} = 0$) | VEE | -8.0 to 0 | Vdc |
| Input Voltage ($V_{CC} = 0$) | VI | 0 to V _{EE} | Vdc |
| Output Current — Continuous — Surge | lout | 50 100 | mA |
| Operating Temperature Range | TA | 0 to +75 | °C |
| Storage Temperature Range — Plastic — Ceramic | T _{stg} | –55 to +150 –55 to +165 | °C ℃ |

ELECTRICAL CHARACTERISTICS (V_{EE} = -5.2 V ±5%) (See Note)

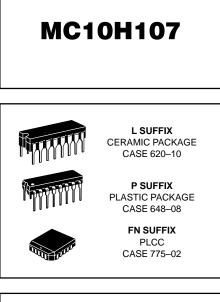
| | | 0 ° | | 25 ° | | 75 ° | | |
|----------------------|------------------|------------|-------|-------------|-------|-------------|--------|------|
| Characteristic | Symbol | Min | Max | Min | Max | Min | Max | Unit |
| Power Supply Current | ١ _E | | 31 | I | 28 | | 31 | mA |
| Input Current High | l _{inH} | | 425 | I | 265 | Ι | 265 | μΑ |
| Input Current Low | l _{inL} | 0.5 | | 0.5 | - | 0.3 | — | μΑ |
| High Output Voltage | ∨он | -1.02 | -0.84 | -0.98 | -0.81 | -0.92 | -0.735 | Vdc |
| Low Output Voltage | VOL | -1.95 | -1.63 | -1.95 | -1.63 | -1.95 | -1.60 | Vdc |
| High Input Voltage | VIH | -1.17 | -0.84 | -1.13 | -0.81 | -1.07 | -0.735 | Vdc |
| Low Input Voltage | VIL | -1.95 | -1.48 | -1.95 | -1.48 | -1.95 | -1.45 | Vdc |

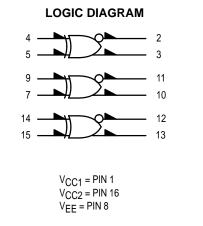
AC PARAMETERS

| Propagation Delay | ^t pd | 0.4 | 1.5 | 0.4 | 1.6 | 0.4 | 1.7 | ns |
|-------------------|-----------------|-----|-----|-----|-----|-----|-----|----|
| Rise Time | t _r | 0.5 | 1.5 | 0.5 | 1.6 | 0.5 | 1.7 | ns |
| Fall Time | t _f | 0.5 | 1.5 | 0.5 | 1.6 | 0.5 | 1.7 | ns |

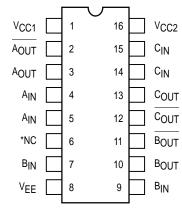
NOTE:

Each MECL 10H series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 linear fpm is maintained. Outputs are terminated through a 50–ohm resistor to -2.0 volts.





DIP PIN ASSIGNMENT

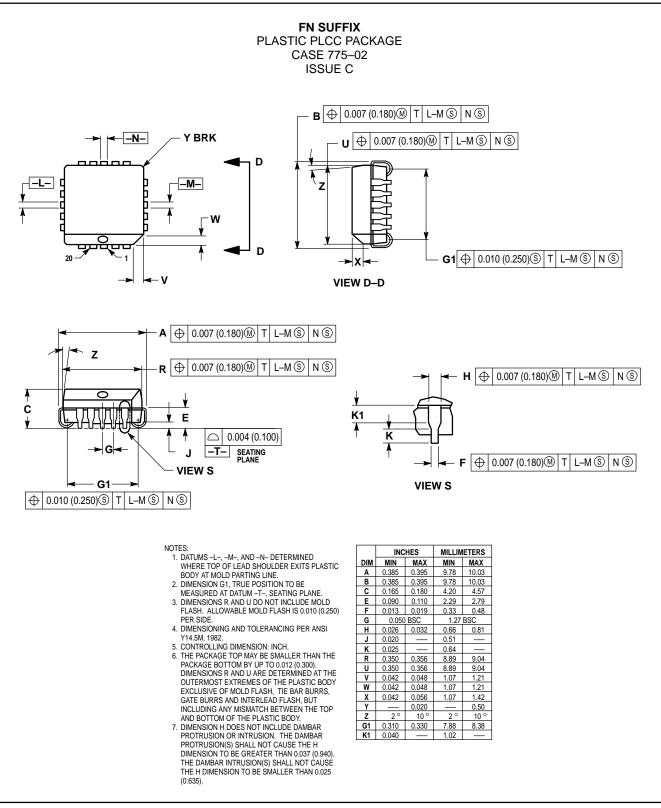


*NC = No Connection

Pin assignment is for Dual–in–Line Package. For PLCC pin assignment, see the Pin Conversion Tables on page 6–11 of the Motorola MECL Data Book (DL122/D).

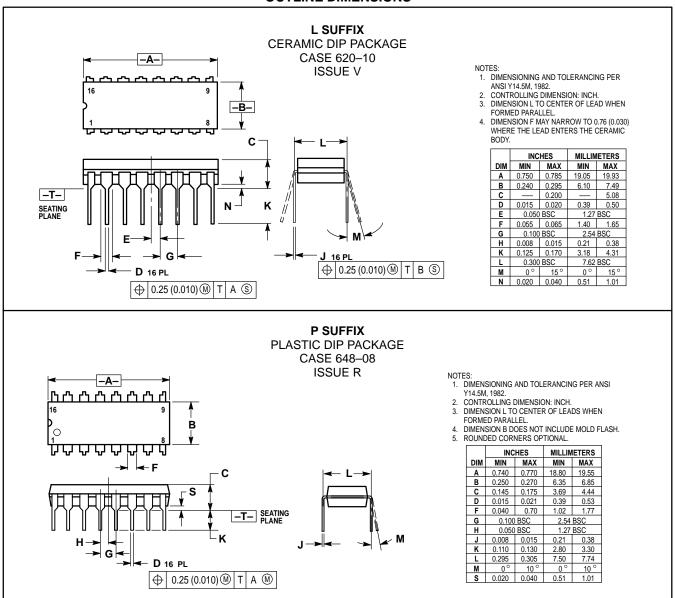


OUTLINE DIMENSIONS



MC10H107

OUTLINE DIMENSIONS



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