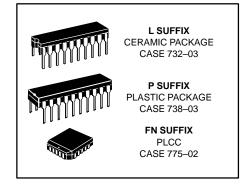
Quad Bus Driver/Receiver with Transmit and Receiver Latches

The MC10H334 is a Quad Bus Driver/Receiver with transmit and receiver latches. When disabled, (OE = high) the bus outputs will fall to -2.0 V. Data to be transmitted or received is passed through its respective latch when the respective latch enable (DLE and RLE) is at a low level. Information is latched on the positive transition of DLE and RLE. The parameters specified are with 25 Ω loading on the bus drivers and 50 Ω loads on the receivers.

- Propagation Delay, 1.6 ns Typical Data-to-Output
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10K-Compatible

MC10H334



MAXIMUM RATINGS

Characteristic	Symbol	Rating	Unit
Power Supply (V _{CC} = 0)	V _{EE}	-8.0 to 0	Vdc
Input Voltage (V _{CC} = 0)	VI	0 to VEE	Vdc
Output Current — Continuous — Surge	lout	50 100	mA
Operating Temperature Range	T _A	0 to +75	°C
Storage Temperature Range — Plastic — Ceramic	T _{stg}	-55 to +150 -55 to +165	°C °C

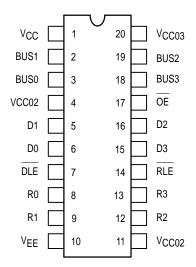
ELECTRICAL CHARACTERISTICS (VEE = -5.2 V ±5%) (See Note)

		0	10	2	5°	7	′5°	
Characteristic	Symbol	Min	Max	Min	Max	Min	Max	Unit
Power Supply Current	ΙΕ		161		161	1	161	mA
Input Current High Pins 5,6,15,16 Pins 7,14 Pin 17	^l inH	111	397 460 520		273 297 357	111	273 297 357	μА
Input Current Low	l _{inL}	0.5	_	0.5	-	0.3	_	μΑ
High Output Voltage	Vон	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
Low Output Voltage	V _{OL}	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
High Input Voltage	VIH	-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
Low Input Voltage	V _{IL}	-1.95	-1.48	-1.95	-1.48	-1.95	-1.45	Vdc

AC PARAMETERS

Propagation Delay <u>Data</u> —to—Bus Output <u>DLE</u> —to—Bus Output OE—to—Bus Output <u>Bus</u> —to—R0 RLE—to—R0 Data—to—Receiver R0	^t pd	0.5 1.0 0.5 0.5 0.5 1.0	2.5 2.7 2.5 1.9 2.1 3.8	0.5 1.0 0.5 0.5 0.5 1.0	2.5 2.7 2.5 1.9 2.1 3.8	0.5 1.0 0.5 0.5 0.5 1.0	2.5 2.7 2.5 1.9 2.1 3.8	ns
Rise Time	t _r	0.5	2.2	0.5	2.2	0.5	2.2	ns
Fall Time	t _f	0.5	2.2	0.5	2.2	0.5	2.2	ns

DIP & PLCC PIN ASSIGNMENT



Pin assignment is for Dual-in-Line Package. For PLCC pin assignment, see the Pin Conversion Tables on page 6–11 of the Motorola MECL Data Book (DL122/D).

NOTE:

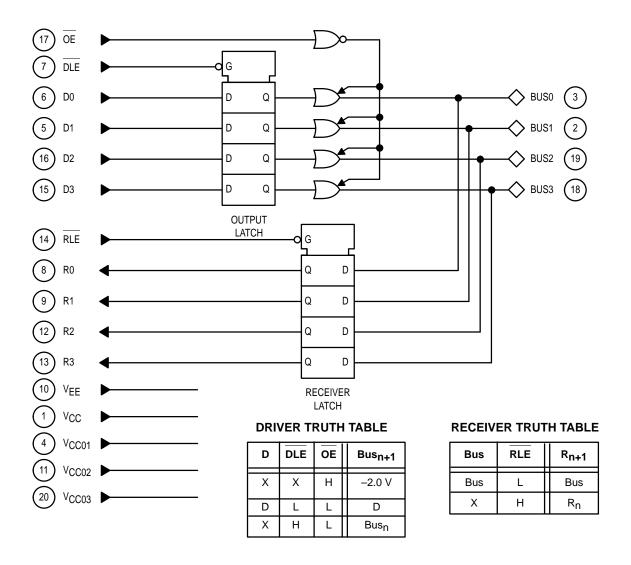
Each MECL 10H series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained. Receiver outputs are terminated through a 50-ohm resistor to -2.0 volts dc. Bus outputs are terminated through a 25-ohm resistor to -2.0 volts dc.

MOTOROLA

3/93

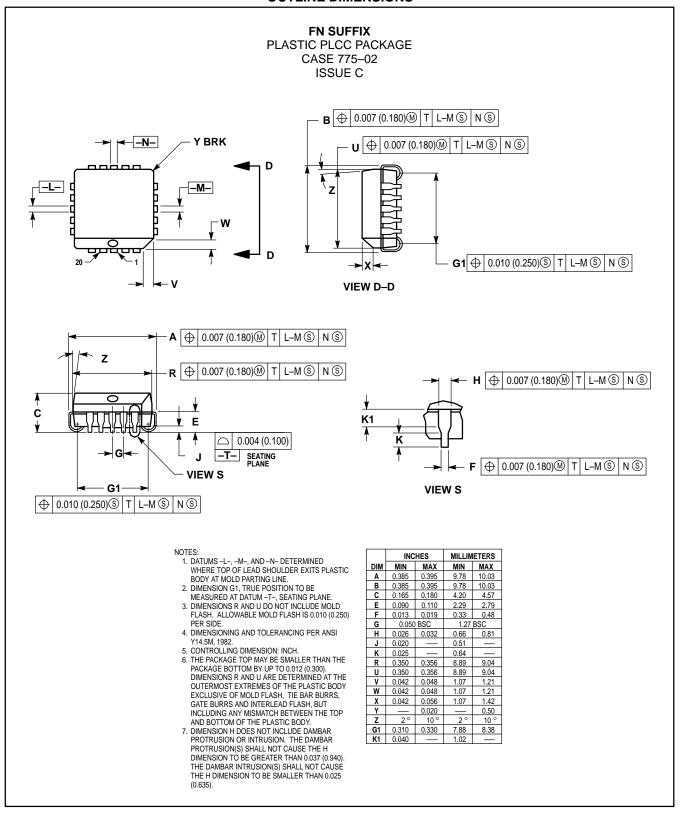
REV 5

LOGIC DIAGRAM



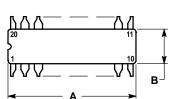
MOTOROLA 2–38

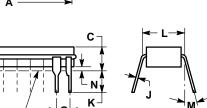
OUTLINE DIMENSIONS



OUTLINE DIMENSIONS

L SUFFIX CERAMIC DIP PACKAGE CASE 732-03 ISSUE E

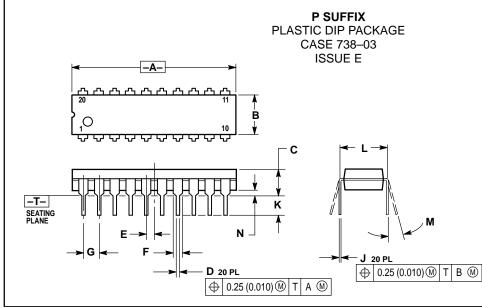




NOTES:

- LEADS WITHIN 0.010 DIAMETER, TRUE
 POSITION AT SEATING PLANE, AT MAXIMUM
- 2. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL 3. DIMENSIONS A AND B INCLUDE MENISCUS.

	INCHES				
DIM	MIN MAX				
Α	0.940	0.990			
В	0.260	0.295			
С	0.150	0.200			
D	0.015	0.022			
F	0.055	0.065			
G	0.100 BSC				
H	0.020	0.050			
J	0.008	0.012			
K	0.125	0.160			
L	0.300 BSC				
M	0°	15°			
N	0.010	0.040			



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
- 4. DIMENSION B DOES NOT INCLUDE MOLD

	INC	HES	MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	1.010	1.070	25.66	27.17	
В	0.240	0.260	6.10	6.60	
С	0.150	0.180	3.81	4.57	
D	0.015	0.022	0.39	0.55	
E	0.050	BSC	1.27 BSC		
H	0.050	0.070	1.27	1.77	
G	0.100 BSC		2.54 BSC		
J	0.008	0.015	0.21	0.38	
K	0.110	0.140	2.80	3.55	
L	0.300	BSC	7.62 BSC		
М	0°	15°	0°	15°	
N	0.020	0.040	0.51	1.01	

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