Quad TTL-to-ECL Translator with ECL Strobe

The MC10H424 is a Quad TTL-to-ECL translator with an ECL strobe. Power supply requirements are ground, +5.0 volts, and -5.2 volts.

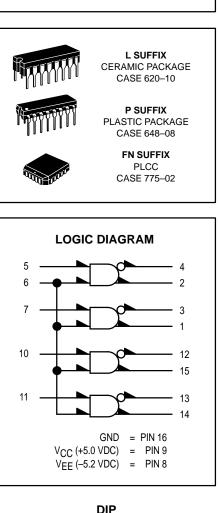
- Propagation Delay, 1.5 ns Typical
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10K Compatible

MAXIMUM RATINGS

Characteristic	Symbol	Rating	Unit
Power Supply ($V_{CC} = 5.0 V$)	V _{EE}	-8.0 to 0	Vdc
Power Supply ($V_{EE} = -5.2 \text{ V}$)	V _{CC}	0 to +7.0	Vdc
Input Voltage (ECL)	VI	0 to V _{EE}	Vdc
Input Voltage (TTL)	VI	0 to V _{CC}	Vdc
Output Current — Continuous — Surge	lout	50 100	mA
Operating Temperature Range	т _А	0 to +75	°C
Storage Temperature Range — Plastic — Ceramic	T _{stg}	–55 to +150 –55 to +165	°C

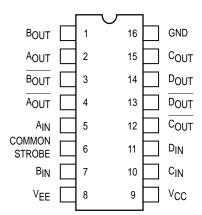
ELECTRICAL CHARACTERISTICS (V_{EE} = $-5.2 \text{ V} \pm 5\%$, V_{CC} = $5.0 \text{ V} \pm 5.0\%$)

		0 °		25°		75°		
Characteristic	Symbol	Min	Max	Min	Max	Min	Max	Unit
Negative Power Supply Drain Current	Ē	I	72	I	66	I	72	mAdc
Positive Power Supply	ICCH		16		16	I	18	mAdc
Drain Current	ICCL	—	25	_	25		25	mAdc
Reverse Current Pin 5,7,10,11	۱ _R	-	50	-	50	-	50	μAdc
Forward Current Pin 5,7,10,11	١ _F	—	-3.2	—	-3.2	-	-3.2	mAdc
Input HIGH Current Pin 6	l _{inH}	_	450	_	310	_	310	μAdc
Input LOW Current Pin 6	l _{inL}	0.5	_	0.5	—	0.3	—	μAdc
Input Breakdown Voltage	V _{(BR)in}	5.5	_	5.5	—	5.5	_	Vdc
Input Clamp Voltage	VI	-	-1.5	_	-1.5	_	-1.5	Vdc
High Output Voltage	VOH	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
Low Output Voltage	VOL	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
High Input Voltage Pin 5,7,10,11	∨ _{IH}	2.0	—	2.0	—	+2.0	—	Vdc
Low Input Voltage Pin 5,7,10,11	VIL	_	0.8	_	0.8	1	0.8	Vdc
High Input Voltage Pin 6	VIH	-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
Low Input Voltage Pin 6	VIL	-1.95	-1.48	-1.95	-1.48	-1.95	-1.45	Vdc



MC10H424

PIN ASSIGNMENT



Pin assignment is for Dual–in–Line Package. For PLCC pin assignment, see the Pin Conversion Tables on page 6–11 of the Motorola MECL Data Book (DL122/D).



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Propaga- tion Delay Data Strobe	^t pd	0.5 0.5	2.2 2.2	0.5 0.5	2.3 2.3	0.5 0.5	2.4 2.4	ns
Rise Time	tr	0.5	2.0	0.5	2.0	0.5	2.2	ns
Fall Time	t _f	0.5	2.0	0.5	2.0	0.5	2.2	ns

AC PARAMETERS

NOTE:

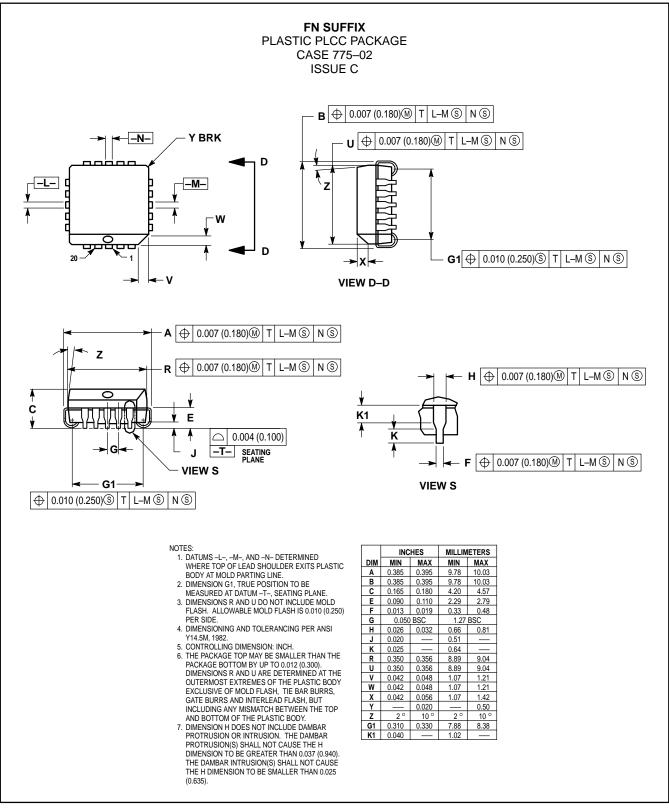
Each MECL 10H series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained. Outputs are terminated through a 50–ohm resistor to -2.1 volts.

APPLICATIONS INFORMATION

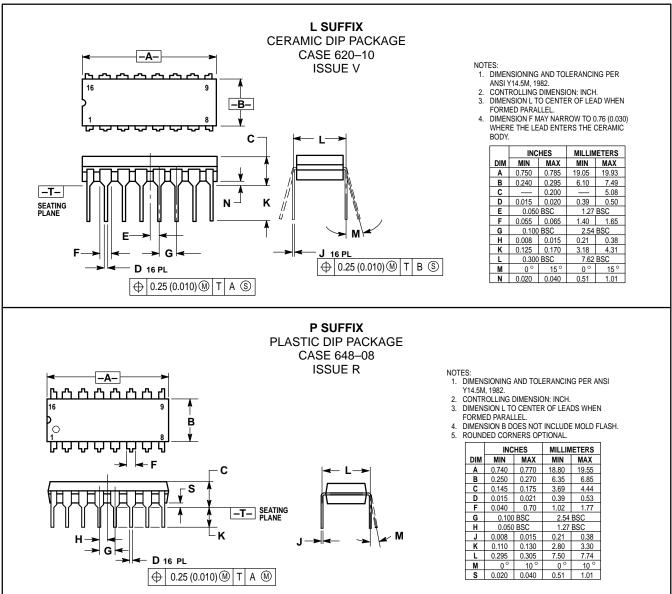
The MC10H424 has TTL–compatible inputs, an ECL strobe and MECL complementary open–emitter outputs that allow use as an inverting/non–inverting translator or as a differential line driver. When the common strobe input is at the low–logic level, it forces all true outputs to a MECL low–logic state and all inverting outputs to a MECL high-logic state.

An advantage of this device is that TTL–level information can be transmitted differentially, via balanced twisted pair lines, to MECL equipment, where the signal can be received by the MC10H115 or MC10H116 differential line receivers.

OUTLINE DIMENSIONS



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