

FAIRCHILD

SEMICONDUCTOR®

April 2003

ISL9R860PF2**8A, 600V Stealth™ Diode****General Description**

The ISL9R860PF2 is a Stealth™ diode optimized for low loss performance in high frequency hard switched applications. The Stealth™ family exhibits low reverse recovery current (I_{RRM}) and exceptionally soft recovery under typical operating conditions.

This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low I_{RRM} and short t_a phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the Stealth™ diode with an SMPS IGBT to provide the most efficient and highest power density design at lower cost.

Formerly developmental type TA49409.

Features

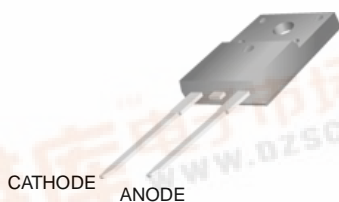
- Soft Recovery $t_b / t_a > 1.2$
- Fast Recovery $t_{rr} < 25ns$
- Operating Temperature $150^{\circ}C$
- Reverse Voltage 600V
- Internally Isolated 1kV
- Avalanche Energy Rated

Applications

- Switch Mode Power Supplies
- Hard Switched PFC Boost Diode
- UPS Free Wheeling Diode
- Motor Drive FWD
- SMPS FWD
- Snubber Diode

Package

TO-220F

**Symbol****Device Maximum Ratings** $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{RRM}	Peak Repetitive Reverse Voltage	600	V
V_{RWM}	Working Peak Reverse Voltage	600	V
V_R	DC Blocking Voltage	600	V
$I_{F(AV)}$	Average Rectified Forward Current ($T_C = 75^{\circ}C$)	8	A
I_{FRM}	Repetitive Peak Surge Current (20kHz Square Wave)	16	A
I_{FSM}	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)	100	A
P_D	Power Dissipation	26	W
E_{AVL}	Avalanche Energy (1A, 40mH)	20	mJ
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to 150	$^{\circ}C$
T_L	Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10s	300	$^{\circ}C$

CAUTION: Stresses above those listed in "Device Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Package Marking and Ordering Information

Device Marking	Device	Package	Tape Width	Quantity
R860PF2	ISL9R860PF2	TO-220F	N/A	50 Units

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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Off State Characteristics

I_R	Instantaneous Reverse Current	$V_R = 600\text{V}$	$T_C = 25^\circ\text{C}$	-	-	100	μA
			$T_C = 125^\circ\text{C}$	-	-	1.0	mA

On State Characteristics

V_F	Instantaneous Forward Voltage	$I_F = 8\text{A}$	$T_C = 25^\circ\text{C}$	-	2.0	2.4	V
			$T_C = 125^\circ\text{C}$	-	1.6	2.0	V

Dynamic Characteristics

C_J	Junction Capacitance	$V_R = 10\text{V}, I_F = 0\text{A}$	-	30	-	pF
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Switching Characteristics

t_{rr}	Reverse Recovery Time	$I_F = 1\text{A}, di_F/dt = 100\text{A}/\mu\text{s}, V_R = 30\text{V}$	-	18	25	ns
		$I_F = 8\text{A}, di_F/dt = 100\text{A}/\mu\text{s}, V_R = 30\text{V}$	-	21	30	ns
t_{rr}	Reverse Recovery Time	$I_F = 8\text{A},$	-	28	-	ns
I_{RRM}	Maximum Reverse Recovery Current	$di_F/dt = 200\text{A}/\mu\text{s},$	-	3.2	-	A
Q_{RR}	Reverse Recovery Charge	$V_R = 390\text{V}, T_C = 25^\circ\text{C}$	-	50	-	nC
t_{rr}	Reverse Recovery Time	$I_F = 8\text{A},$	-	77	-	ns
S	Softness Factor (t_b/t_a)	$di_F/dt = 200\text{A}/\mu\text{s},$	-	3.7	-	
I_{RRM}	Maximum Reverse Recovery Current	$V_R = 390\text{V},$	-	3.4	-	A
Q_{RR}	Reverse Recovery Charge	$T_C = 125^\circ\text{C}$	-	150	-	nC
t_{rr}	Reverse Recovery Time	$I_F = 8\text{A},$	-	53	-	ns
S	Softness Factor (t_b/t_a)	$di_F/dt = 600\text{A}/\mu\text{s},$	-	2.5	-	
I_{RRM}	Maximum Reverse Recovery Current	$V_R = 390\text{V},$	-	6.5	-	A
Q_{RR}	Reverse Recovery Charge	$T_C = 125^\circ\text{C}$	-	195	-	nC
di_M/dt	Maximum di/dt during t_b		-	500	-	$\text{A}/\mu\text{s}$

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance Junction to Case		-	-	4.8	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	TO-220F	-	-	70	$^\circ\text{C}/\text{W}$

Typical Performance Curves

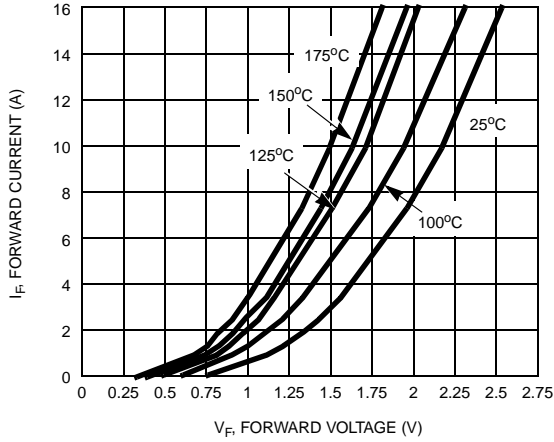


Figure 1. Forward Current vs Forward Voltage

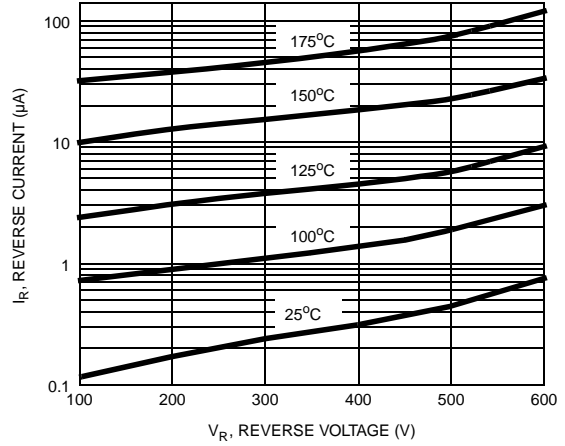


Figure 2. Reverse Current vs Reverse Voltage

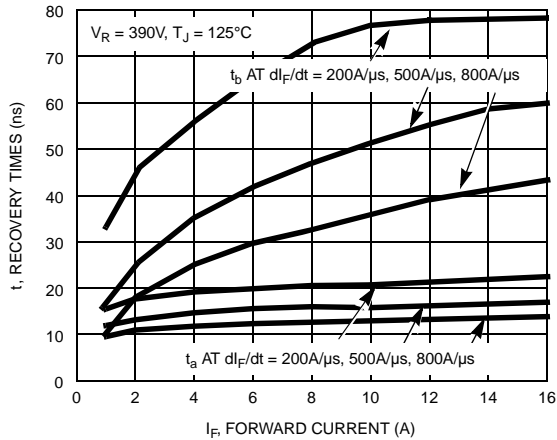


Figure 3. t_a and t_b Curves vs Forward Current

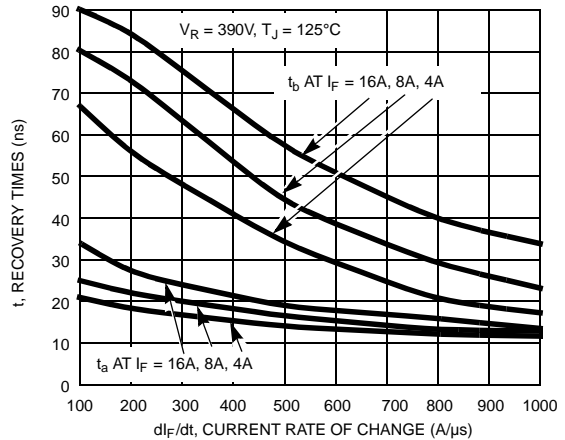


Figure 4. t_a and t_b Curves vs di_F/dt

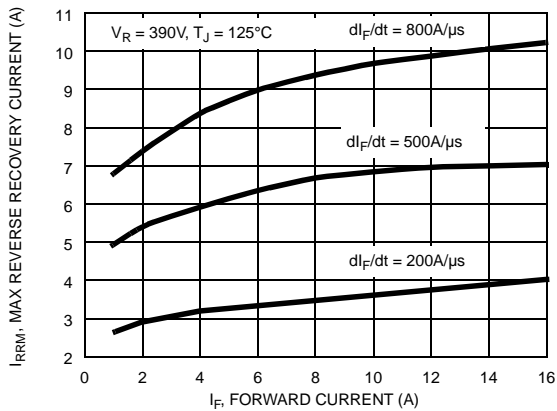


Figure 5. Maximum Reverse Recovery Current vs Forward Current

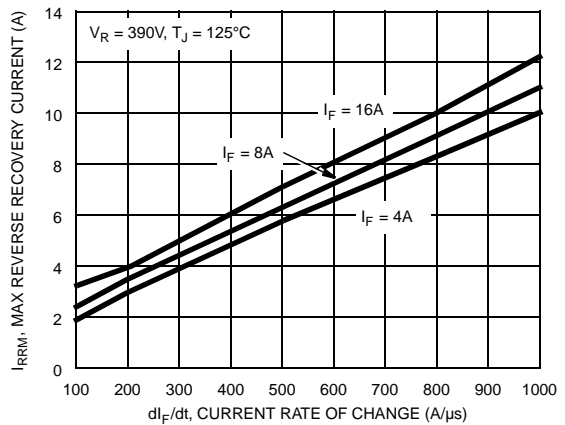


Figure 6. Maximum Reverse Recovery Current vs di_F/dt

Typical Performance Curves (Continued)

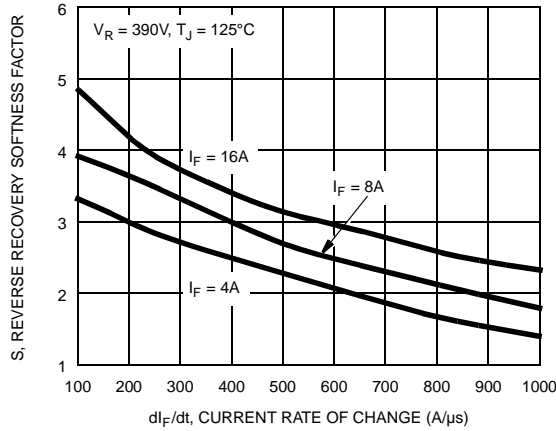


Figure 7. Reverse Recovery Softness Factor vs dI_F/dt

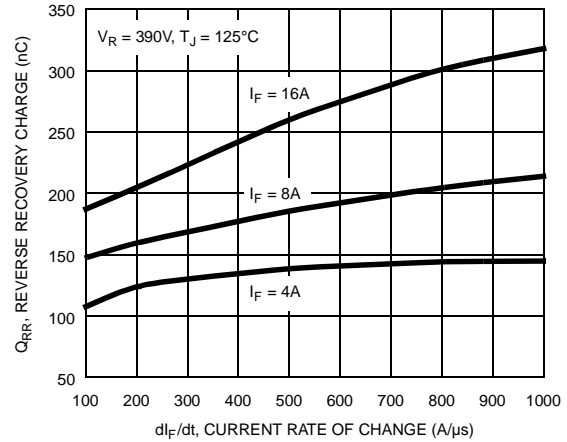


Figure 8. Reverse Recovery Charge vs dI_F/dt

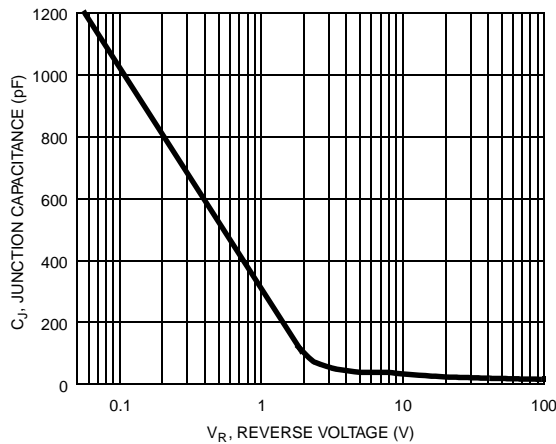


Figure 9. Junction Capacitance vs Reverse Voltage

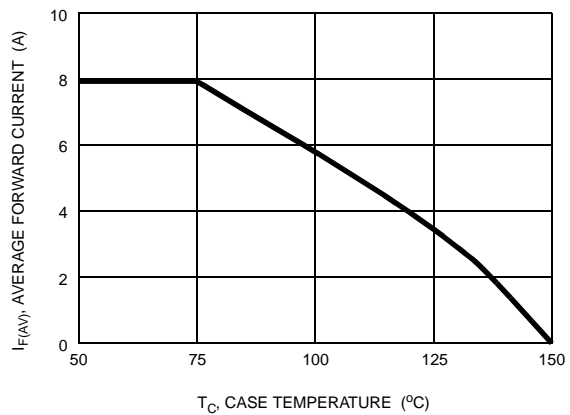


Figure 10. DC Current Derating Curve

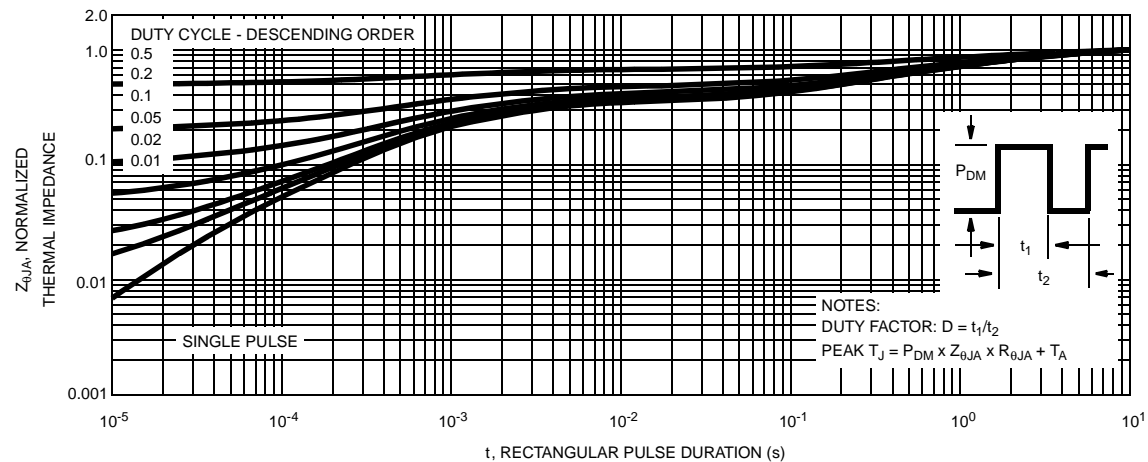


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuits and Waveforms

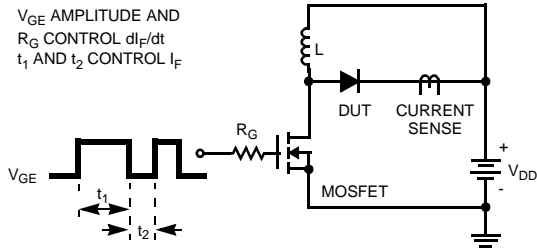


Figure 12. t_{rr} Test Circuit

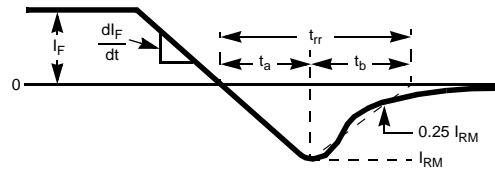


Figure 13. t_{rr} Waveforms and Definitions

$I = 1A$
 $L = 40mH$
 $R < 0.1\Omega$
 $V_{DD} = 50V$
 $E_{AVL} = 1/2LI^2 [V_{R(AVL)}/(V_{R(AVL)} - V_{DD})]$
 $Q_1 = IGBT (BV_{CES} > DUT V_{R(AVL)})$

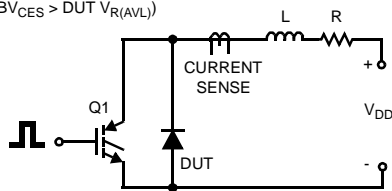


Figure 14. Avalanche Energy Test Circuit

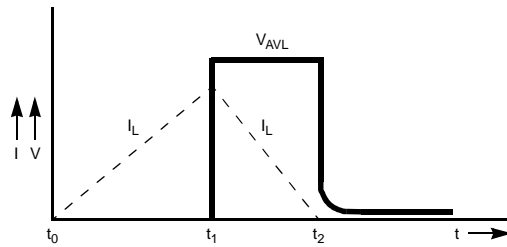


Figure 15. Avalanche Current and Voltage Waveforms

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CoolFET TM	FAST ^r TM	MicroFET TM	PowerTrench [®]	SuperSOT TM -6
CROSSVOLT TM	FRFET TM	MicroPak TM	QFET TM	SuperSOT TM -8
DOME TM	GlobalOptoisolator TM	MICROWIRE TM	QS TM	SyncFET TM
EcoSPARK TM	GTO TM	MSX TM	QT Optoelectronics TM	TinyLogic [®]
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Across the board. Around the world. TM		OCXPro TM	RapidConnect TM	UltraFET [®]
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Definition of Terms

Datasheet Identification	Product Status	Definition
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Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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