

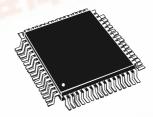
## **STV2310**

## Multistandard TV Digital Video Decoder with Adaptive Comb Filter and RGB/YCrCb Input

**DATABRIEF** 

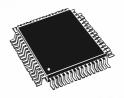
## **MAIN FEATURES**

- **■** Worldwide TV Standards Compatible
- Automatic NTSC/PAL/SECAM Digital Chroma Decoder
- NTSC/PAL Adaptive 4H/2D Comb Filter
- VBI Data Slicer for Teletext, Closed Caption, WSS and other systems
- Analog RGB/Fast Blanking Capture and Insertion in YCrCb Output Flow (SCART legacy)
- Analog YCrCb inputs with Tint Control
- 10-bit, 30-MSPS A/D Converter for Y/CVBS input
- 8-bit, 30-MSPS A/D Converter for C and RGB/ CrCb inputs
- Hue control and automatic flesh control for NTSC CVBS/YC signals
- Programmable Horizontal Scaling (x0.25 to x4 Scaling Factor) and Panorama Vision
- Copy-Protection System compatible
- H and V Synchronisation Processing that is robust to non-standard sources such as VCR, and to weak and noisy signals
- 8-bit Pixel Output Interface Line-Locked ITU-R BT\_656/601 or square pixel YCrCb outputs
- Single System Clock for all Video Input Formats
- Two-wire I<sup>2</sup>C Bus Interface up to 400 kHz
- Typical Power Consumption: 550 mW
- Power Supply: 1.8 V and 3.3 V



TQFP64 14x14x1.4 mm (Thin Quad Flat Package)

ORDER CODE: STV2310D/DT



TQFP64 10x10x1.4 mm (Thin Quad Flat Package)

ORDER CODE: STV2310SD/SDT

The STV2310 is a high-quality front-end video circuit for processing all analog NTSC/PAL/SECAM standards into a 4:2:2 YCrCb digital video format ,as well as conventional analog RGB or YCrCb signals. The STV2310 is programmable through an I<sup>2</sup>C interface.

The STV2310 provides a cost-effective solution for digitized TV, LCD TV/monitors, digital TV, STB, video surveillance/security, video conferencing, video capturing devices and PC video card.

It can be used as a stand-alone chip working with third-party products, as a companion chip to the TV processor STV3500, STV3600 for digitized 100-Hz/ProScan CRT TVs, or as a companion chip to the TV processor STV3550 for LCD-TVs.



Revision 1.0 1/3

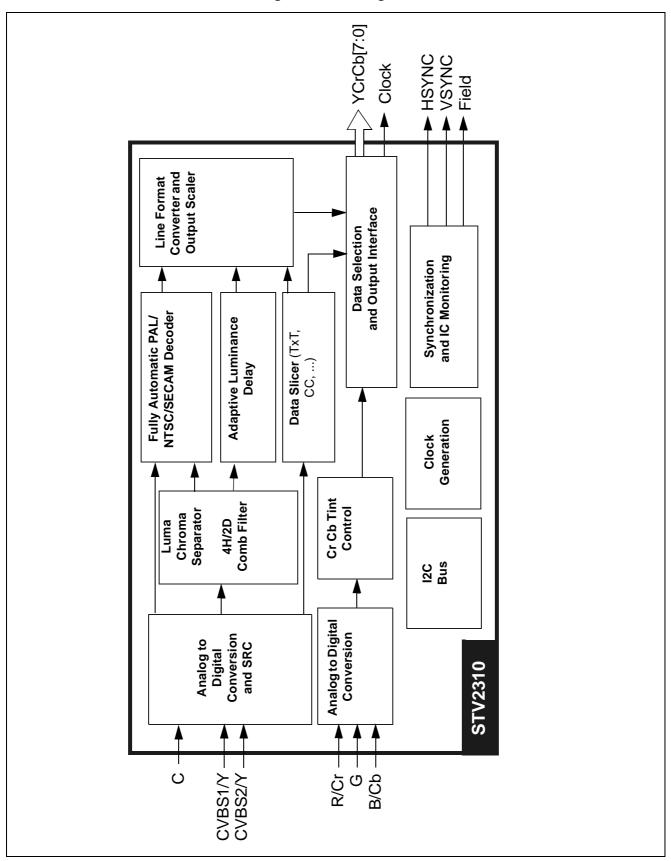


Figure 1: Block Diagram

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