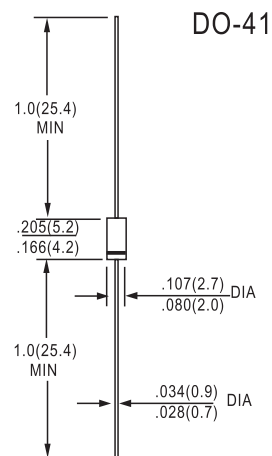


FEATURES

- Molded case feature for auto insertion
- High current capability
- Low leakage current
- Fast switching capability
- High temperature soldering guaranteed
- 250°C /10sec/0.375" lead length at 5 lbs tension
- Glass Passivated chip

MECHANICAL DATA

Terminal: Plated axial leads solderable per
MIL-STD 202E, method 208C
Case: Molded with UL-94 Class V-0 recognized Flame
Retardant Epoxy
Polarity: color band denotes cathode
Mounting position: any



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(single-phase, half-wave, 60HZ, resistive or inductive load rating at 25°C, unless otherwise stated)

	SYMBOL	ERB05GG	units
Maximum Recurrent Peak Reverse Voltage	V _{rrm}	400	V
Maximum RMS Voltage	V _{rms}	280	V
Maximum DC blocking Voltage	V _{dc}	400	V
Maximum Average Forward Rectified Current 3/8"lead length at Ta =55°C	I _{f(av)}	0.5	A
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load	I _{fsm}	10.0	A
Maximum Forward Voltage at rated Forward Current and 25°C	V _f	1.2	V
Maximum full load reverse current full cycle average at 55°C Ambient	I _{r(av)}	100.0	μA
Maximum DC Reverse Current at rated DC blocking voltage Ta =25°C	I _r	5.0	μA
Ta =150°C		100.0	μA
Maximum Reverse Recovery Time (Note 1)	T _{rr}	150	nS
Typical Junction Capacitance (Note 2)	C _j	15.0	pF
Typical Thermal Resistance (Note 3)	R _{th(ja)}	55.0	°C /W
Storage and Operating Junction Temperature	T _{stg, Tj}	-55 to +150	°C

Note:

1. Reverse Recovery Condition I_f =0.5A, I_r =1.0A, I_{rr} =0.25A
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V_{dc}
3. Thermal Resistance from Junction to Ambient at 3/8"lead length, P.C. Board Mounted

RATINGS AND CHARACTERISTIC CURVES ERB05GG

FIG. 1 - FORWARD CURRENT DERATING CURVE

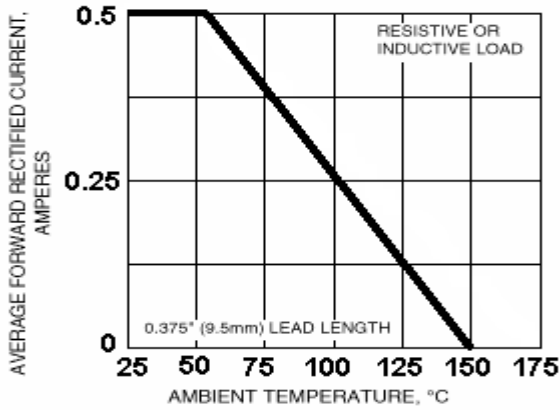


FIG. 2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

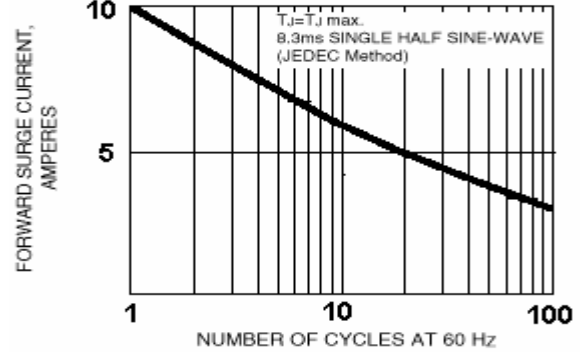


FIG. 3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

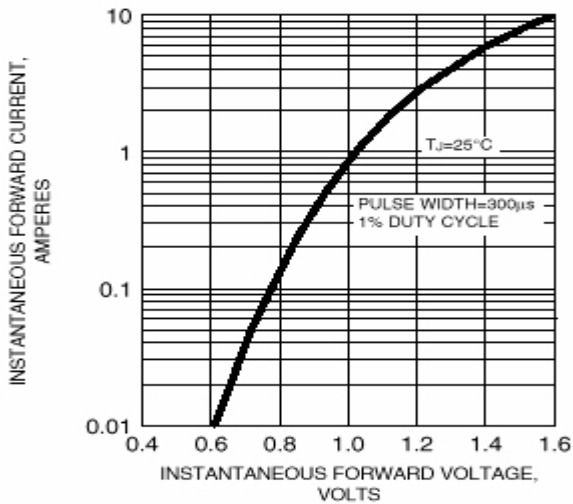


FIG. 4 - TYPICAL REVERSE CHARACTERISTICS

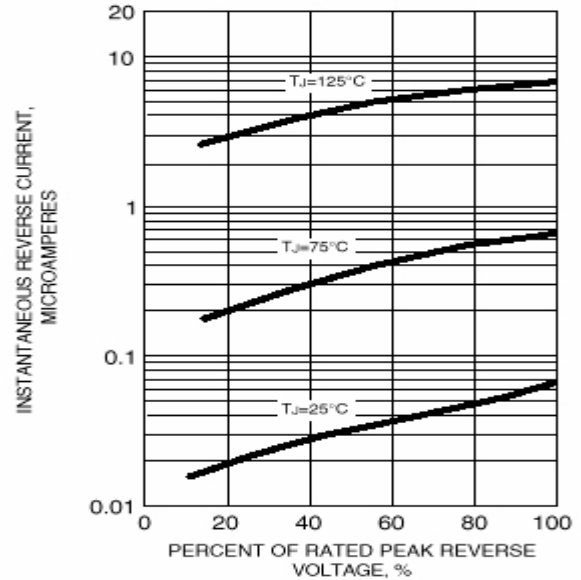


FIG. 5 - TYPICAL JUNCTION CAPACITANCE

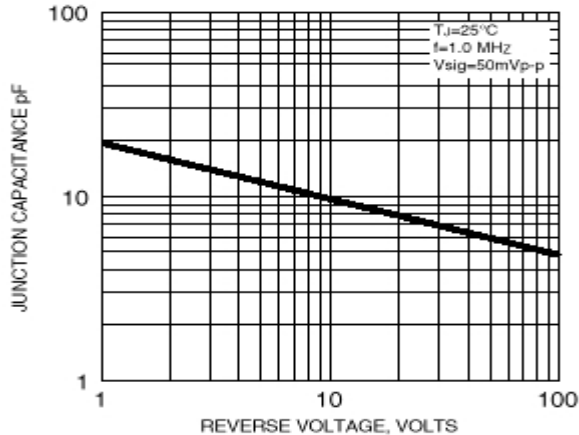


FIG. 6 - TYPICAL TRANSIENT THERMAL IMPEDANCE

