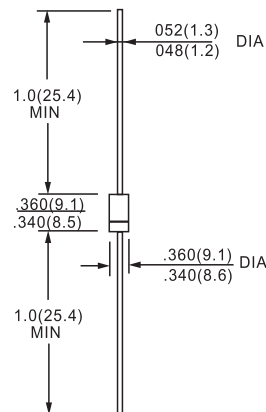


**TAYCHIPST****HIGH EFFICIENCY RECTIFIER****HER601 THRU HER608****50V-1000V 6.0A****FEATURES**

- Diffused Junction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability

**MECHANICAL DATA**

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 2.1 grams (approx.)
- Mounting Position: Any
- Marking: Type Number
- Epoxy: UL 94V-O rate flame retardant

**R-6**

Dimensions in inches and (millimeters)

**MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS**

Single Phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

Characteristic	Symbol	HER 601	HER 602	HER 603	HER 604	HER 605	HER 606	HER 607	HER 608	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	50	100	200	300	400	600	800	1000	V
RMS Reverse Voltage	VR(RMS)	35	70	140	210	280	420	560	700	V
Average Rectified Output Current (Note 1) @T <sub>A</sub> = 55°C	Io	6.0								A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	200								A
Forward Voltage @IF = 6.0A	VFM	1.0				1.3	1.7			V
Peak Reverse Current @T <sub>A</sub> = 25°C At Rated DC Blocking Voltage @T <sub>A</sub> = 100°C	IRM	10.0 100								µA
Reverse Recovery Time (Note 2)	trr	50					75			nS
Typical Junction Capacitance (Note 3)	Cj	100					65			pF
Operating Temperature Range	Tj	-65 to +125								°C
Storage Temperature Range	TSTG	-65 to +150								°C

Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case  
2. Measured with I<sub>F</sub> = 0.5A, I<sub>R</sub> = 1.0A, I<sub>RR</sub> = 0.25A. See figure 5.  
3. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.



**TAYCHIPST**

**HIGH EFFICIENCY RECTIFIER**

HER601 THRU HER608

50V-1000V 6.0A

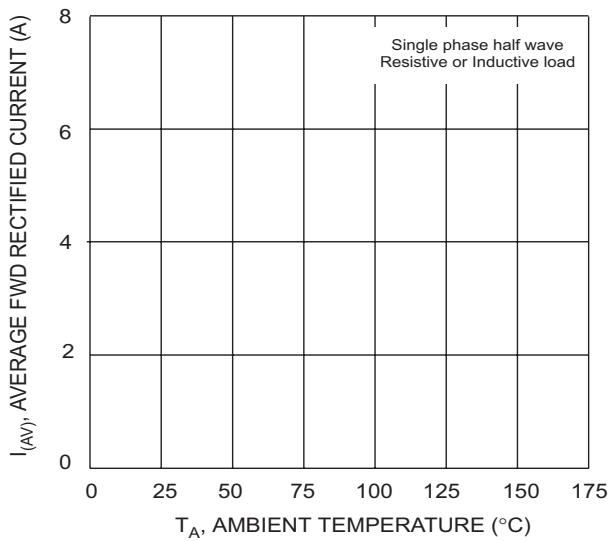


Fig. 1 Forward Current Derating Curve

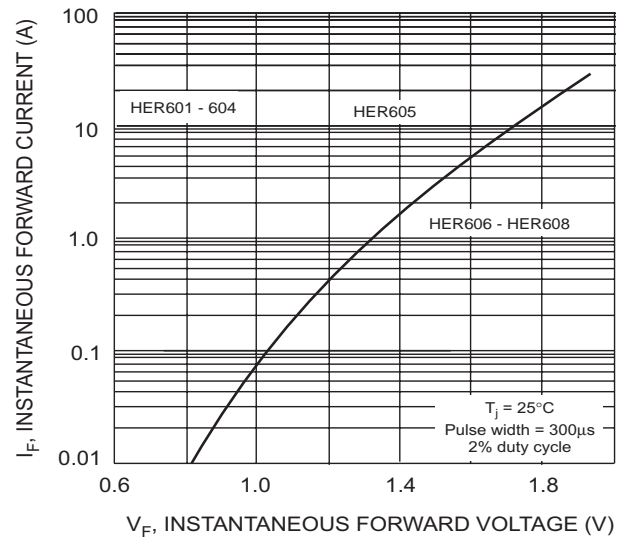


Fig. 2 Typical Forward Characteristics

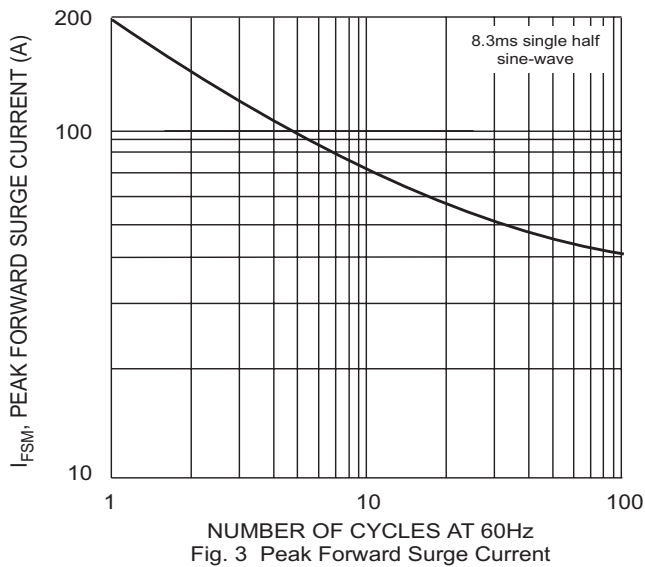


Fig. 3 Peak Forward Surge Current

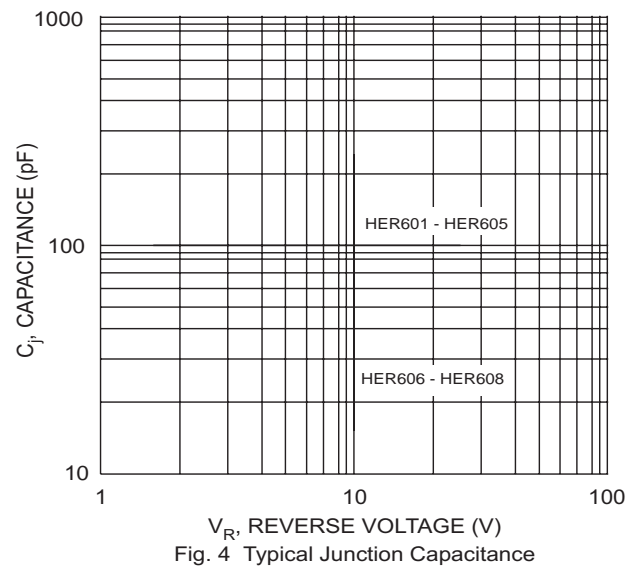
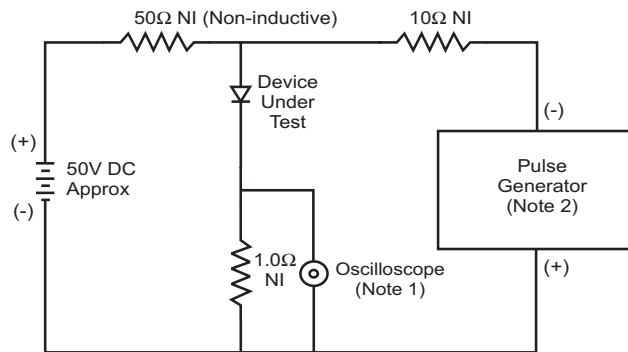
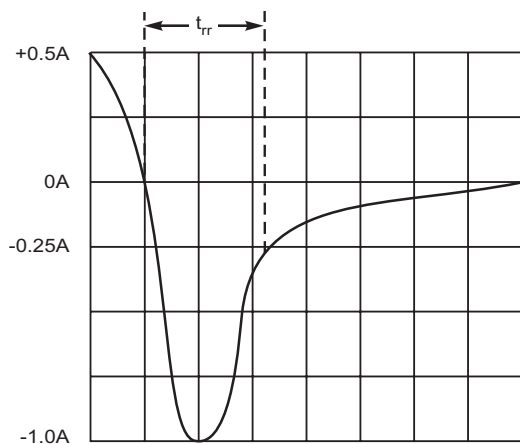


Fig. 4 Typical Junction Capacitance



- Notes:
1. Rise Time = 7.0ns max. Input Impedance = 1.0M $\Omega$ , 22pF.
  2. Rise Time = 10ns max. Input Impedance = 50 $\Omega$ .



Set time base for 5/10ns/cm

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit