山东迪一电子科技有限公司



SB220 - SB2100

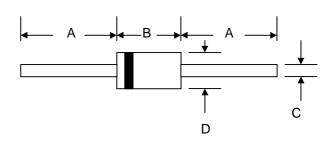




2.0A SCHOTTKY BARRIER DIODE

Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- High Current Capability
- Low Power Loss, High Efficiency
- High Surge Current Capability
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications



Mechanical Data

- Case: DO-15, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.40 grams (approx.)
- Mounting Position: Any
- Marking: Type Number
- Lead Free: For RoHS / Lead Free Version,
 Add "-LF" Suffix to Part Number, See Page 4

DO-15							
Dim	Min	Max					
Α	25.4	_					
В	5.50	7.62					
С	0.71	0.864					
D	2.60	3.60					
All Dimensions in mm							

Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	SB220	SB230	SB240	SB250	SB260	SB280	SB2100	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	20	30	40	50	60	80	100	V
RMS Reverse Voltage	VR(RMS)	14	21	28	35	42	56	70	٧
Average Rectified Output Current @T _L = 100°C (Note 1)	lo	2.0						Α	
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	50					А		
Forward Voltage @I _F = 2.0A	VFM	0.50			0.	.70 0.		85	٧
Peak Reverse Current $@T_A = 25^{\circ}C$ At Rated DC Blocking Voltage $@T_A = 100^{\circ}C$	lкм	0.5 10						mA	
Typical Junction Capacitance (Note 2)	Cj	170			140				pF
Typical Thermal Resistance (Note 1)	$R\hspace{.01in}_{\theta}JA$	35						°C/W	
Operating and Storage Temperature Range	Tj, TSTG	-65 to +150							°C

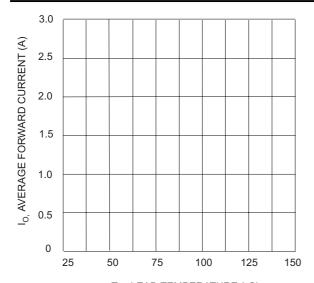
Note: 1. Valid provided that leads are kept at ambient temperature at a distance of 9.5mm from the case.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

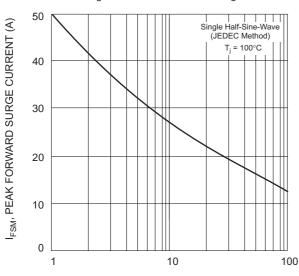


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T_L, LEAD TEMPERATURE (°C) Fig. 1 Forward Current Derating Curve



NUMBER OF CYCLES AT 60 Hz Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

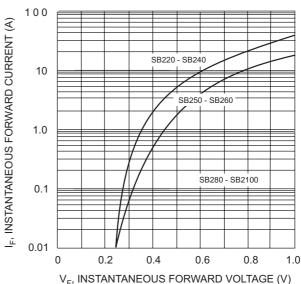
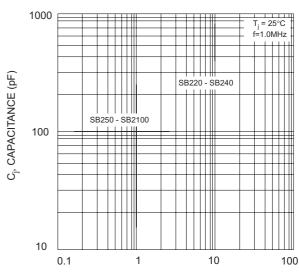
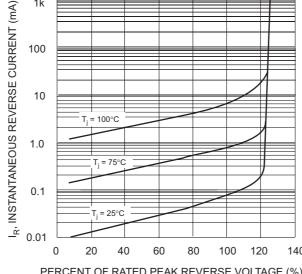


Fig. 2 Typical Forward Characteristics



V_R, REVERSE VOLTAGE (V) Fig. 4 Typical Junction Capacitance



PERCENT OF RATED PEAK REVERSE VOLTAGE (%) Fig. 5 Typical Reverse Characteristics