AO7407

P-Channel Enhancement Mode Field Effect Transistor

General Description

The AO7407 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 1.8V. This device is suitable for use as a load switch or in PWM applications.

Features

V_{DS} (V) = -20V

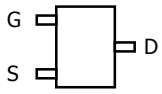
I_D = -1.2 A

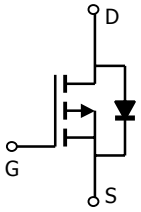
$R_{DS(ON)}$ < 135mΩ (V_{GS} = -4.5V)

$R_{DS(ON)}$ < 170mΩ (V_{GS} = -2.5V)

$R_{DS(ON)}$ < 220mΩ (V_{GS} = -1.8V)

SC-70
SOT 323
Top View





Absolute Maximum Ratings T_A=25°C unless otherwise noted				
Parameter		**Symbol**	**Maximum**	**Units**
Drain-Source Voltage		V_{DS}	-20	V
Gate-Source Voltage		V_{GS}	±8	V
Continuous Drain Current [A]	T_A=25°C	I_D	-1.2	A
	T_A=70°C		-1.0	
Pulsed Drain Current [B]		I_{DM}	-10	
Power Dissipation [A]	T_A=25°C	P_D	0.35	W
	T_A=70°C		0.22	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics					
Parameter		**Symbol**	**Typ**	**Max**	**Units**
Maximum Junction-to-Ambient [A]	t ≤ 10s	$R_{\theta JA}$	300	360	°C/W
Maximum Junction-to-Ambient [A]	Steady-State		350	425	°C/W
Maximum Junction-to-Lead [C]	Steady-State	$R_{\theta JL}$	280	320	°C/W

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	I_D=-250μA, V_{GS}=0V	-20			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=-16V, V_{GS}=0V			-1	μA
		T_J=55°C			-5	
I_{GSS}	Gate-Body leakage current	V_{DS}=0V, V_{GS}=±8V			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS} I_D=-250μA	-0.3	-0.55	-1	V
$I_{D(ON)}$	On state drain current	V_{GS}=-4.5V, V_{DS}=-5V	-10			A
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=-4.5V, I_D=-1.2A		111	135	mΩ
		T_J=125°C		141	175	
		V_{GS}=-2.5V, I_D=-1A		137	170	mΩ
		V_{GS}=-1.8V, I_D=-1A		169	220	mΩ
g_{FS}	Forward Transconductance	V_{DS}=-5V, I_D=-3A	4	7		S
V_{SD}	Diode Forward Voltage	I_S=-1A,V_{GS}=0V		-0.78	-1	V
I_S	Maximum Body-Diode Continuous Current				-0.6	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=-10V, f=1MHz		540		pF
C_{oss}	Output Capacitance			72		pF
C_{rss}	Reverse Transfer Capacitance			49		pF
R_g	Gate resistance	V_{GS}=0V, V_{DS}=0V, f=1MHz		12		Ω
SWITCHING PARAMETERS						
Q_g	Total Gate Charge	V_{GS}=-4.5V, V_{DS}=-10V, I_D=-1A		6.2		nC
Q_{gs}	Gate Source Charge			0.54		nC
Q_{gd}	Gate Drain Charge			1.44		nC
$t_{D(on)}$	Turn-On DelayTime	V_{GS}=-4.5V, V_{DS}=-10V, R_L=15Ω, R_{GEN}=3Ω		12		ns
t_r	Turn-On Rise Time			10.7		ns
$t_{D(off)}$	Turn-Off DelayTime			74		ns
t_f	Turn-Off Fall Time			28.7		ns
t_{rr}	Body Diode Reverse Recovery Time	I_F=-1A, dI/dt=100A/μs		24.5		ns
Q_{rr}	Body Diode Reverse Recovery Charge	I_F=-1A, dI/dt=100A/μs		17.4		nC

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The value in any a given application depends on the user's specific board design. The current rating is based on the t≤ 10s thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C. The $R_{\theta JA}$ is the sum of the thermal impedence from junction to lead $R_{\theta JL}$ and lead to ambient.

D. The static characteristics in Figures 1 to 6,12,14 are obtained using 80μs pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The SOA curve provides a single pulse rating.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

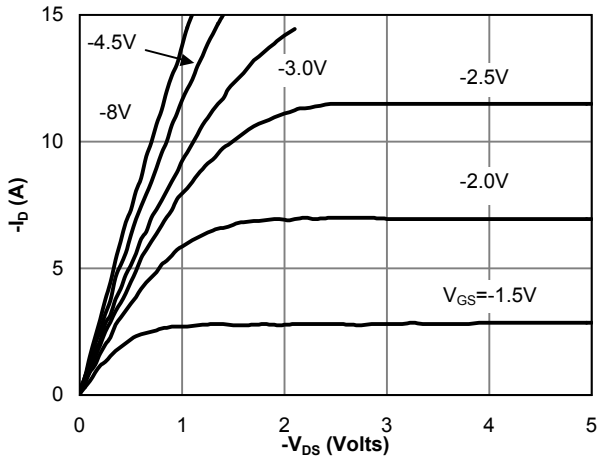


Fig 1: On-Region Characteristics

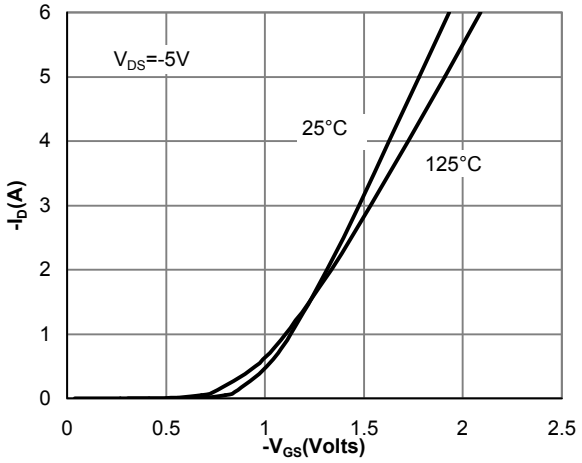


Figure 2: Transfer Characteristics

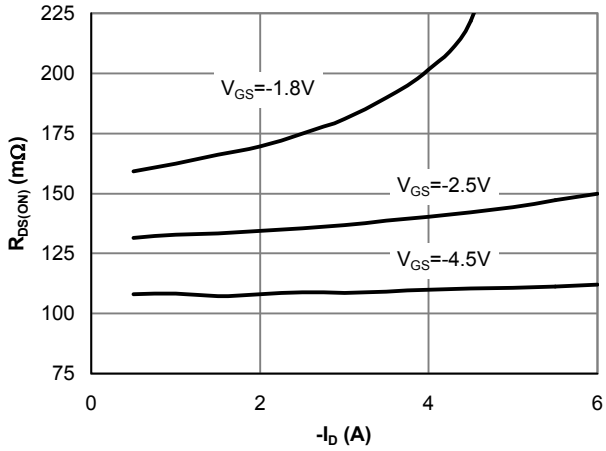


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

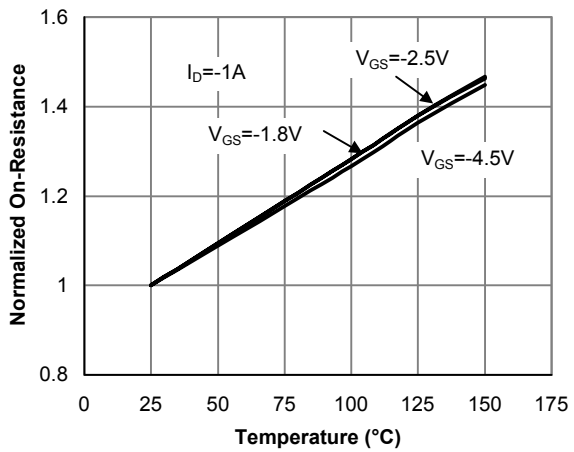


Figure 4: On-Resistance vs. Junction Temperature

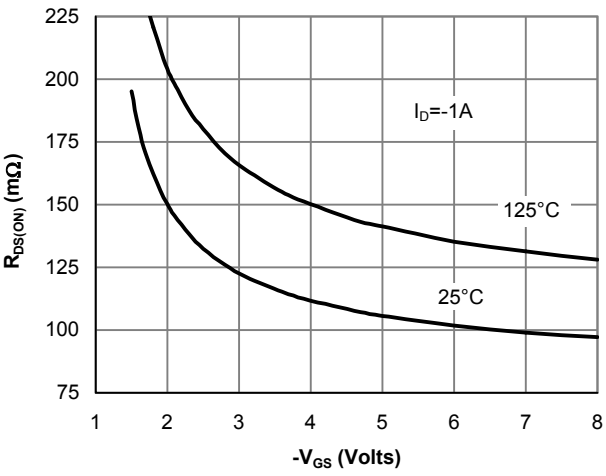


Figure 5: On-Resistance vs. Gate-Source Voltage

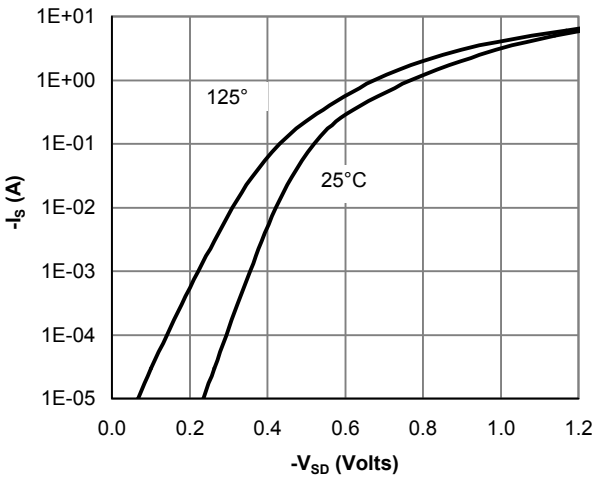


Figure 6: Body-Diode Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

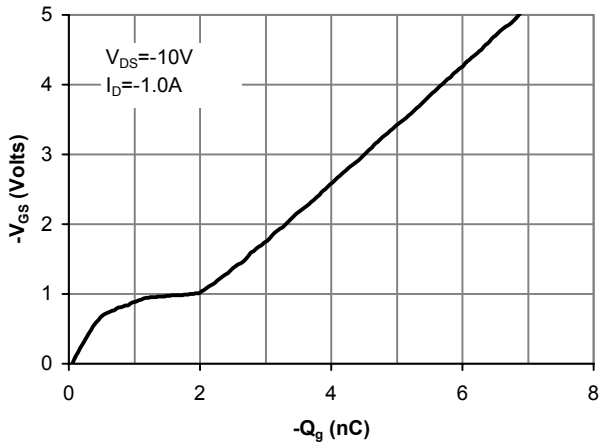


Figure 7: Gate-Charge Characteristics

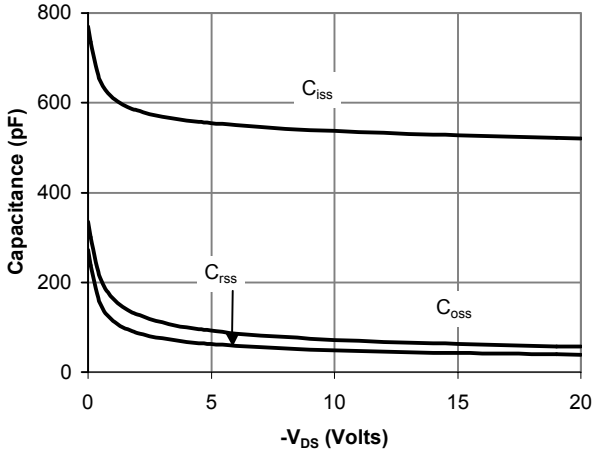


Figure 8: Capacitance Characteristics

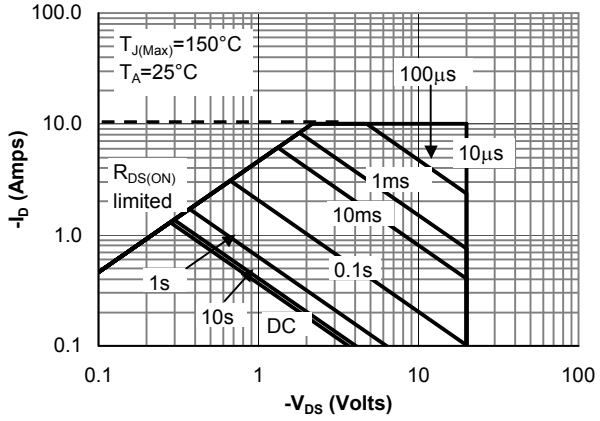


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

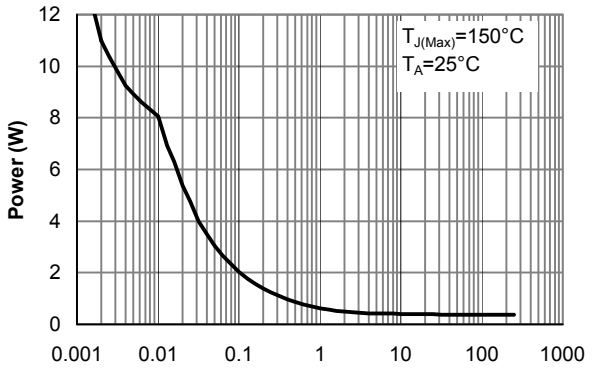


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

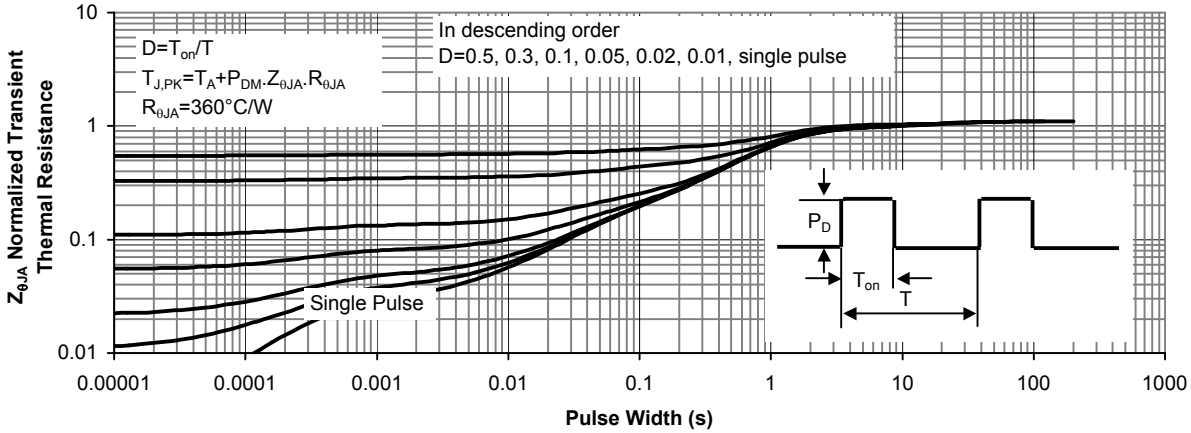
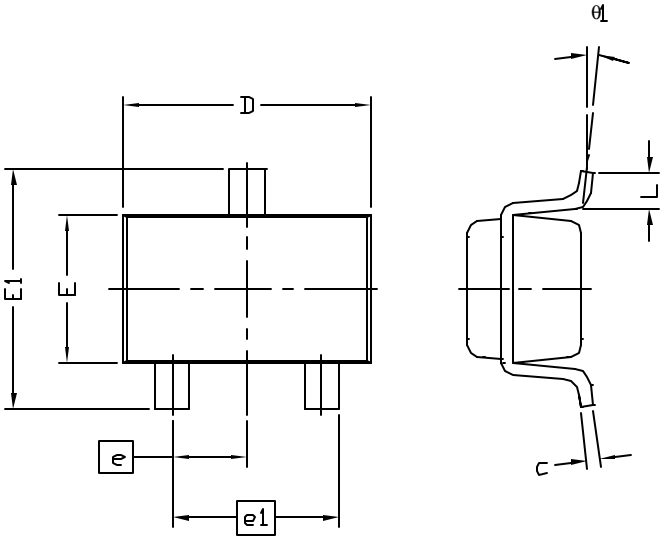


Figure 11: Normalized Maximum Transient Thermal Impedance

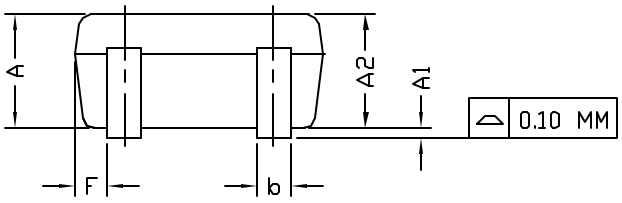
freescale
飞思卡尔(深圳)功率半导体有限公司

SC-70 3L Package Data

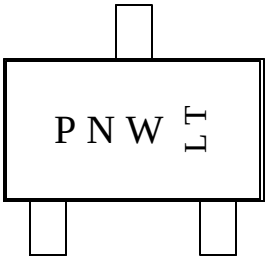


SYMBOLS	DIMENSIONS IN MILLIMETERS	
	MIN	MAX
A	0.90	1.10
A1	0.00	0.10
A2	0.90	1.00
b	0.25	0.40
C	0.10	0.20
D	1.80	2.20
E	1.15	1.35
E1	2.00	2.20
F	0.30	0.40
e	0.65 BSC	
e1	1.30 BSC	
L	0.10	0.30
θ1	1°	8°

NOTE:
1. LEAD FINISH: 150 MICROINCHES (3.8 um) MIN.
   THICKNESS OF Tin/Lead (SOLDER) PLATED ON LEAD
2. TOLERANCE ±0.10 mm (4 mil) UNLESS OTHERWISE
   SPECIFIED
3. COPLANARITY : 0.10 mm
4. OTHER NAME OF THIS PACKAGE IS CALLED SOT-323



PACKAGE MARKING DESCRIPTION

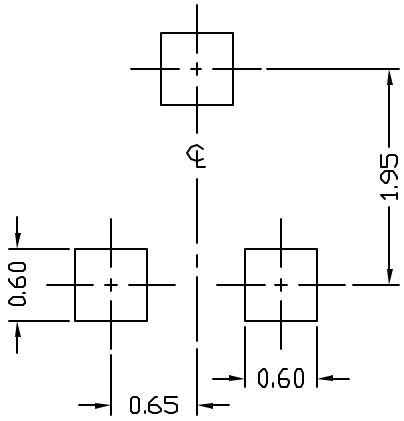


SC-70 3L PART NO. CODE

PART NO.	CODE
AO7407	7

NOTE:
P - PART NUMBER CODE.
N - FOUNDRY AND ASSEMBLY LOCATION CODE
W - YAER AND WEEK CODE.
L T - ASSEMBLY LOT CODE.

RECOMMENDATION OF LAND PATTERN

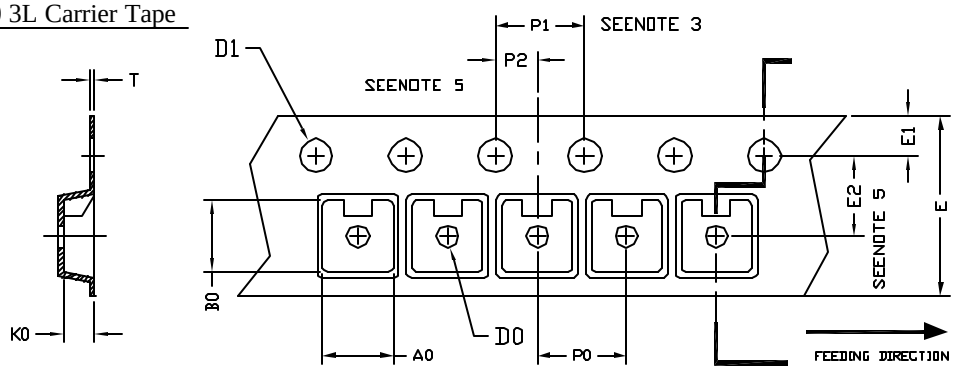


Rev. A

SC-70 3L Tape and Reel Data

freescale
飞思卡尔(深圳)功率半导体有限公司

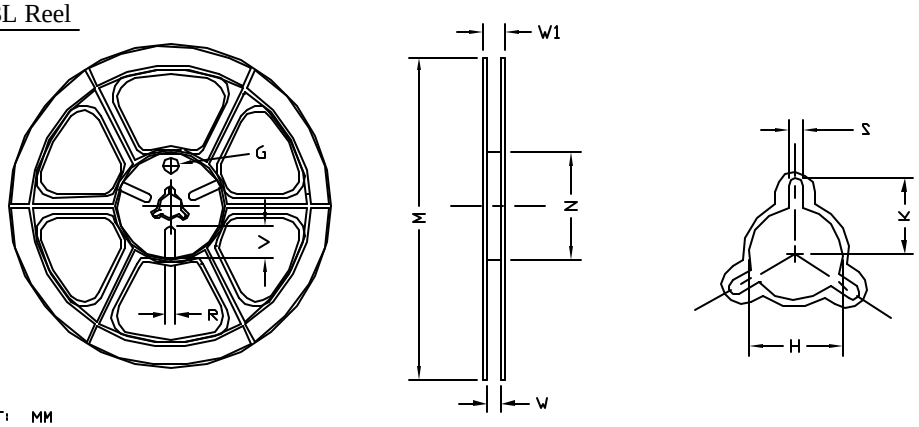
SC-70 3L Carrier Tape



UNIT: MM

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SC-70, 3L (8 mm)	2.40 ±0.10	2.40 ±0.10	1.19 ±0.10	1.00 MIN	1.55 ±0.05	8.00 ±0.30	1.75 ±0.10	3.50 ±0.05	4.00 ±0.10	4.00 ±0.10	2.00 ±0.05	0.25 ±0.05

SC-70 3L Reel



UNIT: MM

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
8 mm	ø180	ø180.00 ±0.50	ø60.50	9.00 ±0.30	11.40 ±1.00	ø13.00 +0.50 -0.20	10.60	2.00 ±0.50	ø9.00	5.00	18.00

SC-70 3L Tape

Leader / Trailer & Orientation