

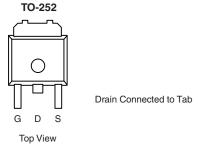
P-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}\left(\Omega\right)$	I _D (A)	Q _g (Typ)	
- 60	0.155 at V _{GS} = - 10 V	- 8.4	12.5	
	0.280 at V _{GS} = - 4.5 V	- 7.4	12.5	

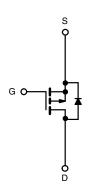
FEATURES

- TrenchFET® Power MOSFETS
- 175 °C Rated Maximum Junction Temperature





Ordering Information: SUD08P06-155L-E3 (Lead (Pb)-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 2$	25 °C, unless othe	rwise noted			
Parameter		Symbol	Limit	Unit	
Gate-Source Voltage	V _{GS}	± 20	V		
Continuous Drain Current (T _{.I} = 175 °C)	T _C = 25 °C	I-	- 8.4		
Continuous Diain Current (1) = 175 C)	T _C = 100 °C	- I _D	- 6		
Pulsed Drain Current		I _{DM}	- 18	Α	
Continuing Source Current (Diode Conduction)		I _S	- 8.4		
Avalanche Current		I _{AS}	- 12		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	7.2	mJ	
Maximum Daylar Dissination	T _C = 25 °C	В	25 ^a	W	
Maximum Power Dissipation	T _A = 25 °C	P _D	2 ^b	"	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
hunding to Ambient	t ≤ 10 sec	R _{thJA}	20	25	°C/W
Junction-to-Ambient ^D	Steady State		62	75	
Junction-to-Case		R _{thJC}	5	6	

Notes:

- a. See SOA curve for voltage derating.
- b. Surface Mounted on 1" x 1" FR-4 boad.



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Parameter	Symbol	Test Conditions	Min	Typ ^a	Max	Unit
Static	- Cyllison	Tool Conditions		.,,,,	mux	- Cinc
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = - 250 μA	- 60			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 1.0	- 2.0	- 3.0	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	-	V _{DS} = - 60 V, V _{GS} = 0 V			- 1	
	I _{DSS}	V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 125 °C			- 50	μΑ
-		V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 175 °C			- 150	
On-State Drain Current ^b	I _{D(on)}	V _{DS} = -5 V, V _{GS} = -10 V	- 10			Α
	(,	V _{GS} = - 10 V, I _D = - 5 A		0.125	0.155	Ω
		V _{GS} = - 10 V, I _D = - 5 A, T _J = 125 °C			0.280	
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = - 10 V, I _D = - 5 A, T _J = 175 °C			0.350	
		V _{GS} = - 4.5 V, I _D = - 2 A		0.158	0.280	
Forward Transconductance ^b	9 _{fs}	V _{DS} = - 15 V, I _D = - 5 A		8		S
Dynamic	<u>.</u>	·		!		
Input Capacitance	C _{iss}	V _{DS} = - 25 V, V _{GS} = 0 V, f = 1 MHz		450		pF
Output Capacitance	C _{oss}			65		
Reverse Transfer Capacitance	C _{rss}	1		40		
Total Gate Charge	Q_g			12.5	19	nC
Gate-Source Charge	Q_{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -8.4 \text{ A}$		2.3		
Gate-Drain Charge	Q_{gd}]		3.2		
Gate Resistance	R_{g}	f = 1 MHz		8.0		Ω
Turn-On Delay Time ^c	t _{d(on)}			5	10	
Rise Time ^c	t _r	$V_{DD} = -30 \text{ V}, R_L = 3.57 \Omega$ $I_D \cong -8.4 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 2.5 \Omega$		14	25	ns
Turn-Off Delay Time ^c	t _{d(off)}			15	25	
Fall Time ^c	t _f	1		7	12	
Source-Drain Diode Ratings and Cha	racteristics	(T _C = 25 °C) ^b		1	<u> </u>	
Pulsed Current	I _{SM}				- 20	Α
Forward Voltage ^b	V_{SD}	I _F = - 2 A, V _{GS} = 0 V		- 0.9	- 1.3	V
Reverse Recovery Time	t _{rr}	I _F = - 8 A, di/dt = 100 A/μs		50	80	ns
Reverse Recovery Time	Q _{rr}	- i _F = - ο A, αι/αι = 100 A/μS		80	120	nC

Notes:

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- c. Independent of operating temperature.

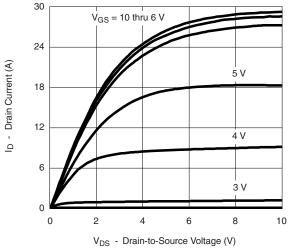
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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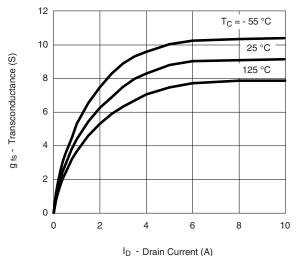
T_C = - 55 °C | | | 25 °C

125 °C

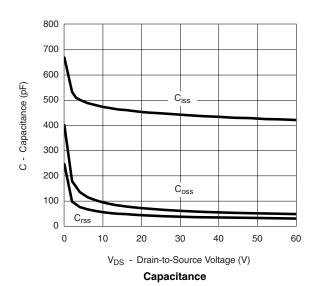
TYPICAL CHARACTERISTICS 25 °C unless noted







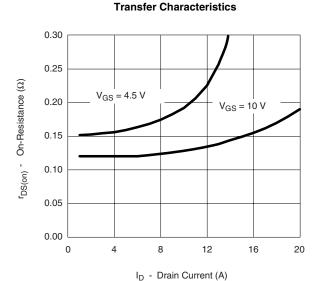
Transconductance



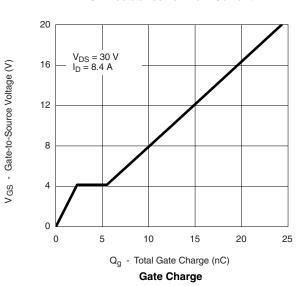
20 16 12 8 4

ID - Drain Current (A)

0.0 0.5 1.0 1.5 2.0 2.5 3.0 3.5 4.0 4.5 5.0 5.5 V_{GS} - Gate-to-Source Voltage (V)

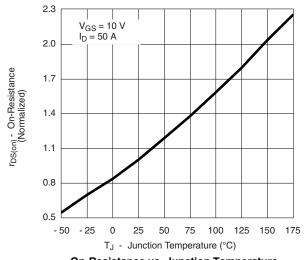


On-Resistance vs. Drain Current

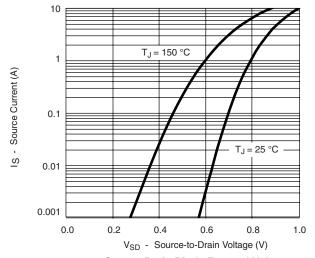


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TYPICAL CHARACTERISTICS 25 °C unless noted

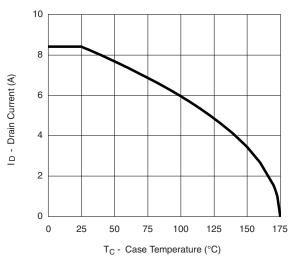


On-Resistance vs. Junction Temperature

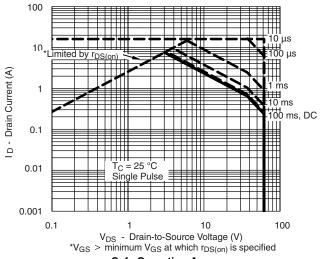


Source-Drain Diode Forward Voltage

THERMAL RATINGS



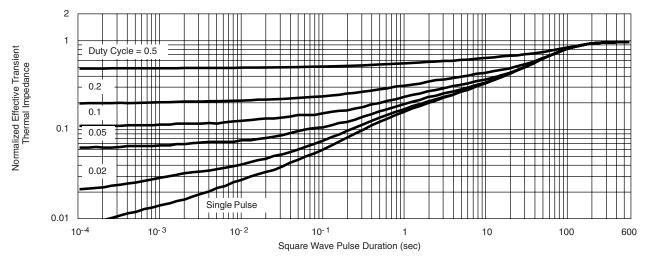
Drain Current vs. Case Temperature



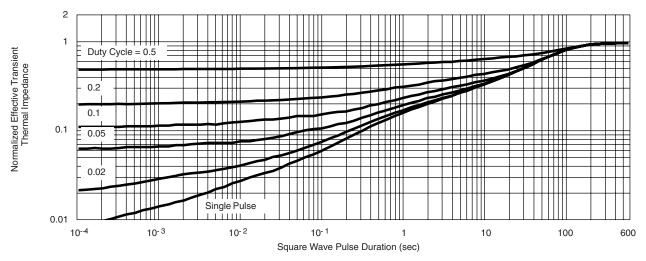
Safe Operating Area

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THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case



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