

N-Channel 30 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}\left(\Omega\right)$	I _D (A)	
30	0.030 @ V _{GS} = 10 V	±30	
	0.045 @ V _{GS} = 4.5 V	±25	



Parameter		Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	±20	—
Continuous Drain Current	T _C = 25°C	,	±30	
$(T_{J} = 175^{\circ}C)$	T _C = 100°C	ID	±21	
Pulsed Drain Current		I _{DM}	±40	一 ^
Continuous Source Current (Diode Conduction)		I _S	30	
Maximum Power Dissipation	T _C = 25°C	D	50	w
Maximum Power Dissipation	T _A = 25°C	P _D	3 ^a	— **
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Maximum Junction-to-Ambient	R _{thJA}	50	°C/W		
Maximum Junction-to-Case	R _{thJC}	3.0]		

Notes

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a. Surface Mounted on 4" x 4" FR4 Board.



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Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit	
Static	•		•	•	•		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30		V		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.0			1	
Gate-Body Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V			±100	nA	
Zero Gate Voltage Drain Current		V _{DS} = 30 V, V _{GS} = 0 V			1	+-	
	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V, T _J = 125°C			50	μΑ	
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 175°C			150	1	
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
		V _{GS} = 10 V, I _D = 15 A		0.020	0.030	Ω	
		$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}, T_J = 125^{\circ}\text{C}$		0.033	0.050		
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = 10 V, I _D = 15 A, T _J = 175°C		0.036	0.054		
		$V_{GS} = 4.5 \text{ V}, I_D = 12.5 \text{ A}$		0.030	0.045		
Forward Transconductanceb	9 _{fs}	V _{DS} = 15 V, I _D = 15 A	10	22		S	
Dynamic ^a			•				
Input Capacitance	C _{iss}			1170		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		320			
Reverse Transfer Capacitance	C _{rss}			60			
Total Gate Charge ^c	Q_{g}			18	35	nC	
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 15 \text{ V}, \ V_{GS} = 10 \text{ V}, \ I_{D} = 30 \text{ A}$		5.5			
Gate-Drain Charge ^c	Q _{gd}			2			
Turn-On Delay Time ^c	t _{d(on)}			10	20	ns	
Rise Time ^c	t _r	$V_{DD} = 15 \text{ V}, R_L = 0.5 \Omega$ $I_D \cong 30 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 7.5 \Omega$		10	20		
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 30 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 7.5 \Omega$		25	40		
Fall Time ^c	t _f			15	30		
Source-Drain Diode Ratings a	nd Characteristi	ic (T _C = 25°C)					
Pulsed Current	I _{SM}				40	А	
Diode Forward Voltageb	V _{SD}	I _F = 30 A, V _{GS} = 0 V		1.1	1.5	V	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 30 A, di/dt = 100 A/μs		50	100	ns	

Notes

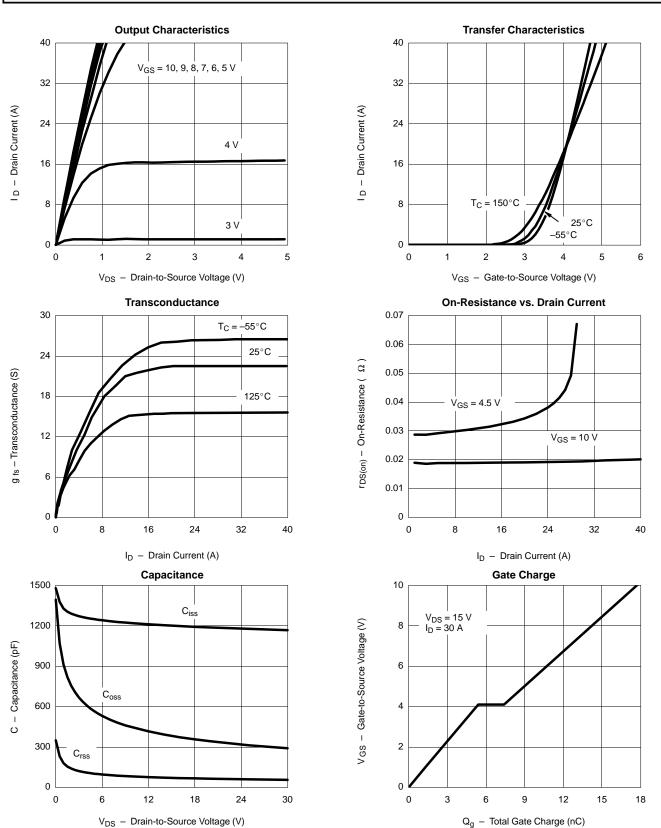
- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width $\leq 300 \,\mu\text{s}$, duty cycle $\leq 2\%$.
- c. Independent of operating temperature.

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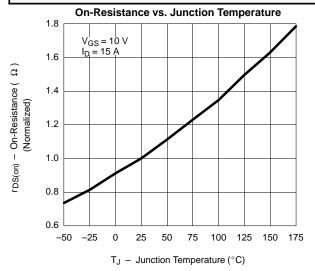
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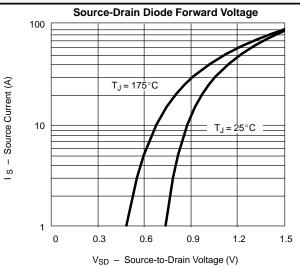
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



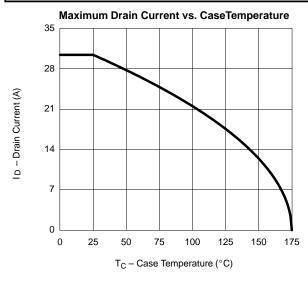
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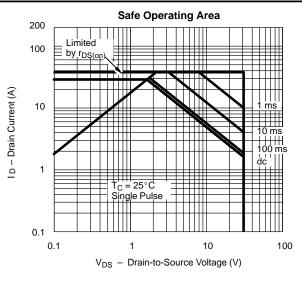
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

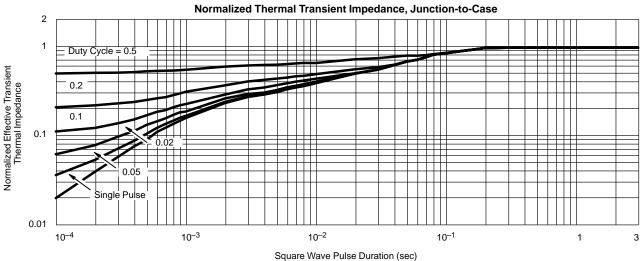




THERMAL RATINGS







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