

P-Channel 30-V (D-S), MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a			
- 30	0.010 at V _{GS} = - 10 V	- 15			
	0.018 at $V_{GS} = -4.5 \text{ V}$	- 12			

FEATURES

• TrenchFET® Power MOSFETs



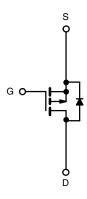




Drain Connected to Tab

Top View

Ordering Information: SUD45P03-10-E3 (Lead (Pb)-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	- 30	V		
Gate-Source Voltage	V _{GS}	± 20				
0 ii	T _A = 25 °C	l _D	- 15	^		
Continuous Drain Current ^b	T _A = 100 °C		- 8			
Pulsed Drain Current		I _{DM}	- 100	A A		
Continuous Source Current (Diode Conduction)		I _S	- 15			
W	T _C = 25 °C	В	70	W		
Maximum Power Dissipation ^b	T _A = 25 °C	P _D	4 ^b			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^b	R _{thJA}		30	°C/W		
Maximum Junction-to-Case	R _{thJC}		1.8	C/VV		

Notes:

- a. Calculated Rating for $T_A = 25$ °C, for comparison purposes only. This cannot be used as continuous rating (see Absolute Maximum Ratings and Typical Characteristics).
- b. Surface Mounted on FR4 board, $t \le 10$ s.

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Parameter	Symbol	mbol Test Conditions		Тур.	Max.	Unit	
Static	•			•			
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 30			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 3.0		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zava Cata Valtaga Dvain Current	1	V _{DS} = - 30 V, V _{GS} = 0 V			- 1	4	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 125 °C			- 50	- μΑ	
On Olate Busin Occurrent	1	V _{DS} = - 5 V, V _{GS} = - 10 V	- 50			А	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 4.5 V	- 20				
		V _{GS} = - 10 V, I _D = - 15 A			0.010		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 15 A, T _J = 125 °C			0.015	Ω	
		V _{GS} = - 4.5 V, I _D = - 15 A			0.018		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 15 A	20			S	
Dynamic ^b							
Input Capacitance	C _{iss}			6000		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = -25 \text{ V}, f = 1 \text{ MHz}$		1100			
Reverse Transfer Capacitance	C _{rss}]		700			
Total Gate Charge ^c	Q_g			90	150		
Gate-Source Charge ^c	Q_{gs}	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 45 A		20		nC	
Gate-Drain Charge ^c	Q_{gd}			16			
Turn-On Delay Time ^c	t _{d(on)}			15	25		
Rise Time ^c	t _r	$V_{DD} = -15 \text{ V}, R_{L} = 0.33 \Omega$		375	550		
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ - 45 A, V_{GEN} = - 10 V, R_G = 2.4 Ω		100	200	ns	
Fall Time ^c	t _f]		140	250		
Source-Drain Diode Ratings and Cha	racteristic T	_C = 25 °C					
Pulsed Current	I _{SM}				100	Α	
Diode Forward Voltage ^a	V_{SD}	I _F = - 45 A, V _{GS} = 0 V		1.0	1.5	V	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 45 A, dI/dt = 100 A/μs		55	100	ns	

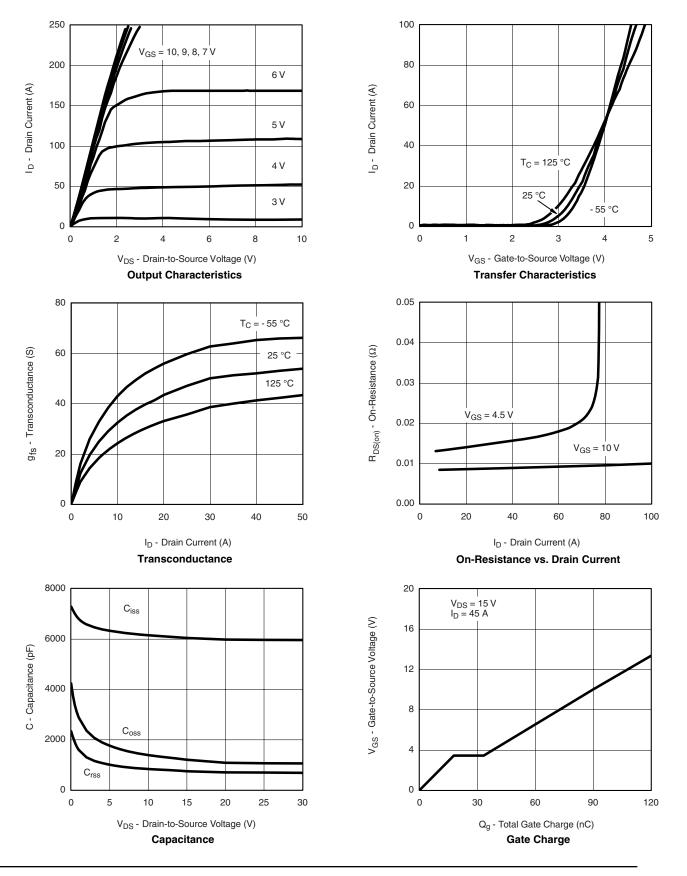
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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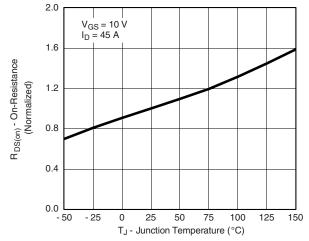
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



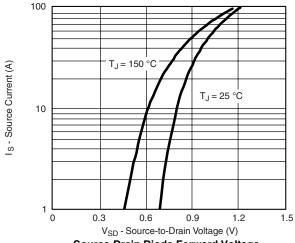
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



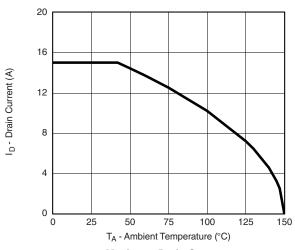
On-Resistance vs. Junction Temperature



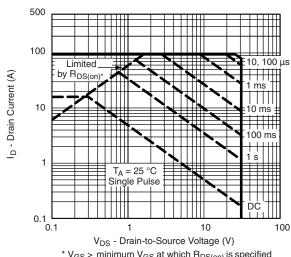
Source-Drain Diode Forward Voltage

THERMAL RATINGS

Normalized Effective Transient Thermal Impedance

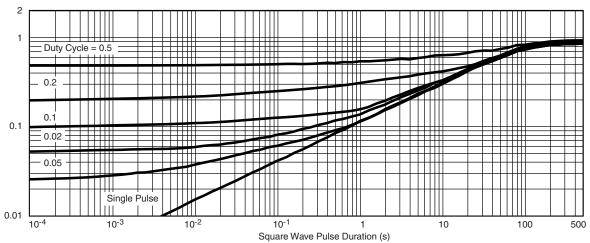


Maximum Drain Current vs. Ambient Temperature



* V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

Safe Operating Area



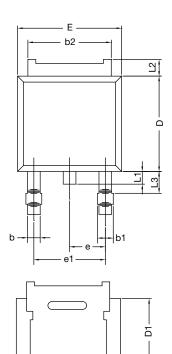
Normalized Thermal Transient Impedance, Junction-to-Ambient

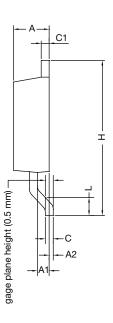
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TO-252AA CASE OUTLINE





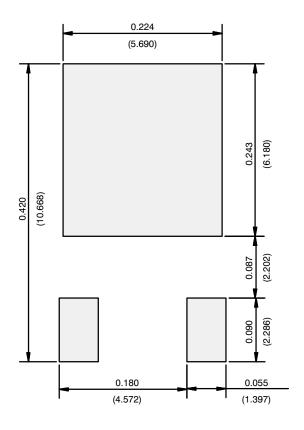
	MILLIN	IETERS	INCHES			
DIM.	MIN.	MAX.	MIN.	MAX.		
Α	2.21	2.38	0.087	0.094		
A1	0.89	1.14	0.035	0.045		
A2	0.030	0.127	0.001	0.005		
b	0.71	0.88	0.028	0.035		
b1	0.76	1.14	0.030	0.045		
b2	5.23	5.44	0.206	0.214		
С	0.46	0.58	0.018	0.023		
C1	0.46	0.58	0.018	0.023		
D	5.97	6.22	0.235	0.245		
D1	4.10	4.45	0.161	0.175		
Е	6.48	6.73	0.255	0.265		
E1	4.49	5.50	0.177	0.217		
е	2.28	2.28 BSC		0.090 BSC		
e1	4.57	BSC	0.180	BSC		
Н	9.65	10.41	0.380	0.410		
L	1.40	1.78	0.055	0.070		
L1	0.64	1.02	0.025	0.040		
L2	0.89	1.27	0.035	0.050		
L3	1.15	1.52	0.040	0.060		
ECN: T11-0110-Rev. L, 18-Apr-11 DWG: 5347						

• Dimension L3 is for reference only.

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RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)

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