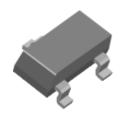
Freescale A03419/ MC3419

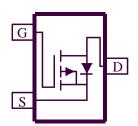
P-Channel 20-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

| PRODUCT SUMMARY | | | |
|-----------------|--------------------------|------------|--|
| $V_{DS}(V)$ | $r_{DS(on)}$ (OHM) | $I_{D}(A)$ | |
| -20 | $0.100 @ V_{GS} = -4.5V$ | -2.9 | |
| | $0.160 @ V_{GS} = -2.5V$ | -2.3 | |
| | $0.290 @ V_{GS} = -1.8V$ | -1.7 | |

- $\begin{tabular}{ll} \textbf{Low} & r_{DS(on)} & provides & higher efficiency & and \\ extends & battery & life \\ \end{tabular}$
- Low thermal impedance copper leadframe SOT-23 saves board space
- Fast switching speed
- High performance trench technology





| RoHS |
|-----------|
| COMPLIANT |
| HALOGEN |
| FRFF |

| ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED) | | | | | | |
|--|---|-----------------------------------|------------|-------|--|--|
| Parameter | | Symbol | Maximum | Units | | |
| Drain-Source Voltage | | V_{DS} | -20 | V | | |
| Gate-Source Voltage | | V_{GS} | ±12 | V | | |
| Continuous Drain Current ^a | $T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$ | T_ | -2.9 | | | |
| Continuous Drain Current | T _A =70°C | ъ | -2.4 | A | | |
| Pulsed Drain Current ^b | | I_{DM} | -10 | | | |
| Continuous Source Current (Diode Conduction) ^a | Diode Conduction) ^a | | ±1.6 | A | | |
| D | $T_A=25^{\circ}C$ | D- | 1.25 | W | | |
| Power Dissipation ^a | $T_A=25^{\circ}C$ $T_A=70^{\circ}C$ | r _D | 0.8 | VV | | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | -55 to 150 | °C | | |

| THERMAL RESISTANCE RATINGS | | | | | | |
|--|--------------|------------|---------|-------|--|--|
| Parameter | | Symbol | Maximum | Units | | |
| Maximum Junction-to-Ambient ^a | t <= 5 sec | D | 100 | °C/W | | |
| | Steady-State | R_{THJA} | 166 | C/VV | | |

1

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

(C)

Freescale AO3419/ MC3419

| D | Carrack - 1 | T (C IV) | Limits | | | TT *4 | |
|---|---------------------|--|--------|------|-------|-------|--|
| Parameter | Symbol | Test Conditions | Min | Тур | Max | Unit | |
| Static | | | | | | | |
| Gate-Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = -250 \text{ uA}$ | -0.4 | | | | |
| Gate-Body Leakage | IGSS | $V_{DS} = 0 \text{ V}, V_{GS} = +/-12 \text{ V}$ | | | ±100 | nA | |
| Zana Cata Walter a Dunin Communi | 7 | $V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$ | | | -1 | A | |
| Zero Gate Voltage Drain Current | I _{DSS} | $V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$ | | | -10 | uA | |
| On-State Drain Current ^A | I _{D(on)} | $V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$ | -3 | | | A | |
| | | $V_{GS} = -4.5 \text{ V}, I_D = -2.9 \text{ A}$ | | | 0.100 | | |
| Drain-Source On-Resistance ^A | rdS(on) | $V_{GS} = -2.5 \text{ V}, I_D = -2.3 \text{ A}$ | | | 0.160 | Ω | |
| | | $V_{GS} = -1.8 \text{ V}, I_D = -1.7 \text{ A}$ | | | 0.290 | | |
| Forward Tranconductance ^A | g _{fs} | $V_{DS} = -5 \text{ V}, I_D = -2.8 \text{ A}$ | | 3 | | S | |
| Diode Forward Voltage | V _{SD} | $I_S = -1.6 \text{ A}, V_{GS} = 0 \text{ V}$ | | -0.7 | | V | |
| Dynamic ^b | | | | | | • | |
| Total Gate Charge | Qg | $V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V},$ | | 6.0 | | nC | |
| Gate-Source Charge | Q_{gs} | $V_{DS} = -3 \text{ v}, V_{GS} = -4.3 \text{ v},$ $I_{D} = -2.6 \text{ A}$ | | 0.8 | | | |
| Gate-Drain Charge | Qgd | ID = -2.0 A | | 1.3 | | | |
| Turn-On Delay Time | t _{d(on)} | | | 6.5 | | | |
| Rise Time | t _r | $V_{\rm DD} = -5 \text{ V}, R_{\rm L} = 5 \text{ OHM},$ | | 20 | | ns | |
| Turn-Off Delay Time | t _{d(off)} | $V_{\rm GEN}$ = -4.5 V, $R_{\rm G}$ = 6 OHM | | 31 | | | |
| Fall-Time | t _f | | | 21 | | | |

Notes

a. Pulse test: $PW \le 300us duty cycle \le 2\%$.

b. Guaranteed by design, not subject to production testing.

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Freescale AO3419/ MC3419

Typical Electrical Characteristics

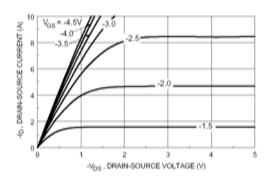
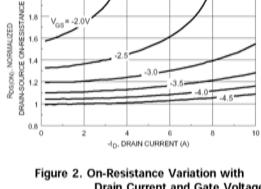


Figure 1. On-Region Characteristics.



Drain Current and Gate Voltage.

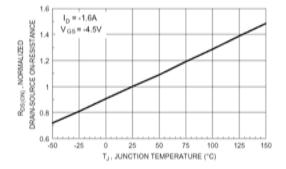


Figure 3. On-Resistance Variation with Temperature.

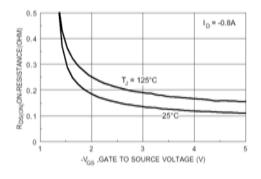


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

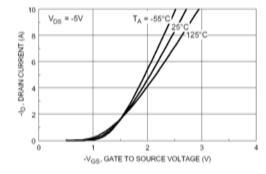


Figure 5. Transfer Characteristics.

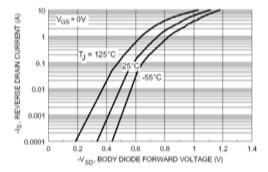


Figure 6 . Body Diode Forward Voltage Variation with Source Current and Temperature.

Freescale AO3419/ MC3419

Typical Electrical Characteristics

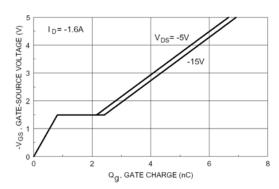


Figure 7. Gate Charge Characteristics.

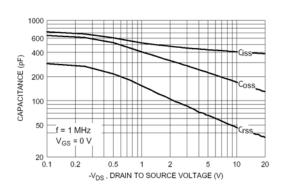


Figure 8. Capacitance Characteristics.

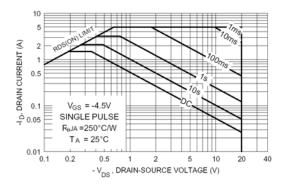


Figure 9. Maximum Safe Operating Area.

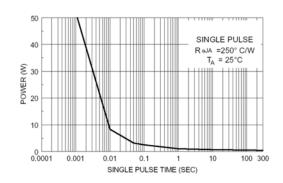


Figure 10. Single Pulse Maximum Power Dissipation.

Normalized Thermal Transient Junction to Ambient

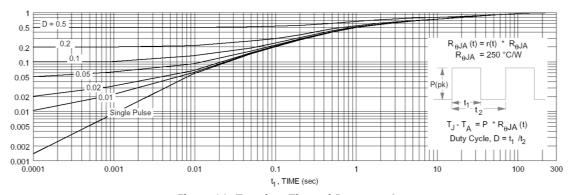


Figure 11. Transient Thermal Response Curve.

Package Information

