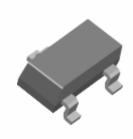
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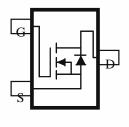
N-Channel 20-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize High Cell Density process. Low $r_{DS(on)}$ assures minimal power loss and conserves energy, making this device ideal for use in power management circuitry. Typical applications are DC-DC converters, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r_{DS(on)} Provides Higher Efficiency and Extends Battery Life
- Miniature SOT-23 Surface Mount Package Saves Board Space

PRODUCT SUMMARY &			
V_{DS} (V)	r _{DS (on)} (Q	$I_{D}(A)$	
	0.070 @ V _{GS} = 4.5V	2.2	
20	0.080 $V_{GS} = 2.5$ V	2.0	
	0.120 @ $V_{GS} = 1.8V$	1.8	





ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)					
Parame te r			Maximum	Units	
Drain-Source Voltage		V_{DS}	20	V	
Gate-Source Voltage		V_{GS}	±8	V	
	$T_A=25^{\circ}C$	т	2.2		
Continuous Drain Current ^a	$T_A=25$ °C $T_A=70$ °C	¹ D	1.8	A	
Pulsed Drain Current ^b		I_{DM}	8		
Continuous Source Current (Diode Conduction) ^a			0.6	A	
D a	$T_A=25^{\circ}C$	D	1.25	W	
Power Dissipation ^a	$T_A=25$ °C $T_A=70$ °C	r D	0.8	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum	Units	
Maximum Junction-to-Ambient ^a	t <= 5 sec	D	100	°C/W	
	Steady-State	R_{THJA}	166		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

(C)

SPECIFICATIONS $(T_A = 25)$	5°C UNLE	SS OTHERWISE NOTED)				
Downworton	G	The Control	Limits			TT .*4
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	0.70			
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = 12 \text{ V}$			1	uA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			0.1	uA
Zero Gate Voltage Drain Current		$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			1	
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	5			A
		$V_{GS} = 4.5 \text{ V}, I_D = 2.2 \text{ A}$			70	
Drain-Source On-Resistance ^A	$r_{\mathrm{DS(on)}}$	$V_{GS} = 2.5 \text{ V}, I_D = 2.0 \text{ A}$			80	mΩ
		$V_{GS} = 1.8 \text{ V}, I_D = 1.8 \text{ A}$			120	
Forward Tranconductance ^A	g_{fs}	$V_{DS} = 5 \text{ V}, I_D = 2.0 \text{ A}$		11		S
Diode Forward Voltage	V_{SD}	$I_S = 0.6 A, V_{GS} = 0 V$		0.60		V
Dynamic ^b						
Total Gate Charge	Q_{g}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$		4.5		
Gate-Source Charge	Q_{gs}	$I_{D} = 2.0 \text{ A}$		0.89		nC
Gate-Drain Charge	Q_{gd}	$I_{\rm D} = 2.0~{\rm A}$		0.95		
Turn-On Delay Time	$t_{d(on)}$			6		
Rise Time	$t_{\rm r}$	$V_{DD}=10$ V, ID = 1.0 A , $R_G=6~\Omega,$		6.5		ns
Turn-Off Delay Time	$t_{d(off)}$	$V_{Gs} = 4.5 \text{ V}$		14		115
Fall-Time	t_{f}			2		

Notes

- a. Pulse test: $PW \le 300us duty cycle \le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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Package Information

