P-Channel 30-V (D-S) MOSFET

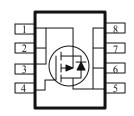
These miniature surface mount MOSFETs utilize High Cell Density process. Low r_{DS(on)} assures minimal power loss and conserves energy, making this device ideal for use in power management circuitry. Typical applications are PWMDC-DC converters, power management in portable and battery-powered products such as computers, printers, battery charger, telecommunication power system, and telephones power system.

| • | Low r _{DS(on)} Provides Higher Efficiency and |
|---|--|
| | Extends Battery Life |

- Miniature SO-8 Surface Mount Package Saves Board Space
- High power and current handling capability
- Extended VGS range (±25) for battery pack applications

| PRODUCT SUMMARY | | | | |
|---------------------|------------------------|---------------------------|--|--|
| V _{DS} (V) | $r_{DS(on)} m(\Omega)$ | I _D (A) | | |
| -30 | $9 @ V_{GS} = -10V$ | -15 | | |
| -30 | $13 @ V_{GS} = -4.5V$ | -11 | | |





| ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED) | | | | | | |
|--|-------------------------------------|-----------------------------------|------------|--------------|--|--|
| Parameter | | Symbol | Maximum | Units | | |
| Drain-Source Voltage | | | -30 | V | | |
| Gate-Source Voltage | | | ±25 | V | | |
| | $T_A=25^{\circ}C$ | | -15 | | | |
| Continuous Drain Current ^a | $T_A=25^{\circ}C$ $T_A=70^{\circ}C$ | 1D | -11 | A | | |
| Pulsed Drain Current ^b | I_{DM} | ±50 | | | | |
| Continuous Source Current (Diode Conduction) ^a | | I_S | -2.1 | A | | |
| D a | $T_A=25^{\circ}C$ | D_{-} | 3.1 | \mathbf{w} | | |
| Power Dissipation ^a | $T_A=25^{\circ}C$ $T_A=70^{\circ}C$ | L D | 2.3 | VV | | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | -55 to 150 | °C | | |

| THERMAL RESISTANCE RATINGS | | | | | | |
|--|------------|-----------------|---------|-------|--|--|
| Parameter | | Symbol | Maximum | Units | | |
| Maximum Junction-to-Case ^a | t <= 5 sec | $R_{	heta JC}$ | 25 | °C/W | | |
| Maximum Junction-to-Ambient ^a | t <= 5 sec | $R_{\theta JA}$ | 50 | °C/W | | |

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Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

| SPECIFICATIONS (T _A = 25°C UNLESS OTHERWISE NOTED) | | | | | | | |
|---|-------------------|--|--------|------|------|------|--|
| Davamatav | Crymbal | Comball Total Com Pillors | Limits | | | Unit | |
| Parameter | Symbol | Symbol Test Conditions | | Тур | Max | Umt | |
| Static | | | | | | | |
| Gate-Threshold Voltage | $V_{GS(th)}$ | $V_{DS} = V_{GS}, I_{D} = -250 \text{ uA}$ | -1 | | | V | |
| Gate-Body Leakage | I_{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$ | | | ±100 | nA | |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}$ | | | -1 | uA | |
| Zero Gate Voltage Drain Current | 1088 | $V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$ | | | -5 | | |
| On-State Drain Current ^A | $I_{D(on)}$ | $V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$ | -50 | | | A | |
| | | $V_{GS} = -10 \text{ V}, I_D = -13 \text{ A}$ | | | 9 | | |
| Drain-Source On-Resistance ^A | $r_{DS(on)}$ | $V_{GS} = -4.5 \text{ V}, I_D = -11 \text{ A}$ | | | 13 | mΩ | |
| | | $V_{GS} = -10 \text{ V}, I_D = -13 \text{ A}, TJ = 55^{\circ}\text{C}$ | | | 11 | | |
| Forward Tranconductance ^A | g_{fs} | $V_{DS} = -5 \text{ V}, I_D = -13 \text{ A}$ | | 44 | | S | |
| Diode Forward Voltage | V_{SD} | $I_S = 2.1 \text{ A}, V_{GS} = 0 \text{ V}$ | | -0.7 | | V | |
| Dynamic ^b | - | | | | | | |
| Total Gate Charge | Q_{g} | $V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V},$ | | 37.0 | | | |
| Gate-Source Charge | Q_{gs} | $I_{DS} = -13 \text{ V}, V_{GS} = -10 \text{ V},$ $I_{D} = -13 \text{ A}$ | | 10.0 | | nC | |
| Gate-Drain Charge | Q_{gd} | I _D – -13 A | | 14.5 | | 1 | |
| Switching | | | | | | | |
| Turn-On Delay Time | $t_{d(on)}$ | | | 19 | | | |
| Rise Time | $t_{\rm r}$ | $V_{DD} = -15 \text{ V}, R_L = 6 \Omega, ID = -1 \text{ A},$ | | 11 | | nS | |
| Turn-Off Delay Time | $t_{d(off)}$ | VGEN = -10 V | | 121 | | 113 | |
| Fall-Time | t_{f} | | | 68 | | | |

Notes

- a. Pulse test: $PW \le 300us duty cycle \le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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Typical Electrical Characteristics (P-Channel)

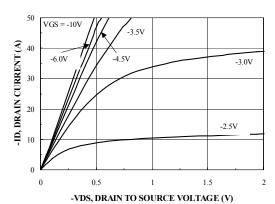


Figure 1. On-Region Characteristics

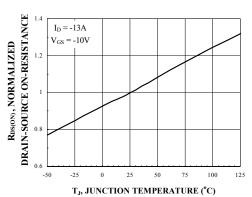


Figure 3. On-Resistance Variation with Temperature

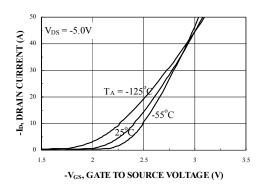


Figure 5. Transfer Characteristics

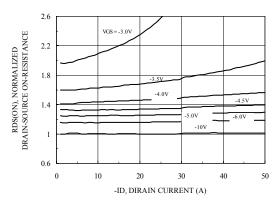


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage

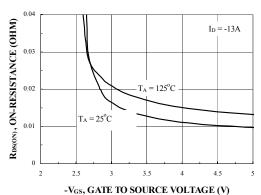
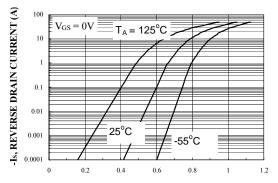
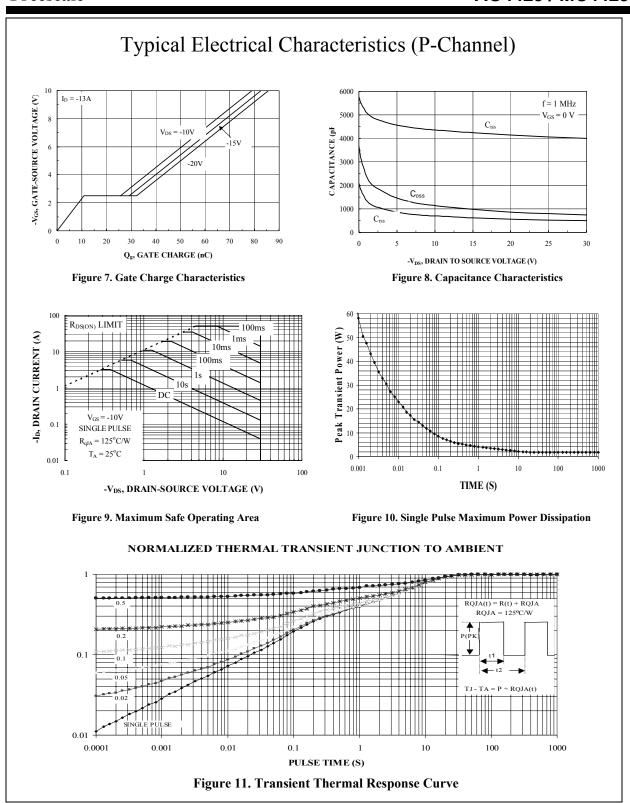


Figure 4. On-Resistance Variation with

Gate to Source Voltage



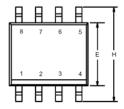
 $\hbox{--}V_{SD},\ BODY\ DIODE\ FORWARD\ VOLTAGE\ (V)$ Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature

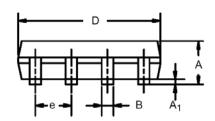


Package Information

SO-8: 8LEAD

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| | MILLIMETERS | | INC | CHES | |
|----------------|-------------|------|--------|-------|--|
| Dim | Min | Max | Min | Max | |
| Α | 1.35 | 1.75 | 0.053 | 0.069 | |
| A ₁ | 0.10 | 0.20 | 0.004 | 0.008 | |
| В | 0.35 | 0.51 | 0.014 | 0.020 | |
| С | 0.19 | 0.25 | 0.0075 | 0.010 | |
| D | 4.80 | 5.00 | 0.189 | 0.196 | |
| E | 3.80 | 4.00 | 0.150 | 0.157 | |
| е | 1.27 | BSC | 0.050 | BSC | |
| Н | 5.80 | 6.20 | 0.228 | 0.244 | |
| h | 0.25 | 0.50 | 0.010 | 0.020 | |
| L | 0.50 | 0.93 | 0.020 | 0.037 | |
| q | 0° | 8° | 0° | 8° | |

