Silicon Epitaxial Planar Diode for Various Detector, 1900 Planar Diode for Various Det Modulator, Demodulator

## HITACHI

ADE-208-146A (Z) Rev. 1 Aug. 1995

WWW.DZSC.

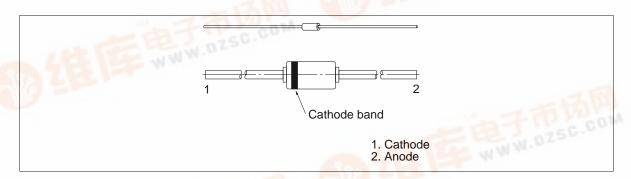
#### **Features**

- Low capacitance. (C = 3.0pF max)
- Short reverse recovery time.  $(t_{rr} = 8.0 \text{ns max})$
- High reliability with glass seal.

# Ordering Information W. D. Z. S. G. COM

Cathode band	Package Code
Navy Blue	DO-35
	WW.DZSC.Co

#### **Outline**





#### **Absolute Maximum Ratings** ( $Ta = 25^{\circ}C$ )

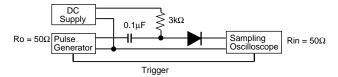
Item	Symbol	Value	Unit
Peak reverse voltage	$V_{RM}$	70	V
Reverse voltage	$V_R$	60	V
Peak forward current	I <sub>FM</sub>	450	mA
Non-Repetitive peak forward surge current	I <sub>FSM</sub> *	1	Α
Average forward current	Io	150	mA
Power dissipation	Pd	250	mW
Junction temperature	Tj	175	°C
Storage temperature	Tstg	-65 to +175	°C

Note: Within 1s forward surge current.

#### **Electrical Characteristics** ( $Ta = 25^{\circ}C$ )

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V <sub>F</sub>	0.64	_	0.80	V	$I_F = 10 \text{mA}$
Reverse current	I <sub>R</sub>	_	_	0.1	μΑ	V <sub>R</sub> = 30V
Capacitance	С	_	_	3.0	pF	$V_R = 1V$ , $f = 1MHz$
Reverse recovery time	t <sub>rr</sub> *	_	_	8.0	ns	$I_F = I_R = 10$ mA, $Irr = 1$ mA

Note: Reverse recovery time test circuit



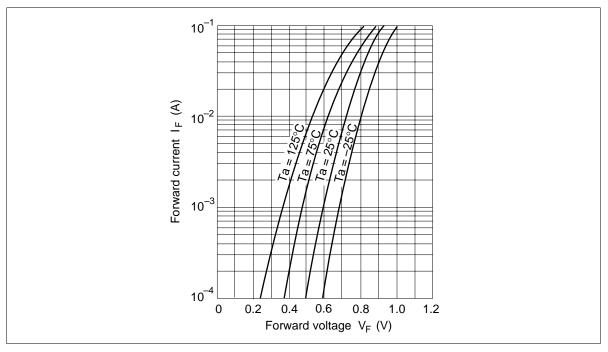


Fig.1 Forward current Vs. Forward voltage

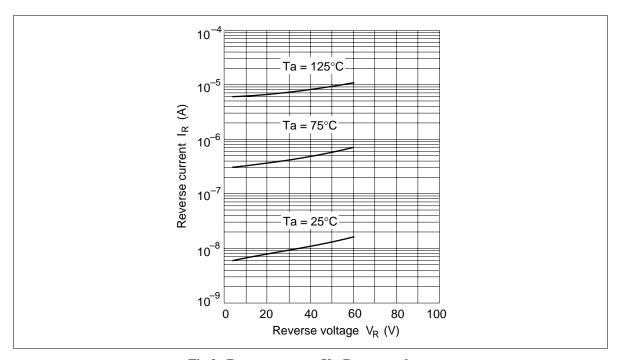


Fig.2 Reverse current Vs. Reverse voltage

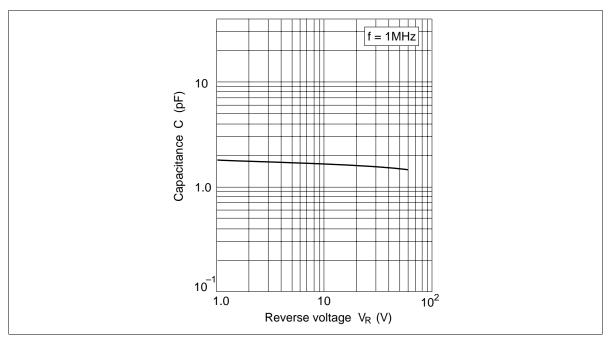
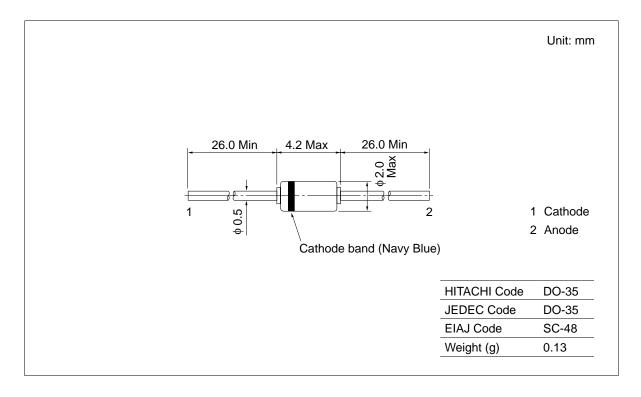


Fig.3 Capacitance Vs. Reverse voltage

#### **Package Dimensions**



#### **Cautions**

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