

## **AP78L05**

#### 5V OUTPUT 3-TERMINAL POSITIVE REGULATOR

#### **Features**

- 5.0V output voltage with tolerances of ±5% over the operating temperature ranges
- Output current in excess of 100mA
- Internal thermal overload protection
- Output transistor safe area protection
- · Internal short circuit current limiting
- No external components
- Available in plastic TO92-3L and plastic SOP-8L low profile packages
- Lead Free Finish / RoHS Compliant (Note 1)
- SOP-8L: Available in "Green" Molding Compound (No Br, Sb) (Note 2)

#### **General Description**

The AP78L05 is a three terminal positive regulator available with fixed output voltages. It can be used in a wide range of applications. It can provide local on-card regulation, eliminating the distribution problems associated with single point regulation. The voltages available allow the AP78L05 to be used in logic system, instrumentation, HiFi, and other solid state electronic equipments.

The AP78L05 is available in the plastic TO92-3L and SOP-8L package using industrial standard package technology. The regulator can deliver 100mA output current with adequate heat sinking. Current limiting is included to limit the peak output current to a safe value. Safe area protection for the output transistors is provided to limit internal power dissipation. Thermal overload protection is integrated to prevent the IC from overheat due to abnormal condition.

#### **Applications**

- Communication
- CD-ROM
- DVD-Player
- Set-Top Box

# Ordering Information

Package Lead Free Packing

V: TO92-3L L: Lead Free (Note 1) -A: Ammo Box
S: SOP-8L G: Green (Note 2) -13: Taping

Note:

- 1. TO92-3L is available in "Lead Free" product only.
- 2. SOP-8L is available in "Green" product only.
- 3. RoHS revision 13.2.2003. Glass and High Temperature Solder Exemptions Applied, see EU Directive Annex Notes 5 and 7.

	Paakaga		Packaging	Ammo Box / Tube		Tape and Reel		
	Device	Code	(Note 4)	Quantity	Part Number Suffix	Quantity	Part Number Suffix	
Pb	AP78L05V	V	TO92-3L	2000/Box	-A	NA	NA	
<b>(P)</b>	AP78L05S	S	SOP-8L	NA	NA	2500/Tape & Reel	-13	

Note: 4. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be on our website at <a href="http://www.diodes.com/datasheets/ap02001.pdf">http://www.diodes.com/datasheets/ap02001.pdf</a>.

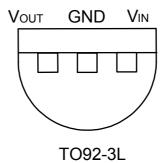




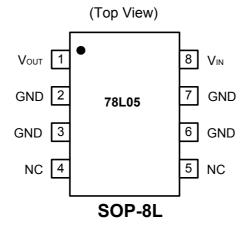
## **Pin Assignments**

#### (1) TO92-3L

(Bottom View)



#### (2) SOP-8L





# **Pin Descriptions**

Name	Description		
V <sub>IN</sub>	Operating Voltage Input		
V <sub>OUT</sub>	Voltage Output Pin		
GND	Ground		
NC	No Connection		

# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Unit
$V_{CC}$	Supply Voltage	+30	V
$V_{OUT}$	Output Voltage to Ground	5	V
T <sub>ST</sub>	Storage Temperature	-65 to +150	°C
T <sub>OP</sub>	Operating Junction Temperature	-20 to 125	°C
TMJ	Maximum Junction Temperature	150	°C



#### **Electrical Characteristics** (All Output Voltage Versions)

Limits in standard typeface are for T<sub>A</sub>=25°C, **Bold typeface applies over -20°Cto 125°C for TO92-3L and SOP-8L Packages.** Unless otherwise specified: V<sub>IN</sub>=10V, I<sub>O</sub>=40mA, C<sub>I</sub>=0.33 $\mu$ F, C<sub>O</sub>=0.1 $\mu$ F.

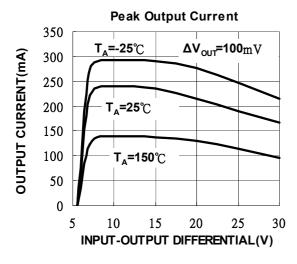
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
			4.8	5	5.2	
Vo	Output Voltage	7V≤V <sub>IN</sub> ≤20V 1mA≤I <sub>O</sub> ≤40mA	4.75		5.25	V
		1mA≤I <sub>O</sub> ≤70mA	4.75		5.25	
$\Delta V_{\rm O}$	Line Regulation	7V≤V <sub>IN</sub> ≤20V		18	75	mV
Δνο	Line Regulation	8V≤V <sub>IN</sub> ≤20V		10	54	111 V
$\Delta V_{\rm O}$	Load Regulation	1mA≤l <sub>O</sub> ≤100mA		20	60	mV
70		1mA≤l <sub>O</sub> ≤40mA		5	30	111 V
$I_{Q}$	Quiescent Current			3	5	
$\Delta l_Q$	Quiescent Current Change	8V≤V <sub>IN</sub> ≤20V			1.0	mA
ΔiQ	Quiescent Gunent Ghange	1mA≤l <sub>O</sub> ≤40mA			0.1	
V <sub>n</sub>	Output Noise Voltage	f=10Hz to 100kHz (Note 5)	-	40		μV
$\Delta V_{IN}/\Delta V_{OUT}$	Ripple Rejection	f=120Hz 8V≤V <sub>IN</sub> ≤16V	47	62		dB
I <sub>PK</sub>	Peak Output Current			140		mA
ΔV <sub>O</sub> /ΔΤ	Average Output Voltage Tempco	I <sub>O</sub> =5mA		-0.65		mV/°C
V <sub>IN</sub> (Min)	Minimum Value of Input Voltage Required to Maintain Line Regulation			6.7	7	V
$\theta_{JA}$	Thermal Resistance	TO92-3L (Note 6)		176		
OJA	Junction to Ambient	SOP-8L (Note 7)		153		°C/W
	Thermal Resistance Junction to Case	TO92-3L (Note 6)		33		C/VV
θ <sub>JC</sub>		SOP-8L (Note 7)		18		

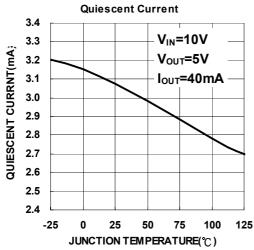
Note:

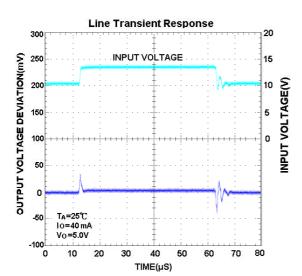
- Recommend  $0.01 \mu F$  minimum load capacitance at output to suppress high frequency noise.
- Test conditions for TO92-3L: No heat sink, no air flow.
   Test conditions for SOP-8L: Device mounted on 2oz copper, minimum recommended pad layout, FR-4 PCB.

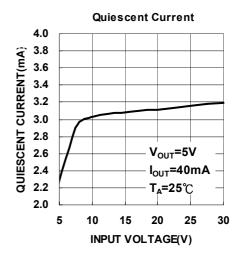


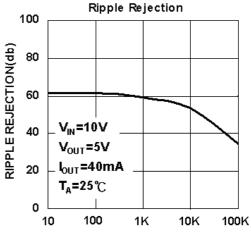
# **Typical Performance Characteristics**

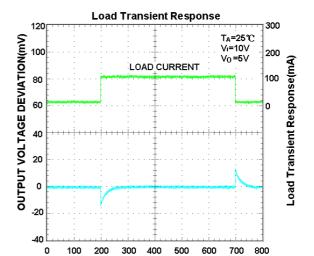










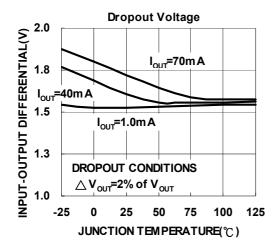


TIME(ms)

FREQUENCY(HZ)



## Typical Performance Characteristics (Continued)



## **Typical Application Circuit**

# IN AP78L05 OUT C1\* 0.33μ F C2\*\* 0.001μ F

Fixed Output regulator

- ★ : Required if the regulator is located more than 3" from the power supply filter.
- $\bigstar \bigstar$  : See (Note 5) in the electrical characteristics table.

## **Function Description**

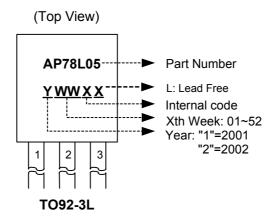
#### Introduction

The AP78L05 fixed-mode 5V output voltage regulator is a three terminal device. The AP78L05 fixed voltage regulator series has built-in thermal overload protection which prevents the device from being damaged due to excessive junction temperature. The regulator also contains internal short-circuit protection which limits the maximum output current, and safe-area protection for the pass transistor which reduces the short-circuit current as the voltage across the pass transistor is increased.

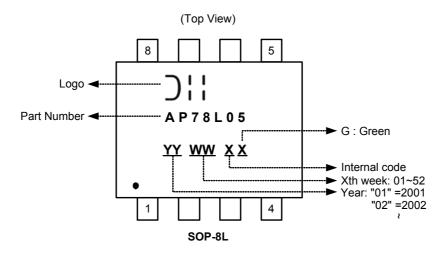


## **Marking Information**

#### (1) TO92-3L



#### (2) SOP-8L

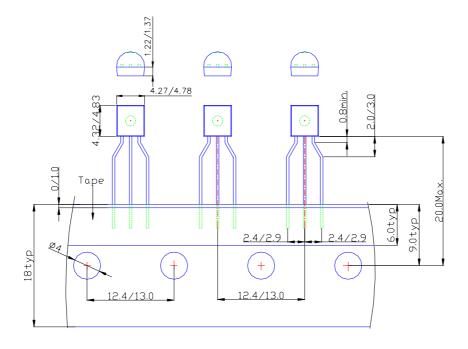


Device	Package	Identification Code
AP78L05S	SOP-8L	AP78L05

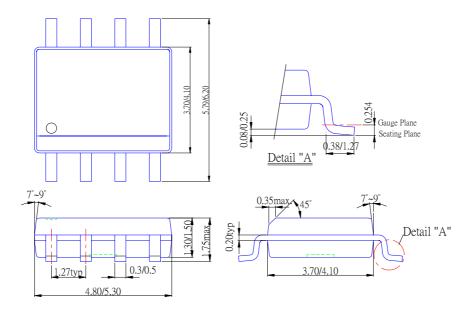


# Package Information (unit: mm)

#### (1) Package Type: TO92-3L



#### (2) Package Type: SOP-8L





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