

STGW30N90D

N-channel 900V - 30A - TO-247 Very fast PowerMESH™ IGBT

Preliminary Data

Features

Туре	V _{CES}	V _{CE(sat)} @25°C	I _C @100°C
STGW30N90D	900V	< 2.75V	30A

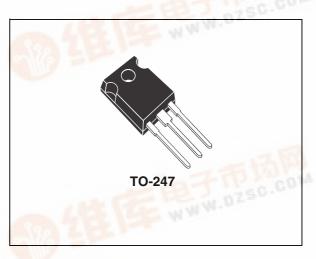
- Low on-losses
- Low on-voltage drop (V_{cesat})
- High current capability
- High input impedance (voltage driven)
- Low gate charge
- Ideal for soft switching application

Description

Using the latest high voltage technology based on its patented strip layout, STMicroelectronics has designed an advanced family of IGBTs, with outstanding performances.

Application

Induction heating





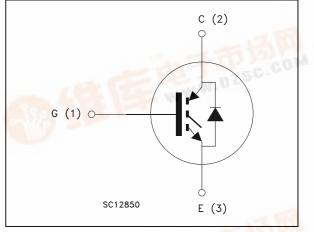


Table 1. Device summary

Order code	Marking	Package	Packaging
STGW30N90D	GW30N90D	TO-247	Tube
STGW30N90D	GW30N90D	TO-247	Tube



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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage ($V_{GS} = 0$)	900	V
I _C ⁽¹⁾	Collector current (continuous) at 25°C	60	А
I _C ⁽¹⁾	Collector current (continuous) at 100°C	30	Α
I _{CL} ⁽²⁾	Collector current (pulsed)	135	Α
V _{GE}	Gate-emitter voltage	±25	V
P _{TOT}	Total dissipation at $T_{C} = 25^{\circ}C$	220	W
۱ _f	Diode RMS forward current at $T_{C} = 25^{\circ}C$	30	А
Тj	Operating junction temperature	-55 to 150	
T _{stg}	Storage temperature	-33 10 150	°C

1. Calculated according to the iterative formula:

$$I_{C}(T_{C}) = \frac{T_{JMAX}^{-T}C}{R_{THJ-C} \times V_{CESAT(MAX)}(T_{C}, I_{C})}$$

2. Vclamp=900V, Tj=125°C, R_G =10 Ω , V_{GE} =15V

Table 3.Thermal resistance

Symbol	Parameter	Value	Unit
Rthj-case	Thermal resistance junction-case	0.57	°C/W
Rthj-amb	Thermal resistance junction-ambient (diode)	1.6	°C/W
Rthj-amb	Thermal resistance junction-ambient (IGBT)	50	°C/W



2 Electrical characteristics

(T_{CASE}=25°C unless otherwise specified)

Table 4.	Static electrical criara					
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{BR(CES)}	Collector-emitter breakdown voltage	I _C = 1mA, V _{GE} = 0	900			V
V _{CE(SAT)}	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 20A, Tj= 25°C V _{GE} = 15V, I _C = 20A, Tj=125°C		2.2 2.0	2.75	
V _{GE(th)}	Gate threshold voltage	$V_{CE} = V_{GE}$, $I_C = 250 \mu A$	3.75		5.75	V
I _{CES}	Collector-emitter leakage current (V _{CE} = 0)	V _{GE} =Max rating,Tc=25°C V _{GE} =Max rating, Tc=125°C			500 10	μA mA
I _{GES}	Gate-emitter leakage current (V _{CE} = 0)	$V_{GE} = \pm 20V, V_{CE} = 0$			± 100	nA
9 _{fs}	Forward transconductance	$V_{CE} = 25V_{,} I_{C} = 20A$		14		S

Table 4. Static electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{ies} C _{oes} C _{res}	Input capacitance Output capacitance Reverse transfer capacitance	V _{CE} = 25V, f = 1 MHz, V _{GE} =0		2510 175 30		pF pF pF
Q _g Q _{ge} Q _{gc}	Total gate charge Gate-emitter charge Gate-collector charge	V _{CE} = 900V, I _C = 20A,V _{GE} =15V		110 16 49	120	nC nC nC

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Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r (di/dt) _{on}	Turn-on delay time Current rise time Turn-on current slope	$V_{CC} = 900V, I_C = 20A$ $R_G = 10\Omega, V_{GE} = 15V,$ $Tj=25^{\circ}C$ (see Figure 2)		29 11 1820		ns ns A/µs
t _{d(on)} t _r (di/dt) _{on}	Turn-on delay time Current rise time Turn-on current slope	$V_{CC} = 900V, I_C = 20A$ $R_G = 10\Omega, V_{GE} = 15V,$ $Tj = 125^{\circ}C$ <i>(see Figure 2)</i>		27 14 1580		ns ns A/µs
t _r (V _{off}) t _{d(off}) t _f	Off voltage rise time Turn-off delay time Current fall time	$V_{CC} = 900V$, $I_C = 20A$ $R_G = 10\Omega$, $V_{GE} = 15V$, $Tj = 25^{\circ}C$ <i>(see Figure 2)</i>		90 275 312		ns ns ns
t _r (V _{off}) t _d (_{off}) t _f	Off voltage rise time Turn-off delay time Current fall time	$V_{CC} = 900V$, $I_C = 20A$ $R_G = 10\Omega$, $V_{GE} = 15V$, $Tj = 125^{\circ}C$ <i>(see Figure 2)</i>		150 336 592		ns ns ns

Table 6. Switching on/off (inductive load)

Table 7. Switching energy (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Eon ⁽¹⁾ E _{off} ⁽²⁾ E _{ts}	Turn-on switching losses Turn-off switching losses Total switching losses	$V_{CC} = 900V, I_C = 20A$ $R_G = 10\Omega, V_{GE} = 15V,$ $T_j = 25^{\circ}C$ <i>(see Figure 2)</i>		1660 4438 6096		μЈ μЈ μЈ
Eon ⁽¹⁾ E _{off} ⁽²⁾ E _{ts}	Turn-on switching losses Turn-off switching losses Total switching losses	$V_{CC} = 900V, I_C = 20A$ $R_G = 10\Omega, V_{GE} = 15V,$ $T_j = 125^{\circ}C$ <i>(see Figure 2)</i>		3015 6900 9915		μJ μJ μJ

 Eon is the turn-on losses when a typical diode is used in the test circuit in figure 2. If the IGBT is offered in a package with a co-pack diode, the co-pack diode is used as external diode. IGBTs & Diode are at the same temperature (25°C and 125°C)

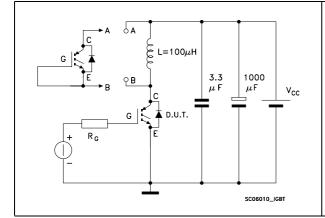
2. Turn-off losses include also the tail of the collector current

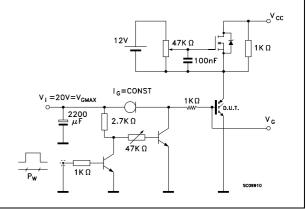
Table 8.	Collector-emitter	diode
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Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _f	Forward on-voltage	If = 20A, Tj = 25°C		1.9	2.5	V
۷f		lf = 20A, Tj = 125°C		1.7		V
t _{rr}	Reverse recovery time	If = 20A, V _R = 27V,		152		ns
Q _{rr}	Reverse recovery charge	T _i = 125°C, di/dt = 100A/μs		722		nC
I _{rrm}	Reverse recovery current	(see Figure 5)		9		А

3 Test circuit

Figure 2. Test circuit for inductive load switching





Gate charge test circuit



Figure 5. Diode recovery time waveform

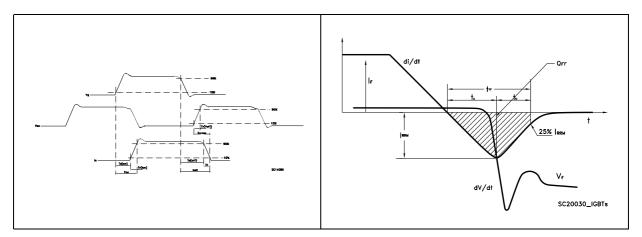


Figure 3.

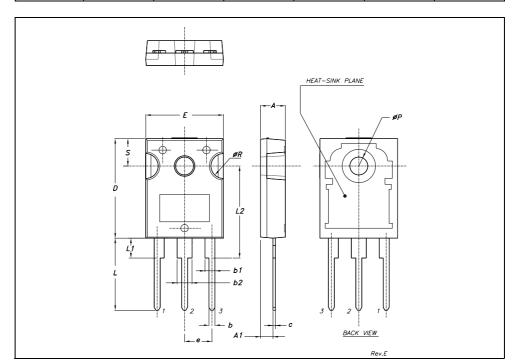
4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: *www.st.com*



DIM.	mm.			inch		
	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.
А	4.85		5.15	0.19		0.20
A1	2.20		2.60	0.086		0.102
b	1.0		1.40	0.039		0.055
b1	2.0		2.40	0.079		0.094
b2	3.0		3.40	0.118		0.134
С	0.40		0.80	0.015		0.03
D	19.85		20.15	0.781		0.793
Е	15.45		15.75	0.608		0.620
е		5.45			0.214	
L	14.20		14.80	0.560		0.582
L1	3.70		4.30	0.14		0.17
L2		18.50			0.728	
øP	3.55		3.65	0.140		0.143
øR	4.50		5.50	0.177		0.216
S		5.50			0.216	





5 Revision history

Table 9.	Revision history
Table 3.	nevision mistory

Date	Revision	Changes
19-Jul-2006	1	First issue.



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