

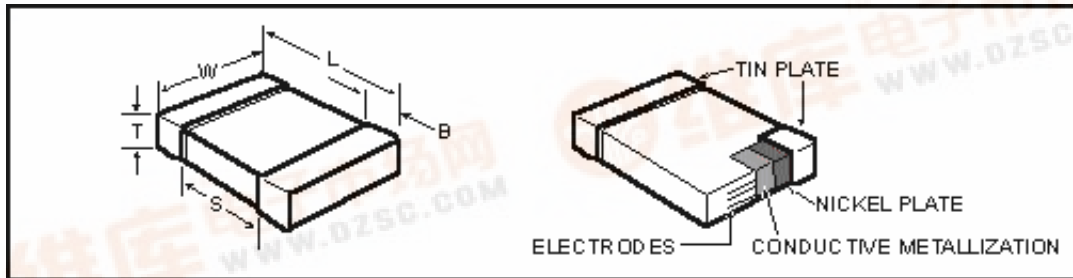


## New Product Bulletin

### Surface Mount Ceramic Chip Capacitors X7R Dielectric

X7R/1206 - 2.7  $\mu$ F (275) - 10.0  $\mu$ F (106) @ 6.3; 10 Volts

#### Outline Drawing



**Table 1**  
**Dimensions - Millimeters (Inches)**

Metric Size Code	EIA Size Code	L - Length	W - Width	Max. T - Thickness	B - Bandwidth	Separation
3216	1206	3.2 (.126) $\pm$ 0.2 (.008)	1.6 (.063) $\pm$ 0.2 (.008)	See Table 2	0.5 (.02) $\pm$ 0.25 (.010)	N/A

**Table 2 - X7R - Capacitance Value Extensions**

Capacitance Values ( $\mu$ F)	KEMET Part Number	Capacitance Tolerance	Thickness mm	Qty 7" Reel	Qty 13" Reel
2.7	C1206C275(1)(2)RAC	K,M	0.95 $\pm$ 0.1	4,000	10,000
3.3	C1206C335(1)(2)RAC	K,M	1.0 $\pm$ 0.1	2,500	10,000
3.9	C1206C395(1)(2)RAC	K,M	1.15 $\pm$ 0.15	2,500	10,000
4.7	C1206C475(1)(2)RAC	K,M	1.25 $\pm$ 0.15	2,500	10,000
5.6	C1206C565(1)(2)RAC	K,M	1.6 $\pm$ 0.2	2,000	8,000
6.8	C1206C685(1)(2)RAC	K,M	1.6 $\pm$ 0.2	2,000	8,000
8.2	C1206C825(1)(2)RAC	K,M	1.6 $\pm$ 0.2	2,000	8,000
10.0	C1206C106(1)(2)RAC	K,M	1.6 $\pm$ 0.2	2,000	8,000

(1) To complete KEMET part number, insert the alpha code for tolerance desired. K =  $\pm$ 10%; M =  $\pm$ 20%

(2) To complete KEMET part number, insert the numeric code for voltage desired. 9 = 6.3 volts; 8 = 10 volts



### **Electrical Parameters**

As detailed in the KEMET Surface Mount Catalog F3102 for X7R, with following specific requirements based on room temperature (25°C) parameters:

- Operating Range: -55°C to +125°C, with no-bias capacitance shift limited to  $\pm 15\%$  over that range
- Insulation Resistance (IR) measured after 2 minutes at rated voltage @ 25°C: Limit is 500 megohm-microfarads.
- Capacitance and Dissipation Factor (DF) measured at 1KHz at 1.0Vrms. DF Limit for 6.3 and 10 volts is 5.0%. DF for 16 volt is 3.5%.

### **Soldering Process**

The 1206 components are suitable for reflow and wave soldering. All parts incorporate the standard KEMET barrier layer of pure nickel, with an overplate of pure tin to provide excellent solderability as well as resistance to leaching.

### **Marking**

These chips are normally supplied unmarked. If required, they can be laser marked as an extra cost option. More detail on the marking format is included in our Surface Mount Catalog F3102.

***In general, the information in the KEMET Surface Mount catalog F3102 applies to these capacitors. The information in this bulletin supplements that in the catalog.***