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BIPOLAR ANALOG INTEGRATED CIRCUIT

 μ PC3237TK

LOW NOISE WIDE BAND SILICON GERMANIUM MMIC AMPLIFIER FOR MOBILE COMMUNICATIONS

DESCRIPTION

The μ PC3237TK is a silicon germanium (SiGe) monolithic integrated circuit designed as low noise amplifier for the mobile digital TV etc. This device exhibits low noise figure and high power gain characteristics.

The package is 6-pin lead-less minimold, suitable for surface mount.

This IC is manufactured using our UHS2 (Ultra High Speed Process) SiGe bipolar process.

FEATURES

• Supply voltage : Vcc = 2.4 to 3.3 V (2.8 V TYP.)

Low current consumption
 Icc = 5 mA TYP. @ Vcc = 2.8 V

• Low noise : NF = 1.4 dB TYP. @ f = 470 MHz

NF = 1.5 dB TYP. @ f = 770 MHz

• Power gain : G_P = 15.3 dB TYP. @ f = 470 MHz

 $G_P = 13.5 \text{ dB TYP.} @ f = 770 \text{ MHz}$

• High-density surface mounting : 6-pin lead-less minimold package $(1.5 \times 1.1 \times 0.55 \text{ mm})$

APPLICATION

· Low noise amplifier for the mobile digital TV etc.

ORDERING INFORMATION

P art Number	Order Number	Package	Marking	S upplying Form
μPC3237TK-E2	μPC3237TK-E2-A	6-pin lead-less minimold (1511 PKG) (Pb-Free)	6N	 8 mm wide embossed taping Pin 1, 6 face the perforation side of the tape Qty 5 kpcs/reel

Remark To order evaluation samples, contact your nearby sales office.

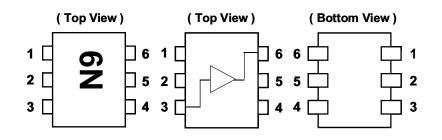
Part number for sample order: μ PC3237TK-A



Caution Observe precautions when handling because these devices are sensitive to electrostaic discharge.

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

PIN CONNECTIONS



Pin No.	P in Name
1	NC
2	GND
3	INPUT
4	V cc
5	GND
6	OUTPUT

ABSOLUTE MAXIMUM RATINGS

Parameter	S ymbol	T est C onditions	R atings	Unit
S upply V oltage	Vcc	TA = +25°C	3.6	٧
Circuit Current	lcc	TA = +25°C	10	mA
Power Dissipation	Po	T _A = +85°C Note	203	mW
Operating Ambient Temperature	ТА		-40 to +85	°C
S torage Temperature	T _{stg}		-55 to +150	°C
Input Power	Pin		+8	dB m

Note Mounted on double-side copper-clad $50 \times 50 \times 1.6$ mm epoxy glass PWB

RECOMMENDED OPERATING RANGE

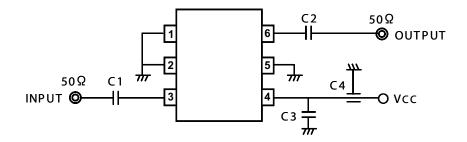
Parameter	S ymbol	MIN.	TYP.	MAX.	Unit
S upply V oltage	V cc	2.4	2.8	3.3	V
Operating Ambient Temperature	Та	-40	+25	+85	°C

ELECTRICAL CHARACTERISTICS

(TA = +25°C, Vcc = 2.8 V, Zs = ZL= $50\,\Omega$, unless otherwise specified)

P arameter	S ymbol	T est C onditions	MIN.	TYP.	MAX.	Unit
Circuit Current	lcc	No S ignal	3.5	5	7	mA
Power Gain	G₽1	f = 470MHz, P _{in} = -30 dBm	13.0	15.3	17.5	dB
	G₽2	f = 770MHz, P _{in} = -30 dBm	11.0	13.5	16.0	dB
Nois e Figure	NF1	f = 470MHz	_	1.4	1.9	dB
	NF2	f = 770MHz	_	1.5	2.0	dB
Input R eturn Loss	R Lin1	f = 470MHz, P _{in} = -30 dBm	6.5	9.5	=	dB
	R Lin2	f = 770MHz, P _{in} = -30 dBm	5.5	8.5	=	dB
Output R eturn Loss	R Lout1	f = 470MHz, P _{in} = -30 dBm	9	14	=	dB
	R Lout2	f = 770MHz, P _{in} = -30 dBm	10	15	_	dB
Isolation	IS L 1	f = 470MHz, P _{in} = -30 dBm	17	22	-	dB
	IS L2	f = 770MHz, P _{in} = -30 dBm	16	21	-	dB
Gain 1 dB Compression Output	P o (1 dB)1	f = 470MHz	-8	-5.5	-	dB m
Power	P O (1 dB)2	f = 770MHz	-8	-5.5	-	dB m

TEST CIRCUIT



COMPONENTS LIST

COMI CHENTO LIGI					
S ymbol	Туре	Value			
C1, C2	Chip Capacitor	100pF			
C3	Chip Capacitor	1000pF			
C4	Feed-through Capacitor	1000pF			

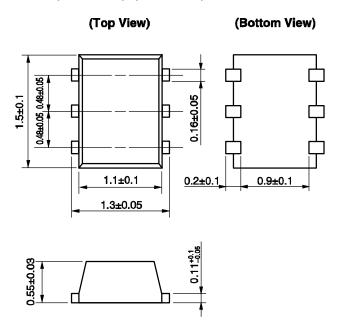
STANDARD CHARACTERISTICS FOR REFERENCE

(TA = $+25^{\circ}$ C, Vcc = 2.8 V, Zs = ZL = 50Ω , unless otherwise specified)

Parameter	S ymbol	Test Conditions	R eference	Unit
S aturated Output Power	Po (sat)1	f = 470MHz, P _{in} = 2 dB m	1.3	dB m
	Po (sat)2	f = 770MHz, P _{in} = 2 dB m	1.3	dB m
Input 3rd Order Distortion	IIP3 1	f1=470MHz, f2=471MHz	—10.5	dB m
Intercept P oint	IIP3 2	f1=770MHz, f2=771MHz	−9.5	dB m
Output 3rd Order Distortion	OIP3 1	f1=470MHz, f2=471MHz	4.8	dB m
Intercept Point	OIP3 2	f1=770MHz, f2=771MHz	4.0	dB m
K factor	K 1	f = 470MHz	1.15	_
	K 2	f = 770MHz	1.20	_

PACKAGE DIMENSIONS

6-PIN LEAD-LESS MINIMOLD (1511 PKG) (UNIT: mm)



NOTES ON CORRECT USE

- (1) Observe precautions for handling because of electro-static sensitive devices.
- (2) Form a ground pattern as widely as possible to minimize ground impedance (to prevent undesired oscillation). All the ground terminals must be connected together with wide ground pattern to decrease impedance difference.
- (3) The bypass capacitor should be attached to Vcc line.
- (4) The DC cut capacitor should be attached to Input and Output pin.
- (5) Pin 1 (NC) should be connected to the ground pattern.

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

S oldering Method	S oldering C onditions		C ondition S ymbol
Infrared R eflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt) or below	IR 260
W ave S oldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2% (W t) or below	WS 260
Partial Heating	Peak temperature (terminal temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

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Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)		on contained devices	
Lead (Pb)	< 1000 PPM	-A -AZ Not Detected (*)		
Mercury	< 1000 PPM	Not Detected		
Cadmium	< 100 PPM	Not Detected		
Hexavalent Chromium	< 1000 PPM	Not Detected		
PBB	< 1000 PPM	Not Detected		
PBDE	< 1000 PPM	Not Detected		

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