

- Qualification in Accordance With AEC-Q100†
- Qualified for Automotive Applications
- Customer-Specific Configuration Control Can Be Supported Along With Major-Change Approval
- ESD Protection Exceeds 2000 V Per MIL-STD-883, Method 3015; Exceeds 200 V Using Machine Model (C = 200 pF, R = 0)
- Outstanding Combination of DC Precision and AC Performance:
  - Unity-Gain Bandwidth . . . 15 MHz Typ
  - $V_n$  . . . 3.3 nV/ $\sqrt{\text{Hz}}$  at  $f = 10 \text{ Hz}$  Typ,  
2.5 nV/ $\sqrt{\text{Hz}}$  at  $f = 1 \text{ kHz}$  Typ
  - $V_{IO}$  . . . 25  $\mu\text{V}$  Max
  - $A_{VD}$  . . . 45 V/ $\mu\text{V}$  Typ With  $R_L = 2 \text{ k}\Omega$ ,  
19 V/ $\mu\text{V}$  Typ With  $R_L = 600 \Omega$

† Contact factory for details. Q100 qualification data available on request.

## description

The TLE20x7 and TLE20x7A contain innovative circuit design expertise and high-quality process control techniques to produce a level of ac performance and dc precision previously unavailable in single operational amplifiers. Manufactured using Texas Instruments state-of-the-art Excalibur process, these devices allow upgrades to systems that use lower-precision devices.

In the area of dc precision, the TLE20x7 and TLE20x7A offer maximum offset voltages of 100  $\mu\text{V}$  and 25  $\mu\text{V}$ , respectively, common-mode rejection ratio of 131 dB (typ), supply voltage rejection ratio of 144 dB (typ), and dc gain of 45 V/ $\mu\text{V}$  (typ).

The ac performance of the TLE2027 and TLE2037 is highlighted by a typical unity-gain bandwidth specification of 15 MHz, 55° of phase margin, and noise voltage specifications of 3.3 nV/ $\sqrt{\text{Hz}}$  and 2.5 nV/ $\sqrt{\text{Hz}}$  at frequencies of 10 Hz and 1 kHz, respectively. The TLE2037 and TLE2037A have been decompensated for faster slew rate (−7.5 V/ $\mu\text{s}$ , typical) and wider bandwidth (50 MHz). To ensure stability, the TLE2037 and TLE2037A should be operated with a closed-loop gain of 5 or greater.

## ORDERING INFORMATION

$T_A$	$V_{IO\text{max}}$ AT 25°C	PACKAGE‡		ORDERABLE PART NUMBER	TOP-SIDE MARKING
−40°C to 125°C	25 $\mu\text{V}$	SOIC (D)	Tape and reel	TLE2027AQDRQ1	2027AQ
	100 $\mu\text{V}$			TLE2037AQDRQ1	2037AQ
	SOIC (D)	Tape and reel	TLE2027QDRQ1	2027Q1	
				TLE2037QDRQ1	2037Q1

‡ Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).

 Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

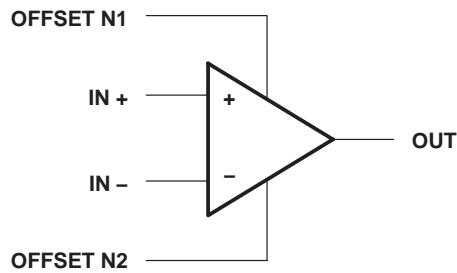
# TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1 EXCALIBUR LOW-NOISE HIGH-SPEED PRECISION OPERATIONAL AMPLIFIERS

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

## description (continued)

Both the TLE20x7 and TLE20x7A are available in a wide variety of packages, including the industry-standard 8-pin small-outline version for high-density system applications. The Q-suffix devices are characterized for operation from  $-40^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ .

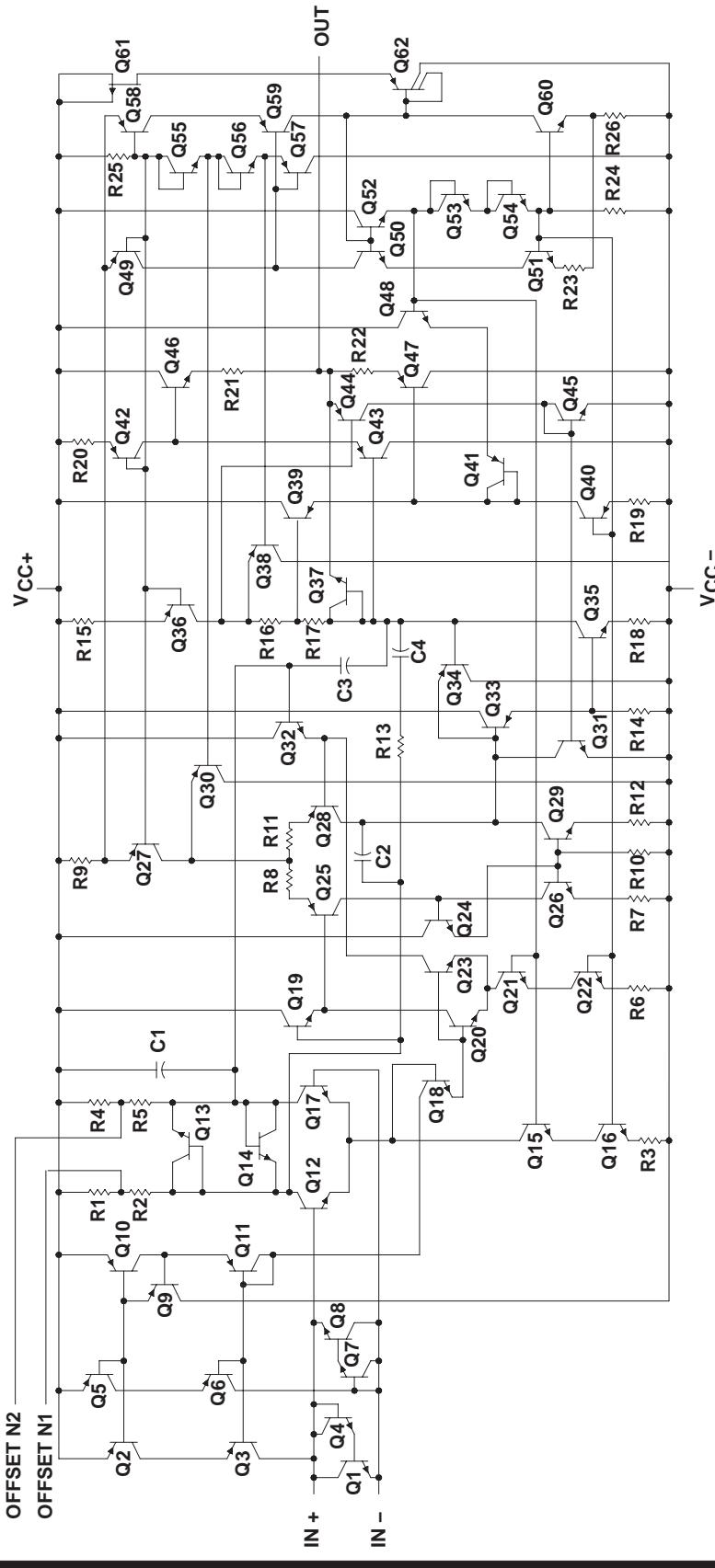
## symbol



**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGS202A – OCTOBER 2003 – REVISED OCTOBER 2006

equivalent schematic



ACTUAL DEVICE COMPONENT COUNT		
COMPONENT	TLE2027	TLE2037
Transistors	61	61
Resistors	26	26
epiFET	1	1
Capacitors	4	4

# TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1

## EXCALIBUR LOW-NOISE HIGH-SPEED

## PRECISION OPERATIONAL AMPLIFIERS

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

Supply voltage, $V_{CC+}$ (see Note 1)	.....	19 V
Supply voltage, $V_{CC-}$	.....	-19 V
Differential input voltage, $V_{ID}$ (see Note 2)	.....	$\pm 1.2$ V
Input voltage range, $V_I$ (any input)	.....	$V_{CC\pm}$
Input current, $I_I$ (each Input)	.....	$\pm 1$ mA
Output current, $I_O$	.....	$\pm 50$ mA
Total current into $V_{CC+}$	.....	50 mA
Total current out of $V_{CC-}$	.....	50 mA
Duration of short-circuit current at (or below) 25°C (see Note 3)	.....	Unlimited
Junction temperature, $T_J$	.....	142°C
Operating free-air temperature range, $T_A$ : Q suffix	.....	-40°C to 125°C
Storage temperature range, $T_{stg}$	.....	-65°C to 150°C
Package thermal impedance, $\theta_{JA}$ (D Package) (0 LFPM) (see Note 4)	.....	101°C/W
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds: D package	.....	260°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All voltage values, except differential voltages, are with respect to the midpoint between  $V_{CC+}$  and  $V_{CC-}$ .  
 2. Differential voltages are at  $IN+$  with respect to  $IN-$ . Excessive current flows if a differential input voltage in excess of approximately  $\pm 1.2$  V is applied between the inputs, unless some limiting resistance is used.  
 3. The output may be shorted to either supply. Temperature and/or supply voltages must be limited to ensure that the maximum dissipation rating is not exceeded.  
 4. The thermal impedance is calculated in accordance with JESD 51-7.

### recommended operating conditions

		MIN	MAX	UNIT
Supply voltage, $V_{CC\pm}$		$\pm 4$	$\pm 19$	V
Common-mode input voltage, $V_{IC}$	$T_A = 25^\circ\text{C}$	-11	11	V
		-10.2	10.2	
Operating free-air temperature, $T_A$		-40	125	°C

<sup>‡</sup> Full range is -40°C to 125°C for Q-suffix devices.

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TLE20x7-Q1 electrical characteristics at specified free-air temperature,  $V_{CC\pm} = \pm 15$  V (unless otherwise noted)**

PARAMETER	TEST CONDITIONS	$T_A^\dagger$	TLE20x7-Q1			TLE20x7A-Q1			UNIT
			MIN	TYP	MAX	MIN	TYP	MAX	
$V_{IO}$	$V_{IC} = 0$ , $R_S = 50\Omega$	25°C	20	100	100	10	25	25	$\mu V$
		Full range		200			105		
		Full range	0.4	1	1	0.2	1	1	$\mu V^\circ C$
		25°C	0.006	1	1	0.006	1	1	$\mu V/mo$
		25°C	6	90	90	6	90	90	$nA$
		Full range		150			150		
		25°C	15	90	90	15	90	90	$nA$
		Full range		150			150		
$V_{ICR}$	$R_S = 50\Omega$	25°C	-11 to 11	-13 to 13		-11 to 11	-13 to 13		$V$
		Full range	-10.3 to 10.3			-10.4 to 10.4			
$V_{OM} +$	$R_L = 600\Omega$	25°C	10.5	12.9	12.9	10.5	12.9	12.9	$V$
		Full range	10		10		10		
	$R_L = 2\text{ k}\Omega$	25°C	12	13.2	13.2	12	13.2	13.2	
		Full range	11		11		11		
$V_{OM} -$	$R_L = 600\Omega$	25°C	-10.5	-13		-10.5	-13		$V$
		Full range	-10			-10			
	$R_L = 2\text{ k}\Omega$	25°C	-12	-13.5		-12	-13.5		
		Full range	-11			-11			
$A_{VD}$	$V_O = \pm 11\text{ V}$ , $R_L = 2\text{ k}\Omega$	25°C	5	45	45	10	45	45	$V/\mu V$
	$V_O = \pm 10\text{ V}$ , $R_L = 2\text{ k}\Omega$	Full range	2.5			3.5			
	$V_O = \pm 10\text{ V}$ , $R_L = 1\text{ k}\Omega$	25°C	3.5	38	38	8	38	38	
		Full range	1.8			2.2			
	$V_O = \pm 10\text{ V}$ , $R_L = 600\Omega$	25°C	2	19	19	5	19	19	
$C_i$	Input capacitance	25°C		8			8		$\text{pF}$
$z_o$	Open-loop output impedance	$I_O = 0$	25°C		50		50		$\Omega$
CMRR	Common-mode rejection ratio	$V_{IC} = V_{ICR\text{min}}$ , $R_S = 50\Omega$	25°C	100	131	117	131	131	$\text{dB}$
			Full range	96		113			
$k_{SVR}$	Supply-voltage rejection ratio ( $\Delta V_{CC\pm} / \Delta V_{IO}$ )	$V_{CC\pm} = \pm 4\text{ V to } \pm 18\text{ V}$ , $R_S = 50\Omega$	25°C	94	144	110	144	144	$\text{dB}$
		$V_{CC\pm} = \pm 4\text{ V to } \pm 18\text{ V}$ , $R_S = 50\Omega$	Full range	90		105			
$I_{CC}$	Supply current	$V_O = 0$ , No load	25°C	3.8	5.3	3.8	5.3	5.3	$\text{mA}$
			Full range		5.6		5.6		

<sup>†</sup> Full range is  $-40^\circ\text{C}$  to  $125^\circ\text{C}$ .

NOTE 4: Typical values are based on the input offset voltage shift observed through 168 hours of operating life test at  $T_A = 150^\circ\text{C}$  extrapolated to  $T_A = 25^\circ\text{C}$  using the Arrhenius equation and assuming an activation energy of 0.96 eV.

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TLE20x7-Q1 operating characteristics at specified free-air temperature,  $V_{CC} \pm \pm 15$  V,  $T_A = 25^\circ\text{C}$  (unless otherwise specified)**

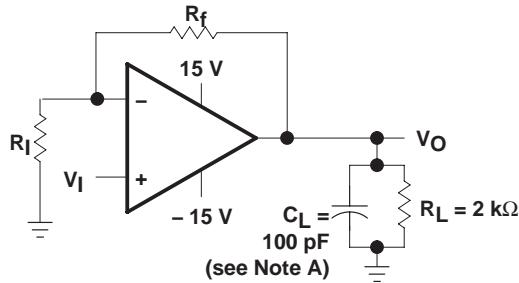
PARAMETER	TEST CONDITIONS	TLE20x7-Q1			TLE20x7A-Q1			UNIT
		MIN	TYP	MAX	MIN	TYP	MAX	
SR	$R_L = 2 \text{ k}\Omega$ , $C_L = 100 \text{ pF}$ , See Figure 1	TLE2027	1.7	2.8	1.7	2.8		V/ $\mu$ s
		TLE2037	6	7.5	6	7.5		
	$R_L = 2 \text{ k}\Omega$ , $C_L = 100 \text{ pF}$ , $T_A = -55^\circ\text{C}$ to $125^\circ\text{C}$ , See Figure 1	TLE2027	1		1			
		TLE2037	4.4		4.4			
$V_n$	Equivalent input noise voltage (see Figure 2)	$R_S = 20 \Omega$ , $f = 10 \text{ Hz}$		3.3	8	3.3	4.5	nV/ $\sqrt{\text{Hz}}$
		$R_S = 20 \Omega$ , $f = 1 \text{ kHz}$		2.5	4.5	2.5	3.8	
$V_{N(PP)}$	Peak-to-peak equivalent input noise voltage	$f = 0.1 \text{ Hz}$ to $10 \text{ Hz}$		50	250	50	130	nV
$I_n$	Equivalent input noise current	$f = 10 \text{ Hz}$		10		10		pA/ $\sqrt{\text{Hz}}$
		$f = 1 \text{ kHz}$		0.8		0.8		
THD	Total harmonic distortion	$V_O = +10 \text{ V}$ , $A_{VD} = 1$ , See Note 5	TLE2027	<0.002		<0.002		%
		$V_O = +10 \text{ V}$ , $A_{VD} = 5$ , See Note 5	TLE2037	<0.002		<0.002		
$B_1$	Unity-gain bandwidth (see Figure 3)	$R_L = 2 \text{ k}\Omega$ , $C_L = 100 \text{ pF}$	TLE2027	7	13	9	13	MHz
			TLE2037	35	50	35	50	
$B_{OM}$	Maximum output-swing bandwidth	$R_L = 2 \text{ k}\Omega$	TLE2027	30		30		kHz
			TLE2037	80		80		
$\phi_m$	Phase margin at unity gain (see Figure 3)	$R_L = 2 \text{ k}\Omega$ , $C_L = 100 \text{ pF}$	TLE2027	55		55		°
			TLE2037	50		50		

NOTE 5: Measured distortion of the source used in the analysis was 0.002%.

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

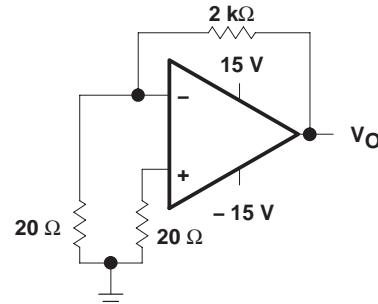
SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**PARAMETER MEASUREMENT INFORMATION**

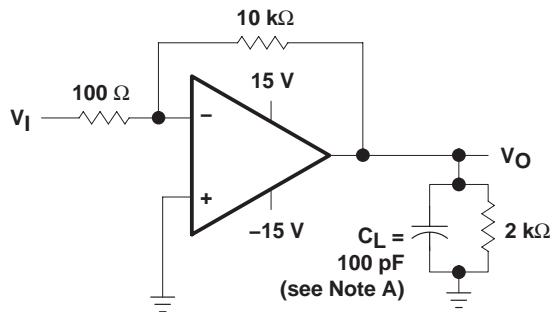


NOTE A:  $C_L$  includes fixture capacitance.

**Figure 1. Slew-Rate Test Circuit**

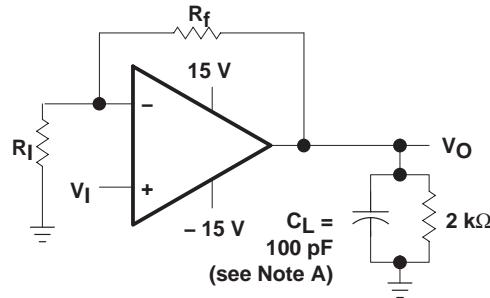


**Figure 2. Noise-Voltage Test Circuit**



NOTE A:  $C_L$  includes fixture capacitance.

**Figure 3. Unity-Gain Bandwidth and Phase-Margin Test Circuit (TLE2027 Only)**



NOTES: A.  $C_L$  includes fixture capacitance.  
 B. For the TLE2037 and TLE2037A,  $AVD$  must be  $\geq 5$ .

**Figure 4. Small-Signal Pulse-Response Test Circuit**

# TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1 EXCALIBUR LOW-NOISE HIGH-SPEED PRECISION OPERATIONAL AMPLIFIERS

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

## typical values

Typical values presented in this data sheet represent the median (50% point) of device parametric performance.

## initial estimates of parameter distributions

In the ongoing program of improving data sheets and supplying more information to our customers, Texas Instruments has added an estimate of not only the typical values, but also the spread around these values. These are in the form of distribution bars that show the 95% (upper) points and the 5% (lower) points from the characterization of the initial wafer lots of this new device type (see Figure 5). The distribution bars are shown at the points where data was actually collected. The 95% and 5% points are used instead of  $\pm 3$  sigma, since some of the distributions are not true Gaussian distributions.

The number of units tested and the number of different wafer lots used are on all of the graphs where distribution bars are shown. As noted in Figure 5, there were a total of 835 units from two wafer lots. In this case, there is a good estimate for the within-lot variability and a possibly poor estimate of the lot-to-lot variability. This is always the case on newly released products, since there can only be data available from a few wafer lots.

The distribution bars are not intended to replace the minimum and maximum limits in the electrical tables. Each distribution bar represents 90% of the total units tested at a specific temperature. While 10% of the units tested fell outside any given distribution bar, this should not be interpreted to mean that the same individual devices fell outside every distribution bar.

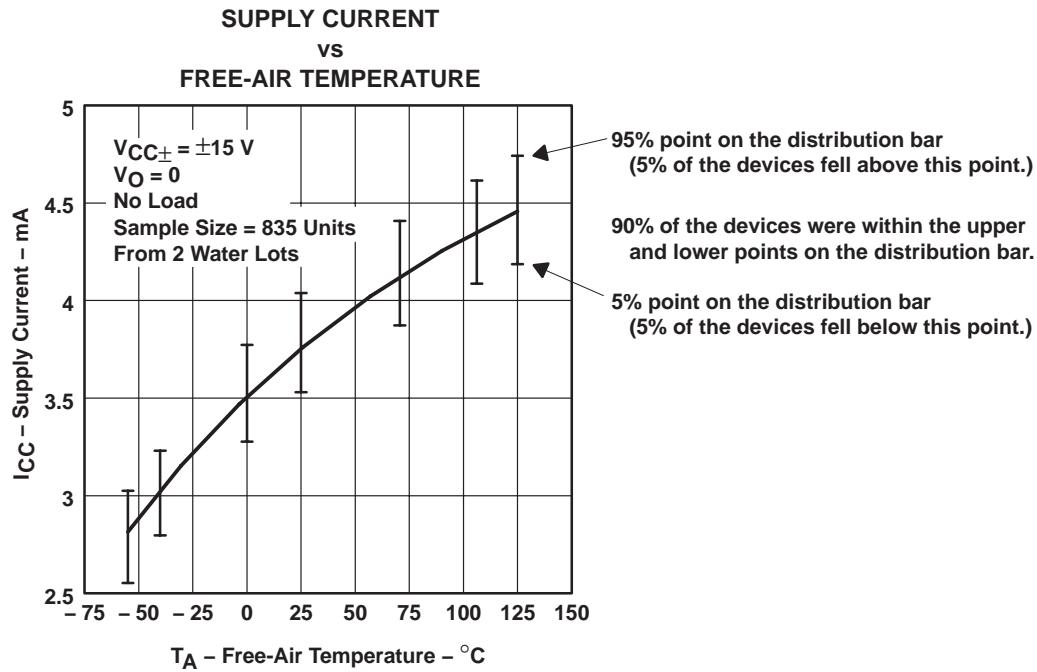


Figure 5. Sample Graph With Distribution Bars

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**

**Table of Graphs**

			<b>FIGURE</b>	
$V_{IO}$	Input offset voltage	Distribution	6, 7	
$\Delta V_{IO}$	Input offset voltage change	vs Time after power on	8, 9	
$I_{IO}$	Input offset current	vs Free-air temperature	10	
$I_{IB}$	Input bias current	vs Free-air temperature	11	
		vs Common-mode input voltage	12	
$I_I$	Input current	vs Differential input voltage	13	
$V_{O(PP)}$	Maximum peak-to-peak output voltage	vs Frequency	14, 15	
$V_{OM}$	Maximum (positive/negative) peak output voltage	vs Load resistance	16, 17	
		vs Free-air temperature	18, 19	
$A_{VD}$	Large-signal differential voltage amplification	vs Supply voltage	20	
		vs Load resistance	21	
		vs Frequency	22 – 25	
		vs Free-air temperature	26	
$z_0$	Output impedance	vs Frequency	27	
CMRR	Common-mode rejection ratio	vs Frequency	28	
$k_{SVR}$	Supply-voltage rejection ratio	vs Frequency	29	
$I_{OS}$	Short-circuit output current	vs Supply voltage	30, 31	
		vs Elapsed time	32, 33	
		vs Free-air temperature	34, 35	
$I_{CC}$	Supply current	vs Supply voltage	36	
		vs Free-air temperature	37	
Voltage-follower pulse response		Small signal	38, 40	
		Large signal	39, 41	
$V_n$	Equivalent input noise voltage	vs Frequency	42	
		Noise voltage (referred to input)	Over 10-second interval	
$B_1$	Unity-gain bandwidth	vs Supply voltage	44	
		vs Load capacitance	45	
Gain bandwidth product		vs Supply voltage	46	
		vs Load capacitance	47	
SR	Slew rate	vs Free-air temperature	48, 49	
$\phi_m$	Phase margin	vs Supply voltage	50, 51	
		vs Load capacitance	52, 53	
		vs Free-air temperature	54, 55	
Phase shift		vs Frequency	22 – 25	

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**

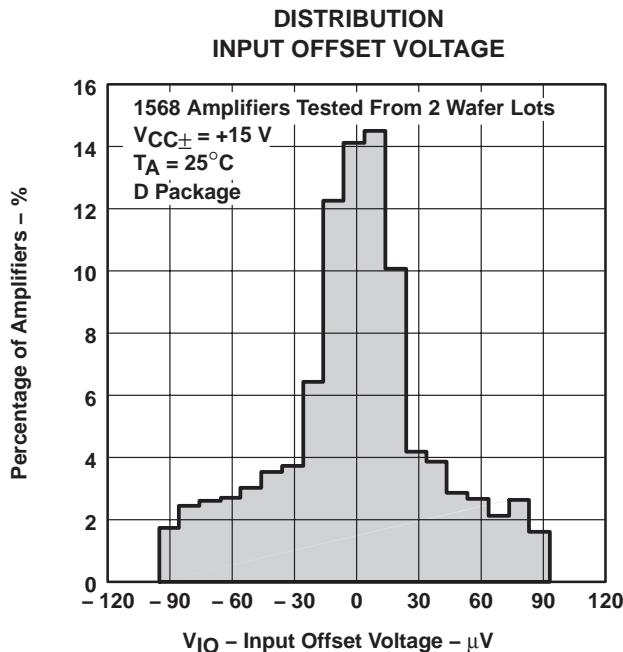


Figure 6

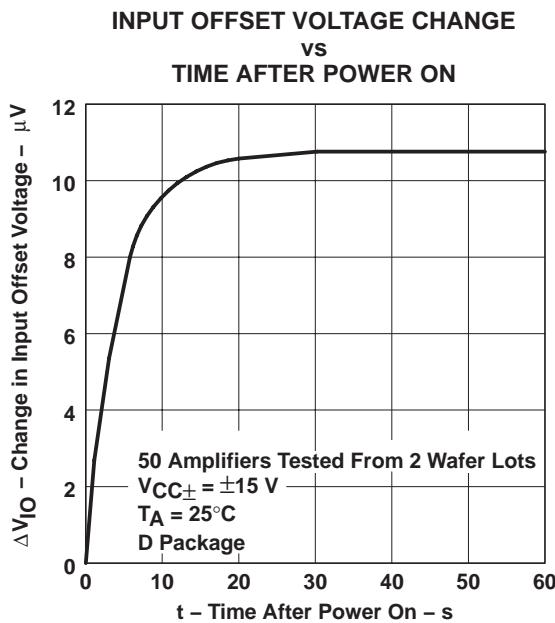


Figure 7

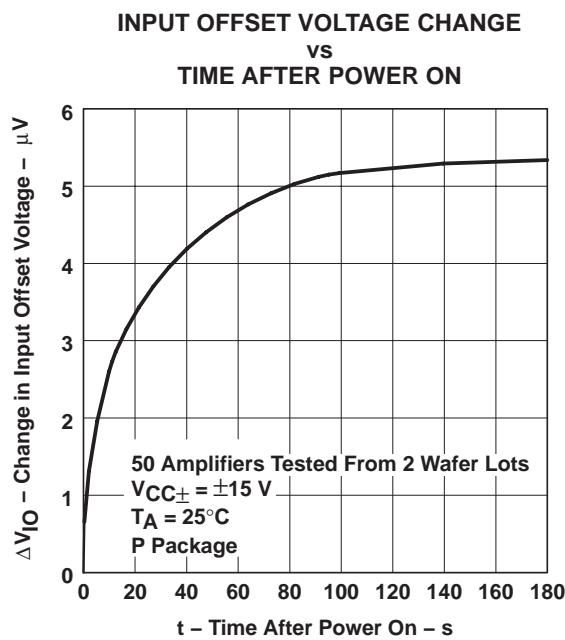


Figure 8

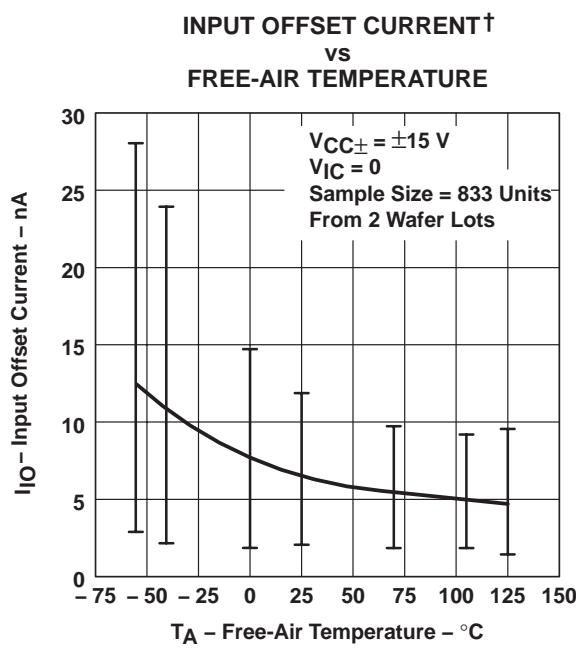


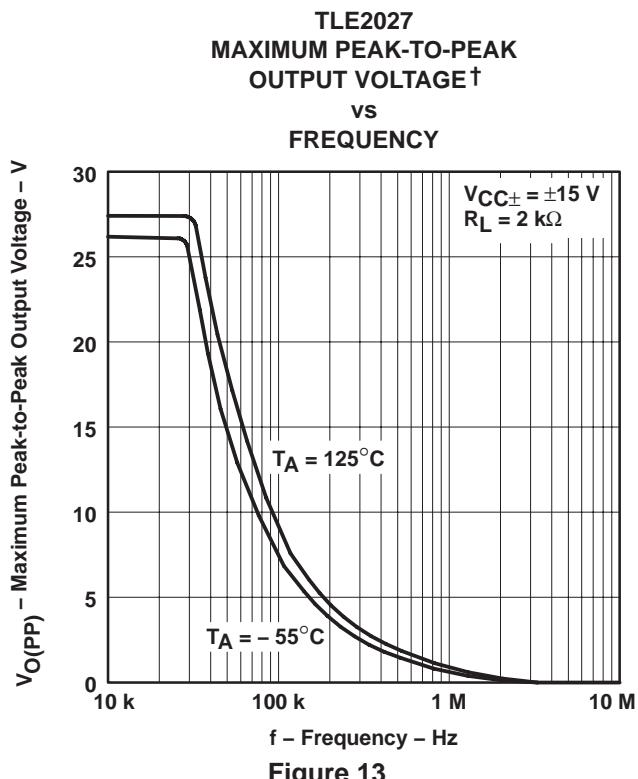
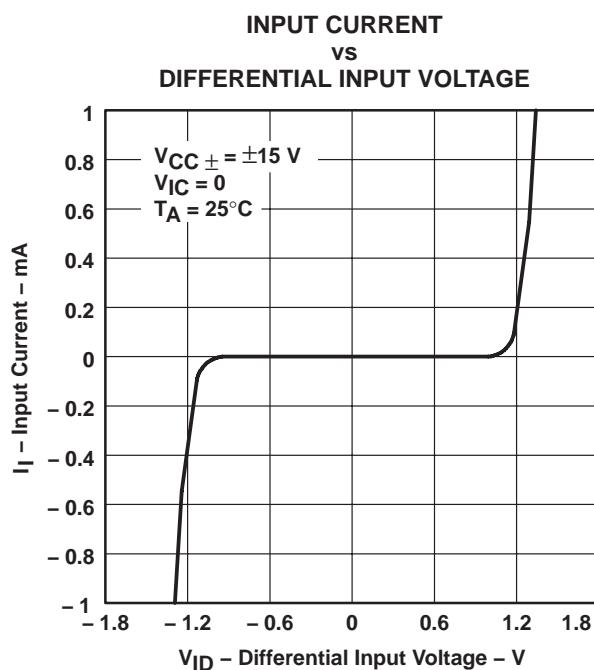
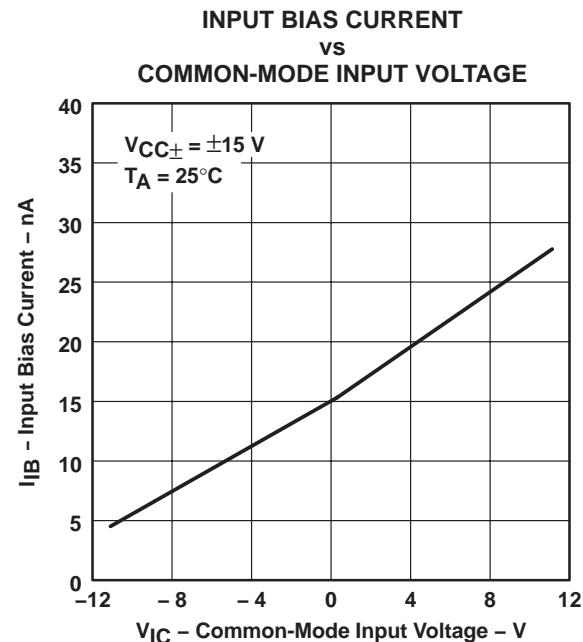
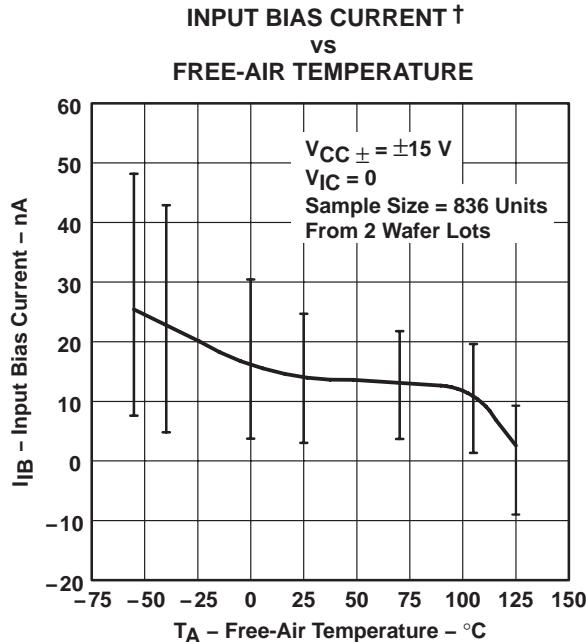
Figure 9

<sup>†</sup> Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**



† Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**

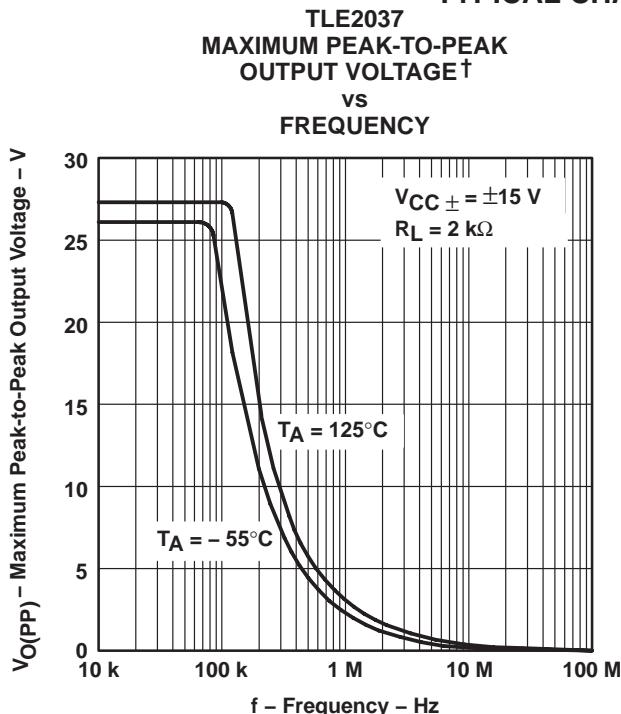


Figure 14

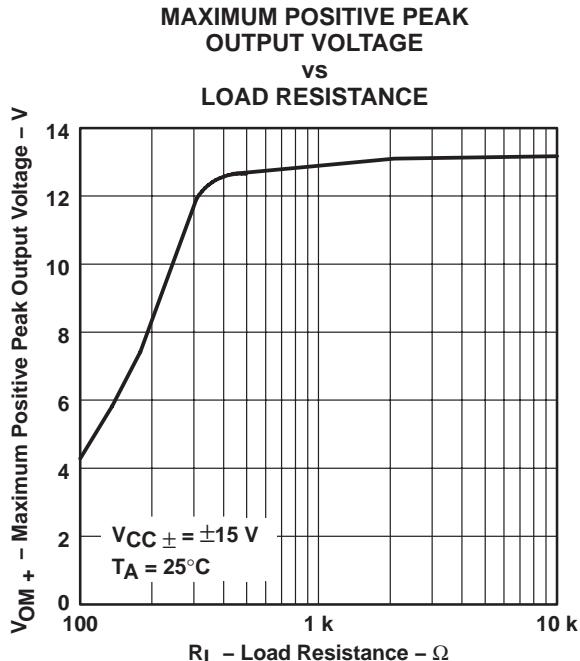


Figure 15

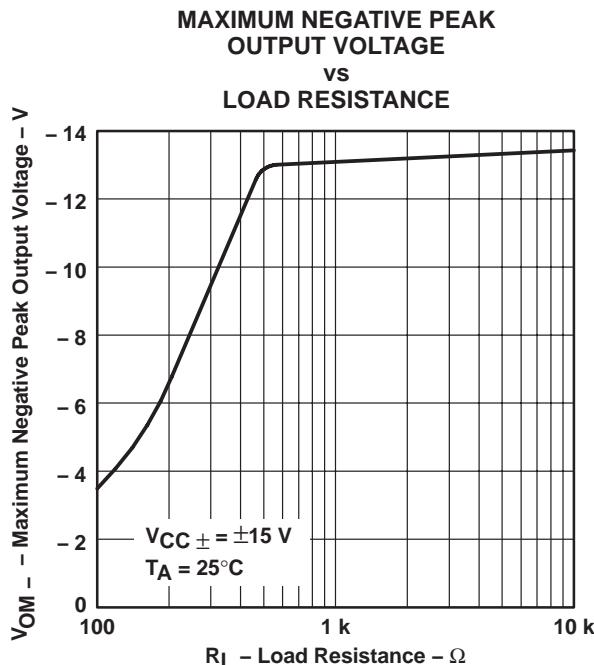


Figure 16

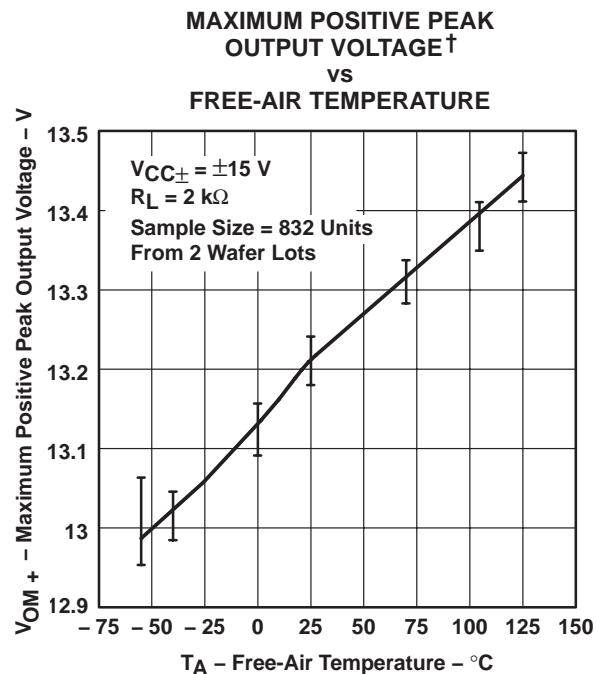


Figure 17

<sup>†</sup> Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**

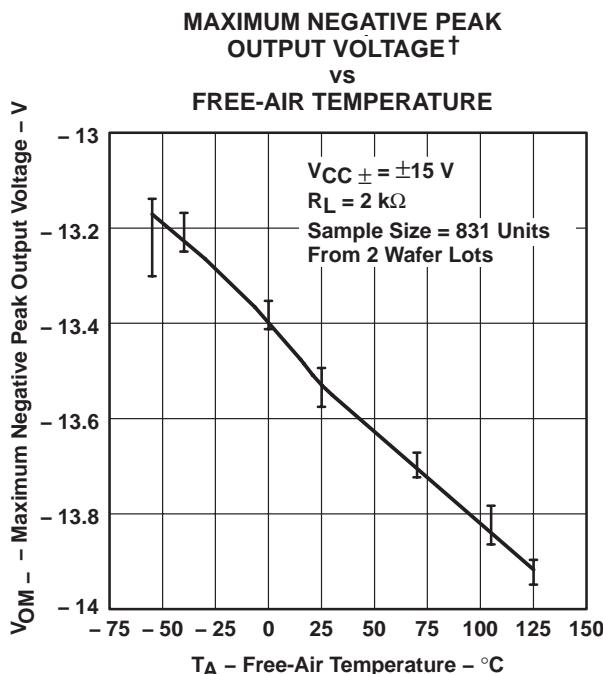


Figure 18

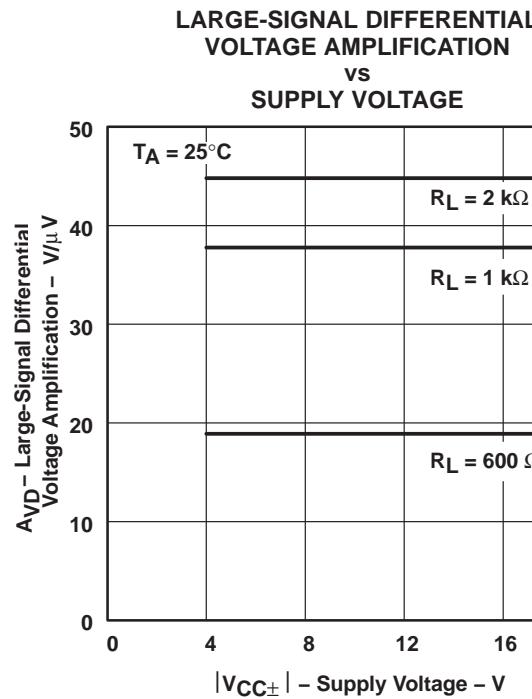


Figure 19

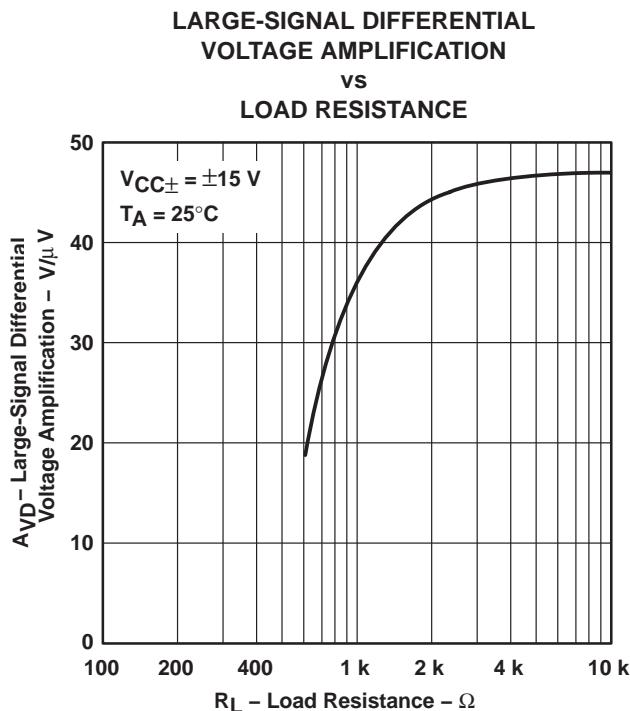


Figure 20

<sup>†</sup> Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**

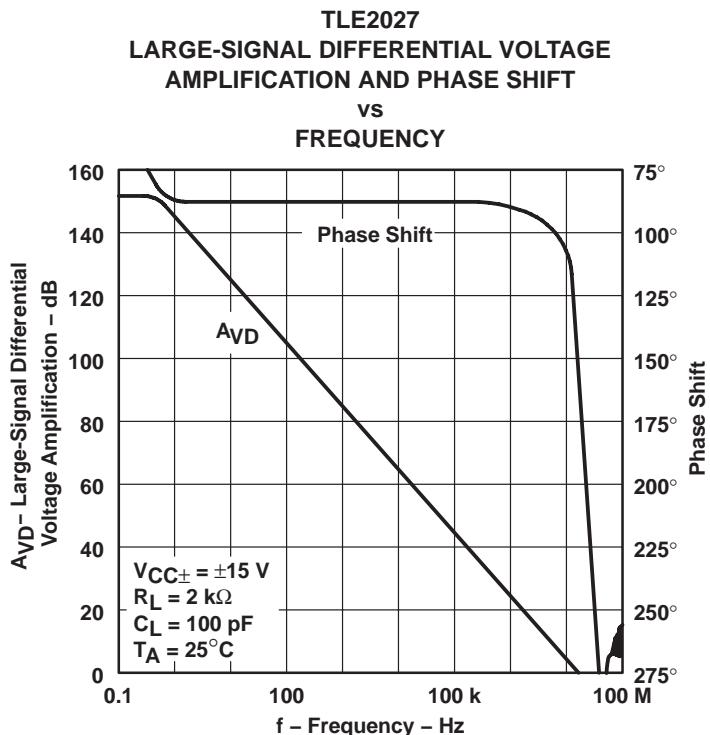


Figure 21

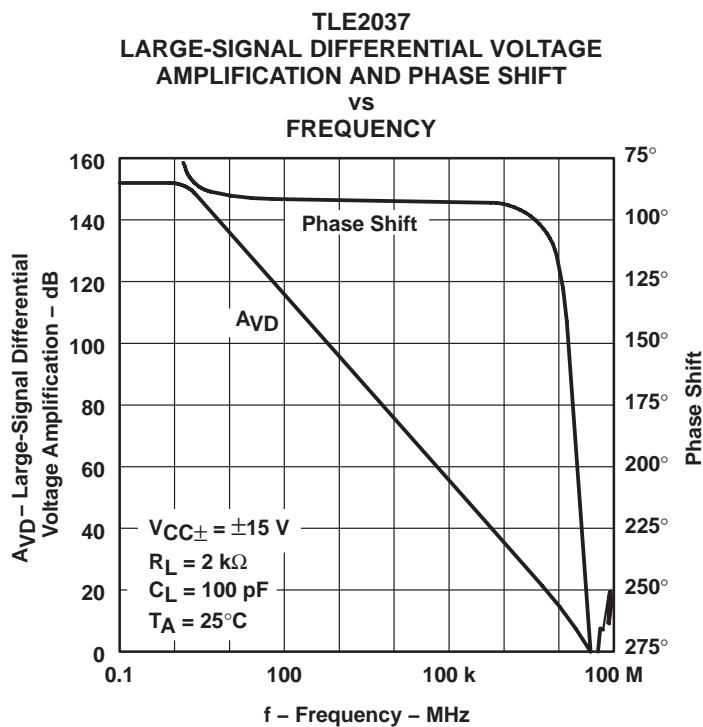


Figure 22

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**

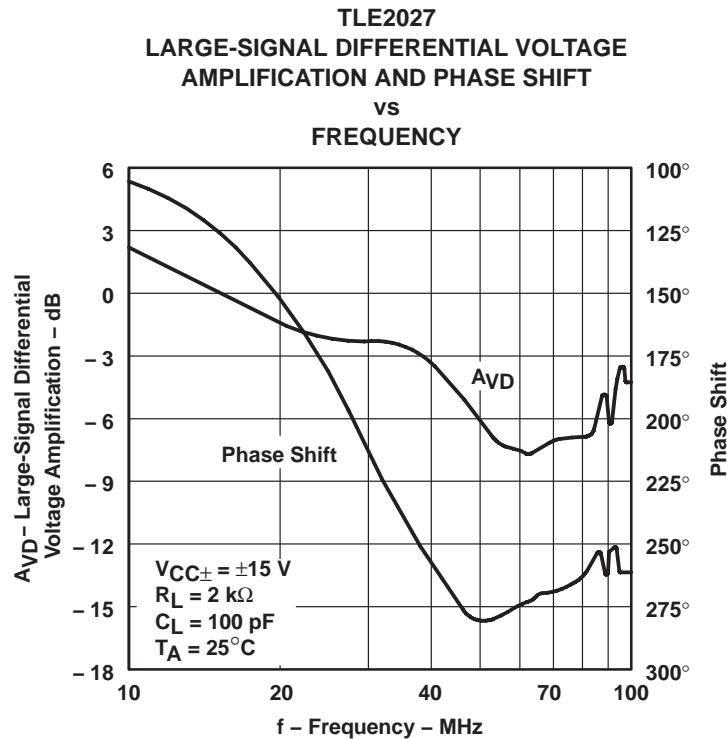


Figure 23

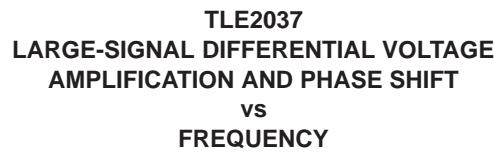


Figure 24

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**

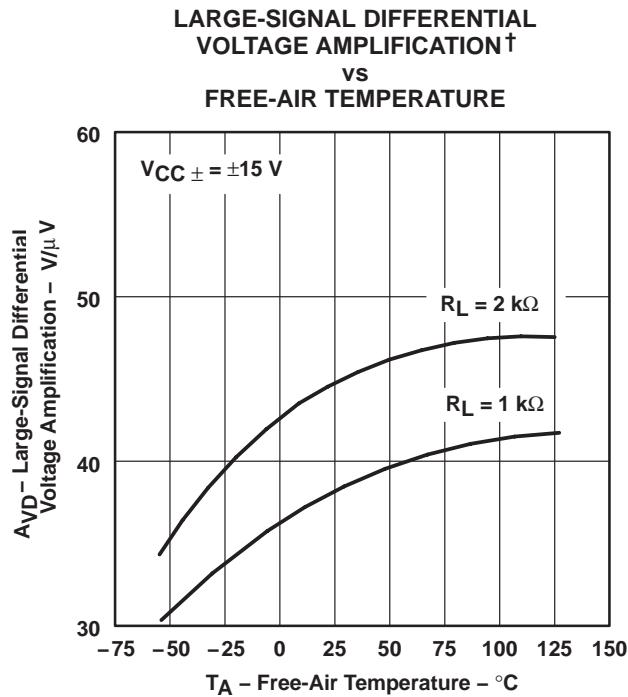


Figure 25

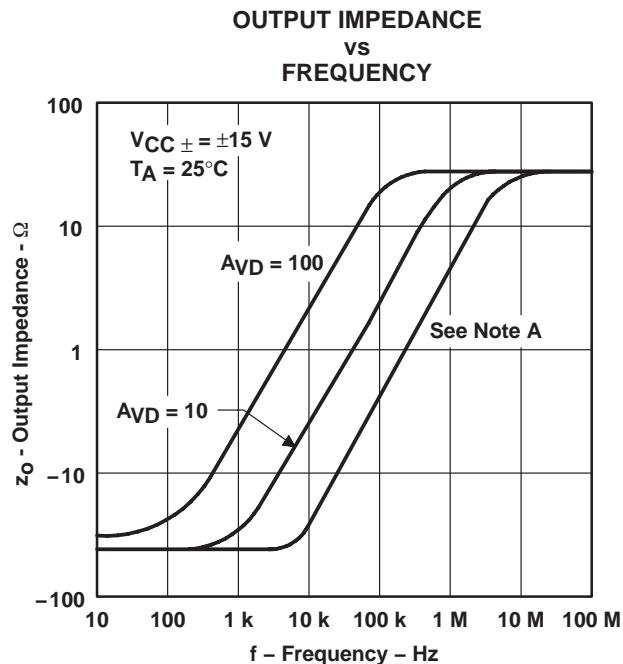


Figure 26

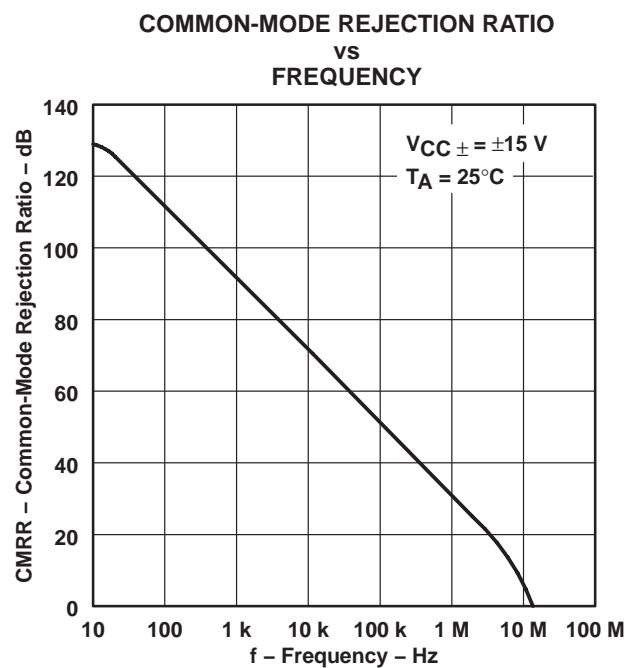


Figure 27

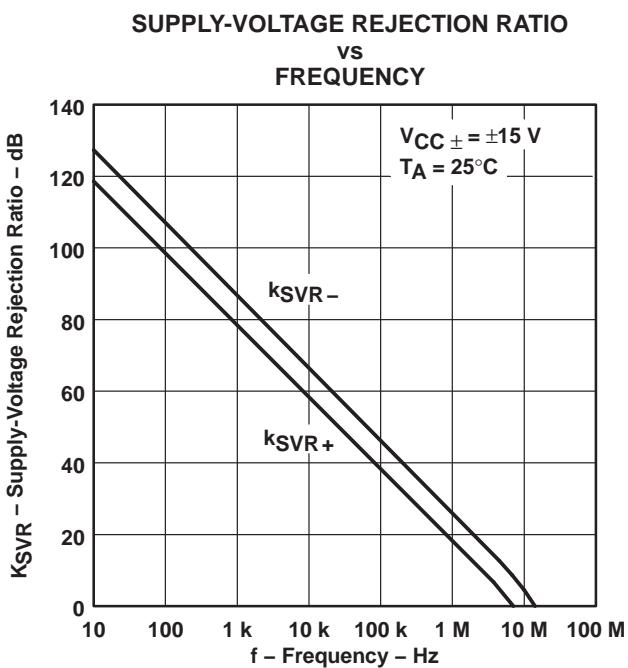


Figure 28

<sup>†</sup> Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**

**SHORT-CIRCUIT OUTPUT CURRENT  
vs  
SUPPLY VOLTAGE**

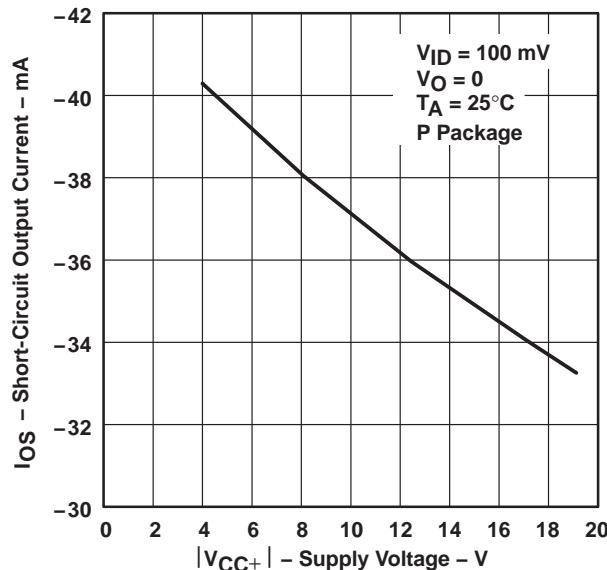


Figure 29

**SHORT-CIRCUIT OUTPUT CURRENT  
vs  
SUPPLY VOLTAGE**

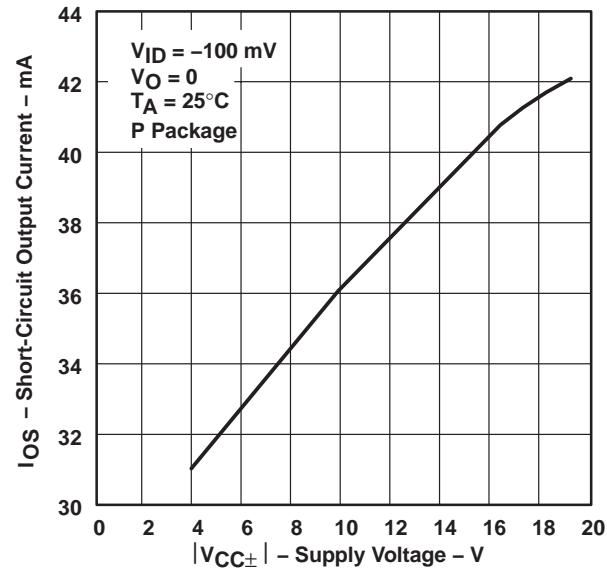


Figure 30

**SHORT-CIRCUIT OUTPUT CURRENT  
vs  
ELAPSED TIME**

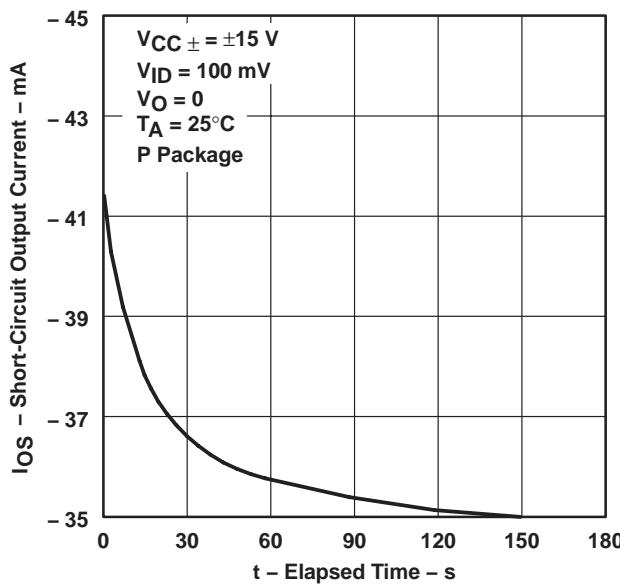


Figure 31

**SHORT-CIRCUIT OUTPUT CURRENT  
vs  
ELAPSED TIME**

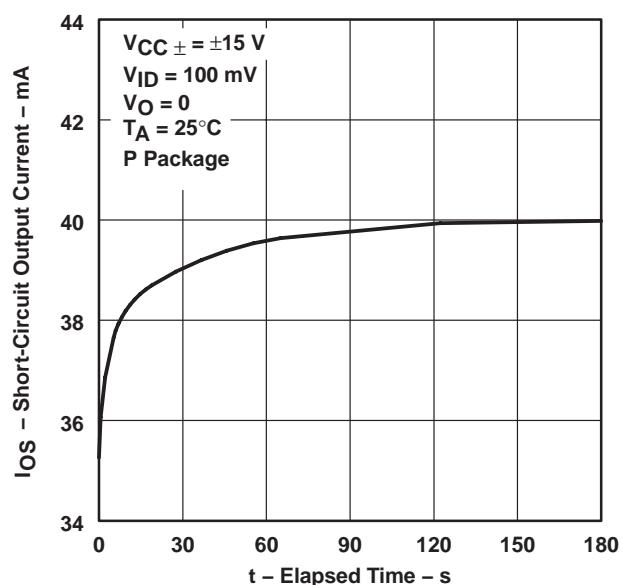


Figure 32

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**

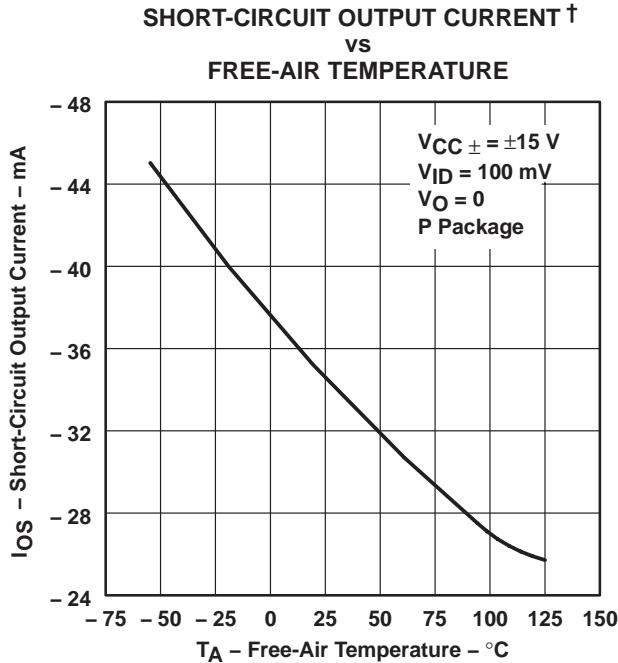


Figure 33

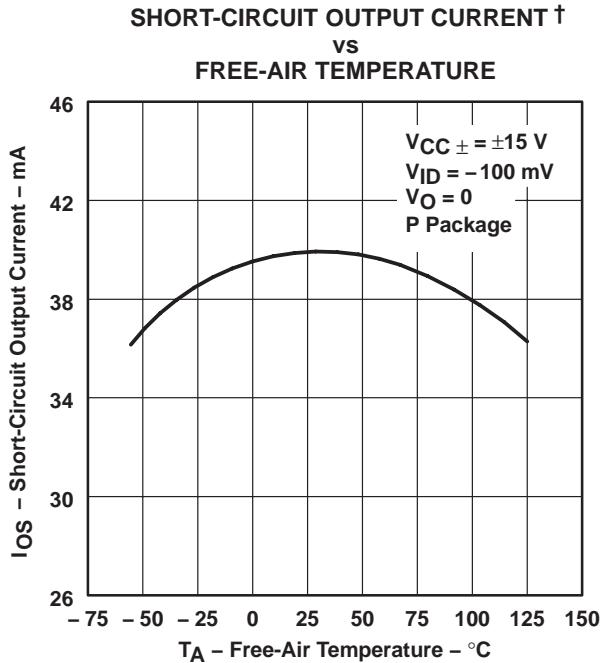


Figure 34

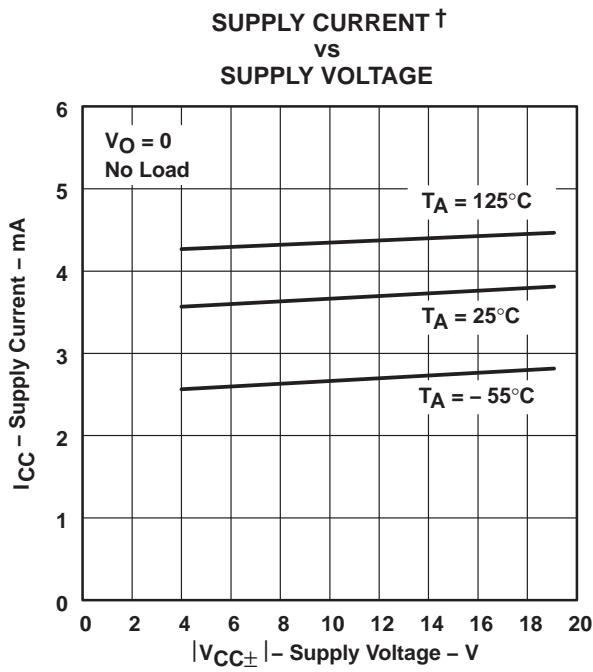


Figure 35

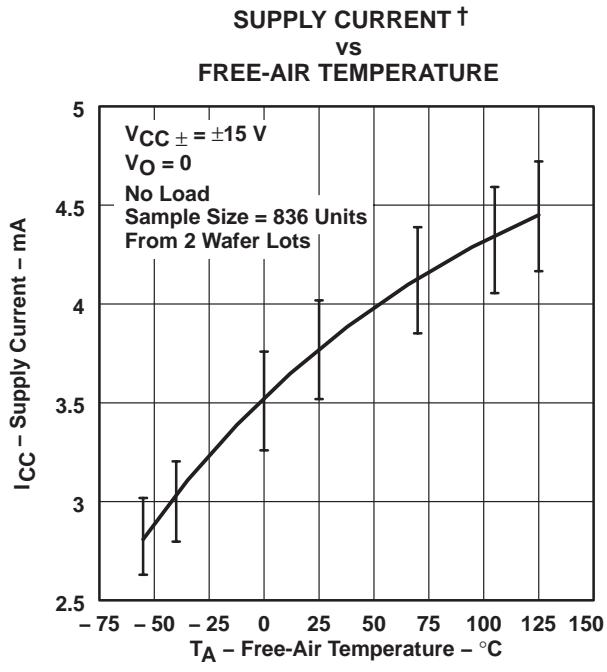


Figure 36

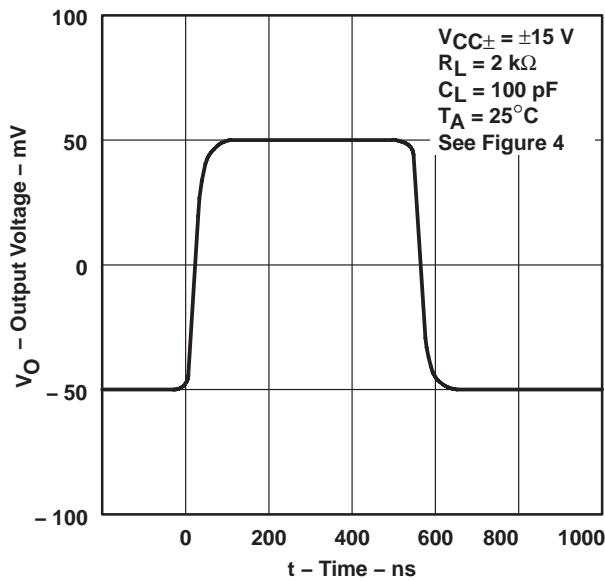
<sup>†</sup> Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

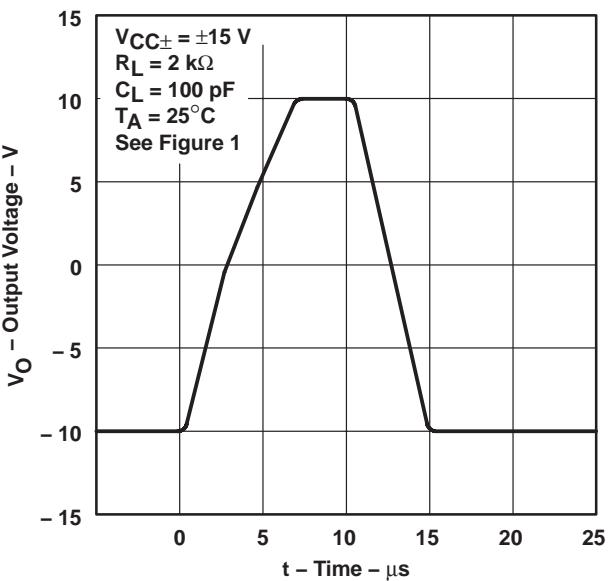
**TYPICAL CHARACTERISTICS**

**TLE2027**  
**VOLTAGE-FOLLOWER**  
**SMALL-SIGNAL**  
**PULSE RESPONSE**



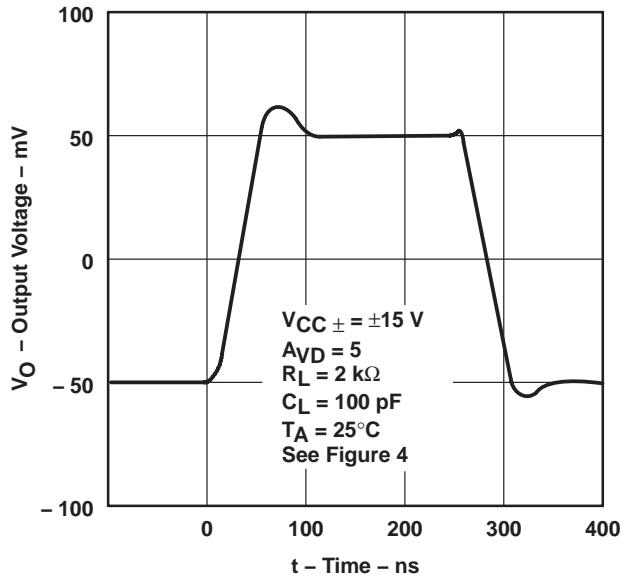
**Figure 37**

**TLE2027**  
**VOLTAGE-FOLLOWER**  
**LARGE-SIGNAL**  
**PULSE RESPONSE**



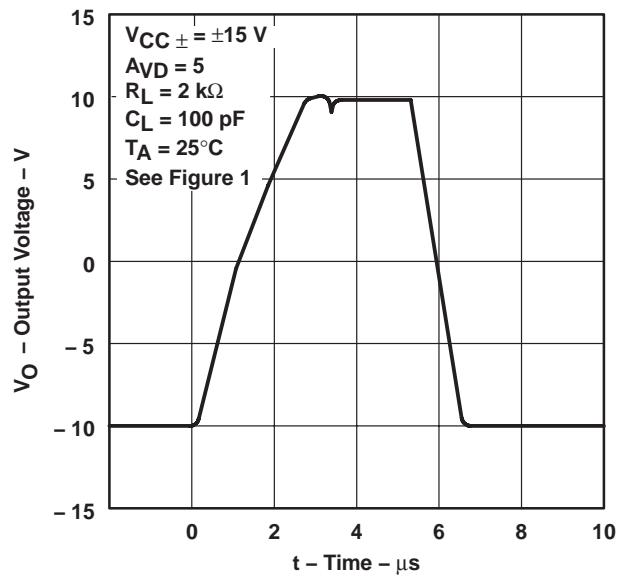
**Figure 38**

**TLE2037**  
**VOLTAGE-FOLLOWER**  
**SMALL-SIGNAL**  
**PULSE RESPONSE**



**Figure 39**

**TLE2037**  
**VOLTAGE-FOLLOWER**  
**LARGE-SIGNAL**  
**PULSE RESPONSE**



**Figure 40**

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**

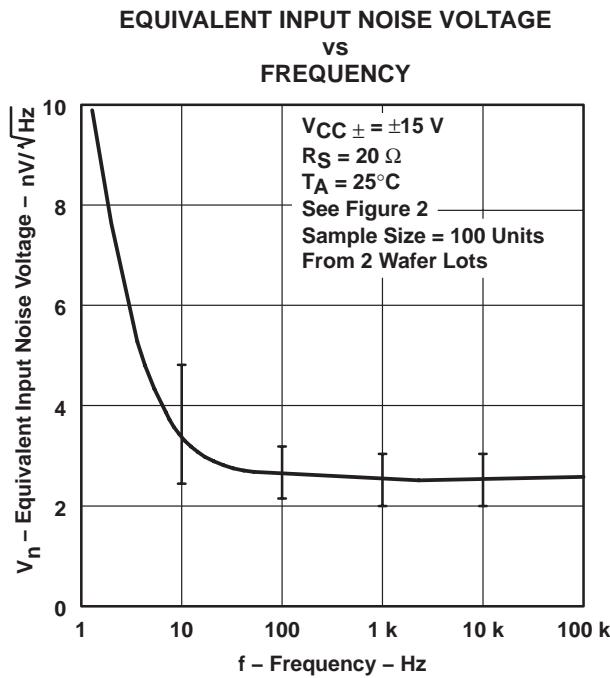


Figure 41

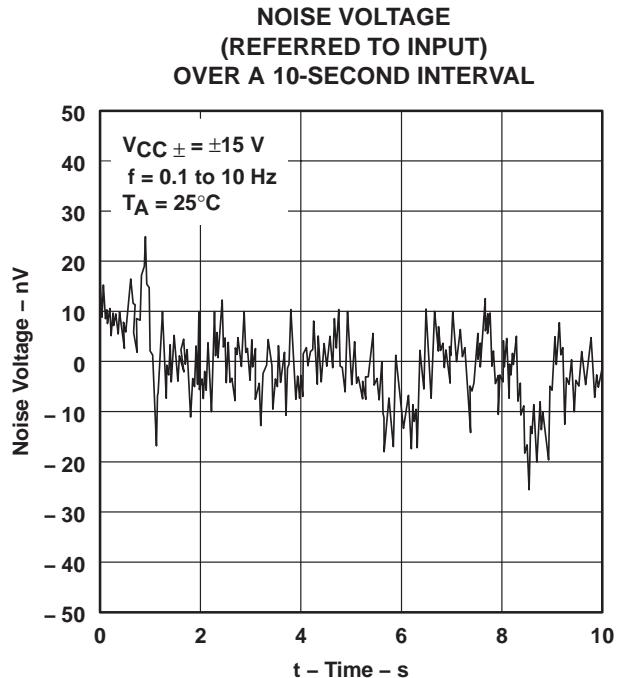


Figure 42

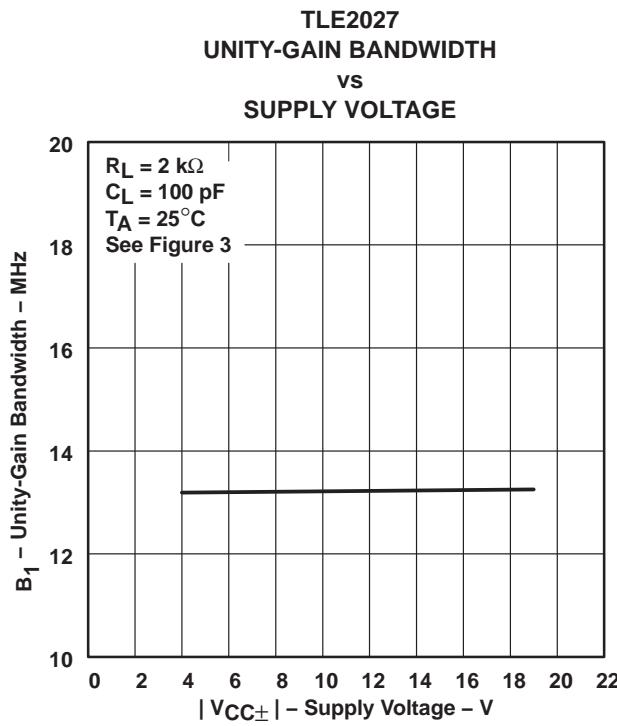


Figure 43

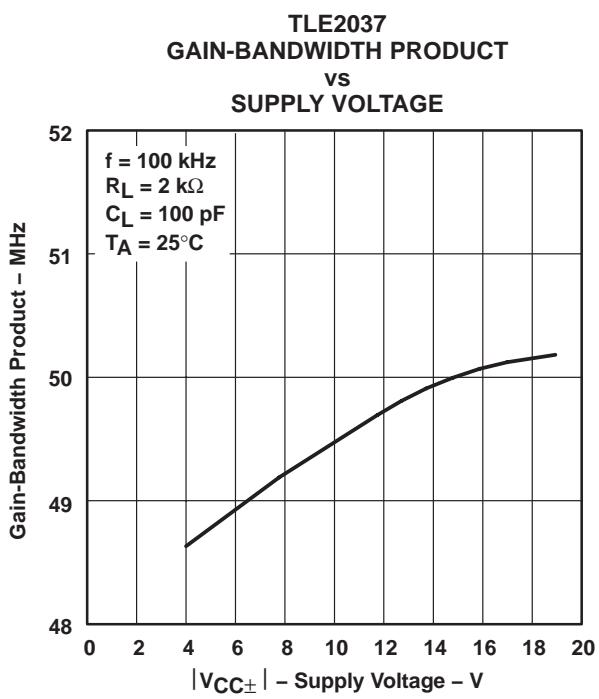


Figure 44

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**

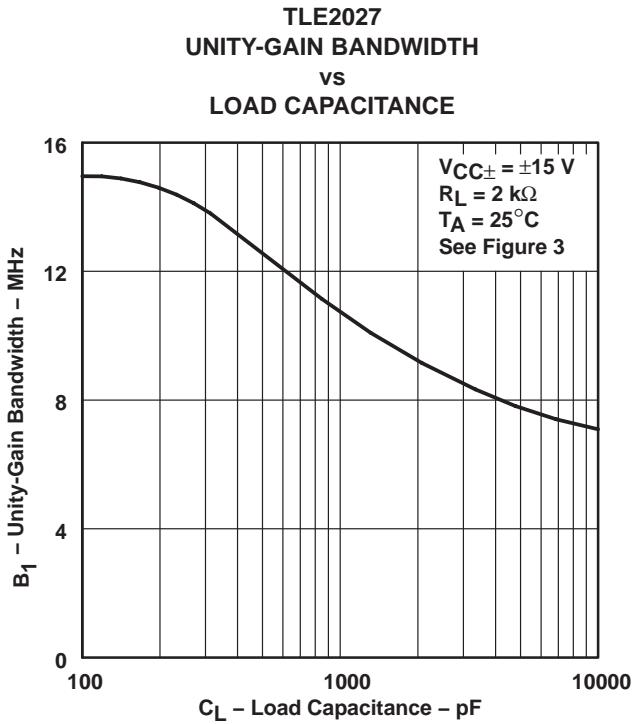


Figure 45

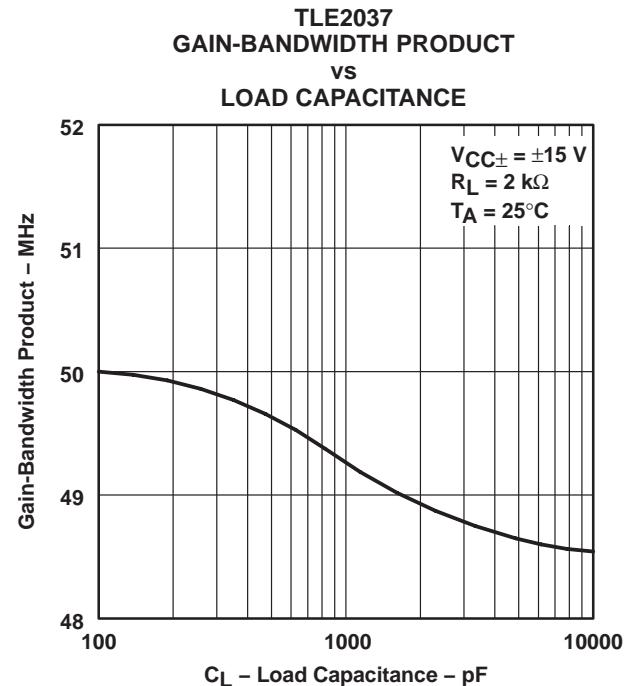


Figure 46

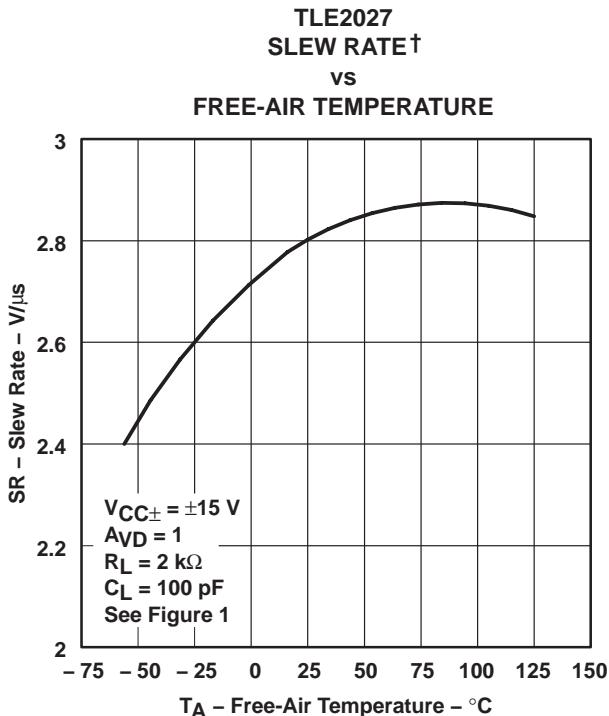


Figure 47

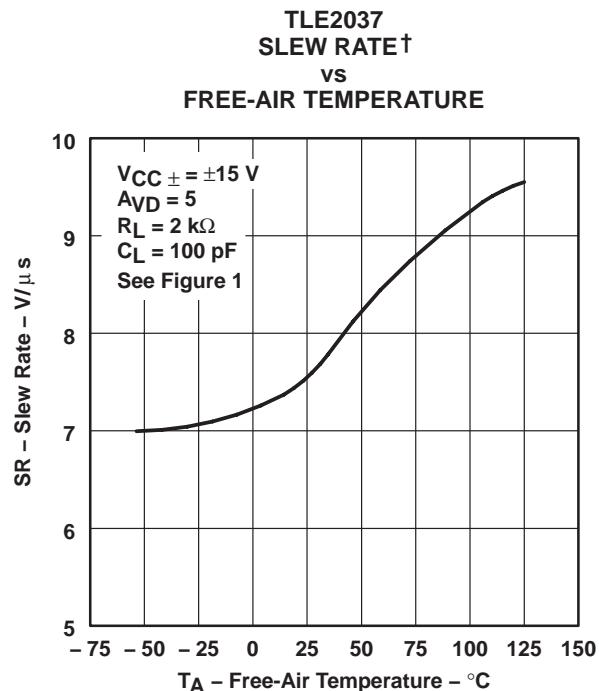


Figure 48

† Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**

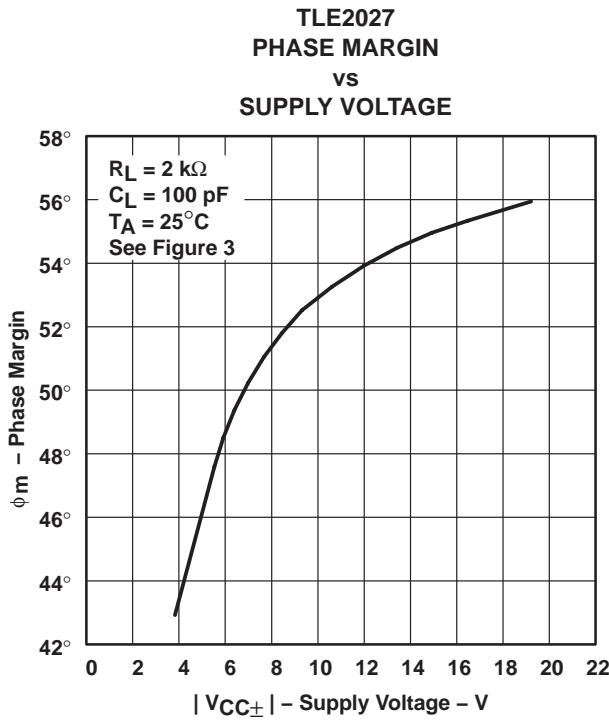


Figure 49

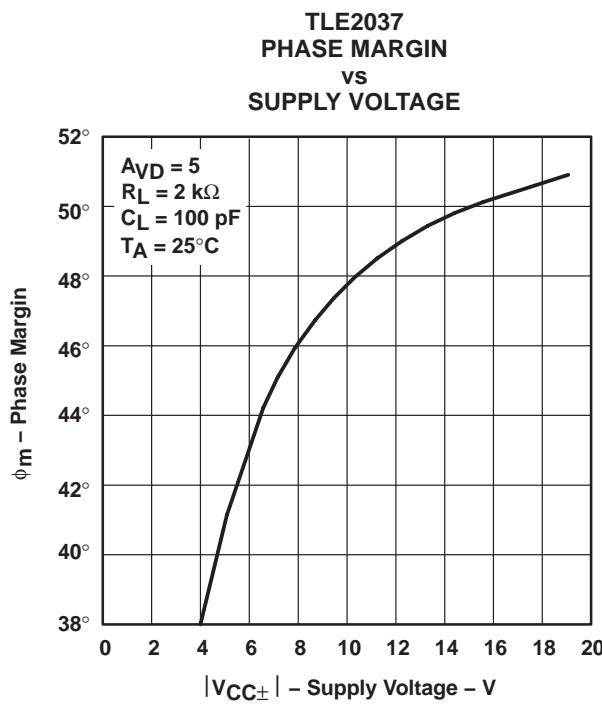


Figure 50

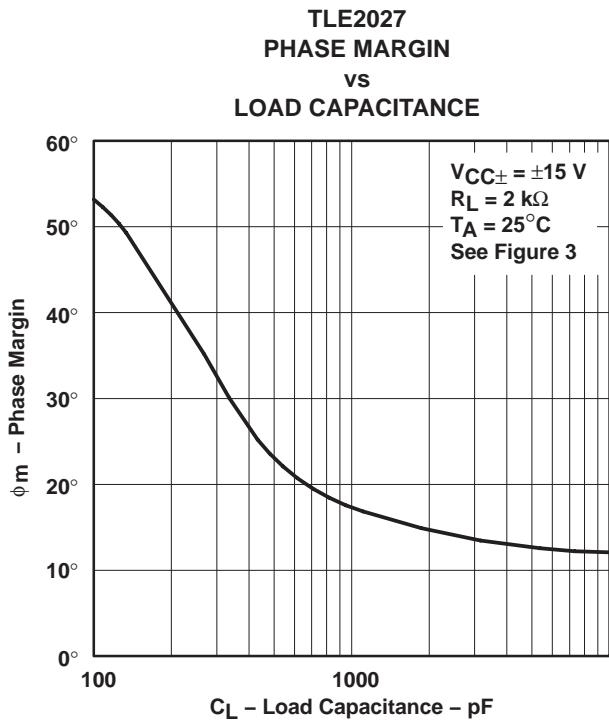


Figure 51

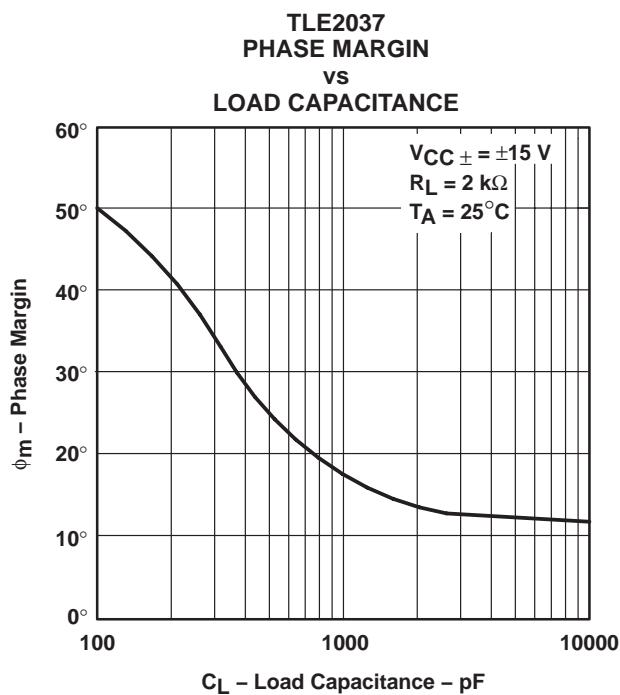
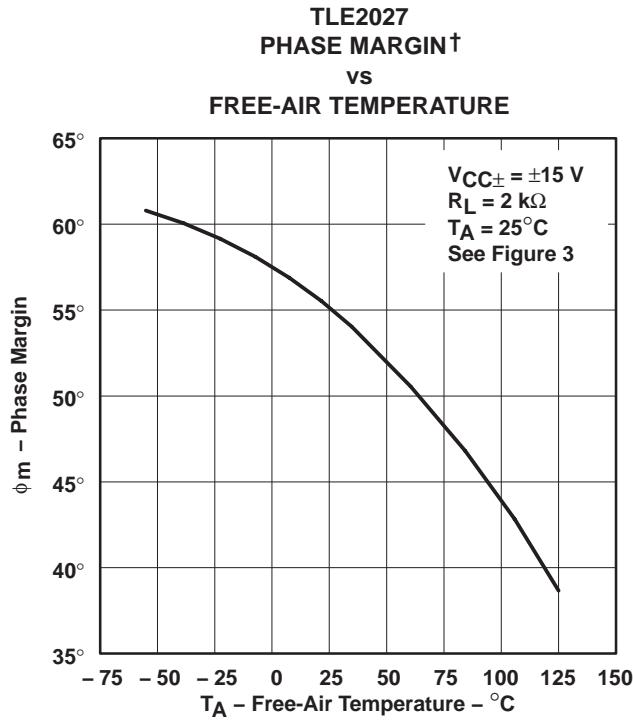


Figure 52

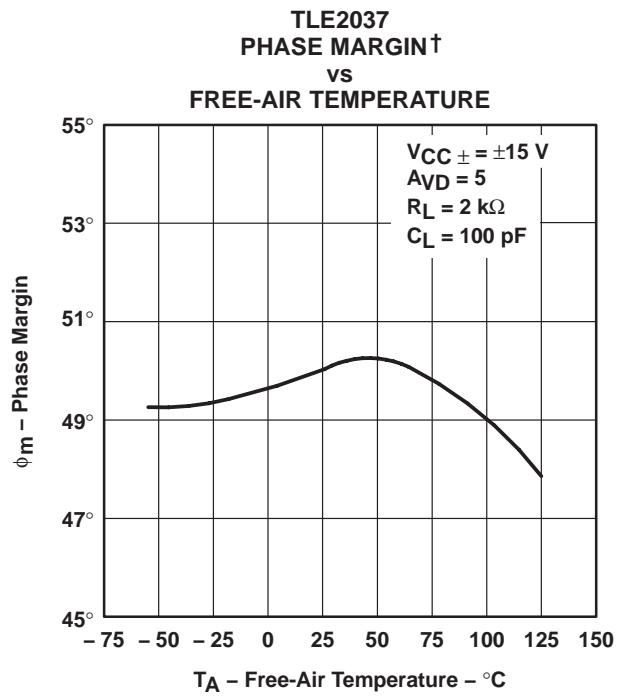
**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**TYPICAL CHARACTERISTICS**



**Figure 53**



**Figure 54**

<sup>†</sup> Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.

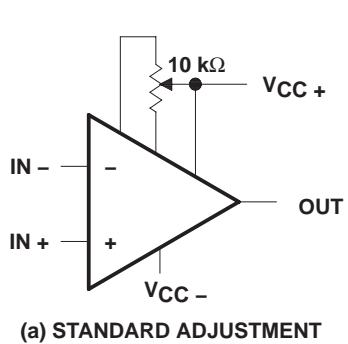
# TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1 EXCALIBUR LOW-NOISE HIGH-SPEED PRECISION OPERATIONAL AMPLIFIERS

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

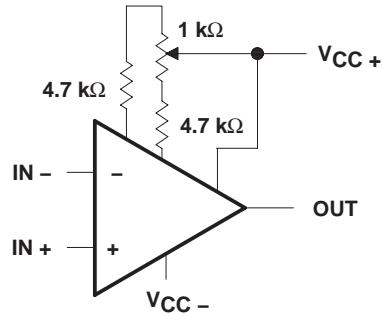
## APPLICATION INFORMATION

### input offset voltage nulling

The TLE2027 and TLE2037 series offers external null pins that can be used to further reduce the input offset voltage. The circuits of Figure 55 can be connected as shown if the feature is desired. If external nulling is not needed, the null pins may be left disconnected.



(a) STANDARD ADJUSTMENT



(b) ADJUSTMENT WITH IMPROVED SENSITIVITY

Figure 55. Input Offset Voltage Nulling Circuits

### voltage-follower applications

The TLE2027 circuitry includes input-protection diodes to limit the voltage across the input transistors; however, no provision is made in the circuit to limit the current if these diodes are forward biased. This condition can occur when the device is operated in the voltage-follower configuration and driven with a fast, large-signal pulse. It is recommended that a feedback resistor be used to limit the current to a maximum of 1 mA to prevent degradation of the device. Also, this feedback resistor forms a pole with the input capacitance of the device. For feedback resistor values greater than 10 kΩ, this pole degrades the amplifier phase margin. This problem can be alleviated by adding a capacitor (20 pF to 50 pF) in parallel with the feedback resistor (see Figure 56).

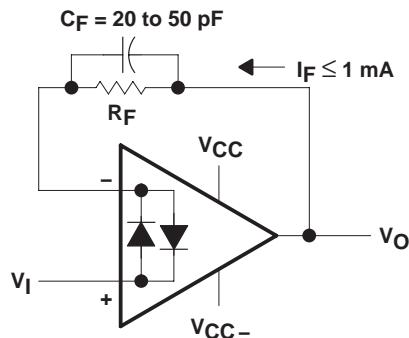


Figure 56. Voltage Follower

# **TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1 EXCALIBUR LOW-NOISE HIGH-SPEED PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

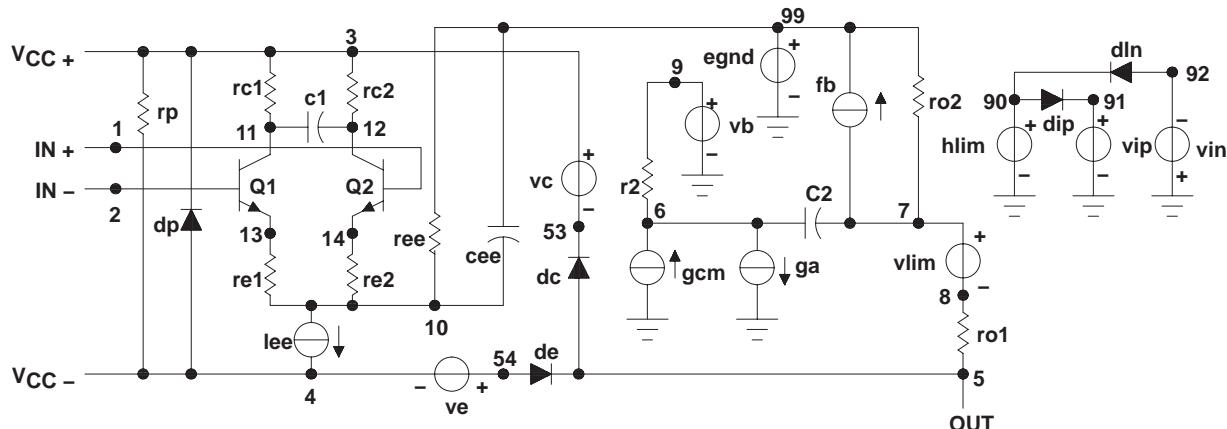
## APPLICATION INFORMATION

## macromodel information

Macromodel information provided was derived using Microsim *Parts*<sup>TM</sup>, the model generation software used with Microsim *PSpice*<sup>TM</sup>. The Boyle macromodel (see Note 6) and subcircuit in Figure 57, Figure 58, and Figure 59 were generated using the TLE20x7 typical electrical and operating characteristics at 25°C. Using this information, output simulations of the following key parameters can be generated to a tolerance of 20% (in most cases):

- Maximum positive output voltage swing
- Maximum negative output voltage swing
- Slew rate
- Quiescent power dissipation
- Input bias current
- Open-loop voltage amplification
- Gain-bandwidth product
- Common-mode rejection ratio
- Phase margin
- DC output resistance
- AC output resistance
- Short-circuit output current limit

NOTE 6: G. R. Boyle, B. M. Cohn, D. O. Pederson, and J. E. Solomon, "Macromodeling of Integrated Circuit Operational Amplifiers", IEEE Journal of Solid-State Circuits, SC-9, 353 (1974).



**Figure 57. Boyle Macromodel**

**TLE2027-Q1, TLE2037-Q1, TLE2027A-Q1, TLE2037A-Q1**  
**EXCALIBUR LOW-NOISE HIGH-SPEED**  
**PRECISION OPERATIONAL AMPLIFIERS**

SGLS202A – OCTOBER 2003 – REVISED OCTOBER 2006

**APPLICATION INFORMATION**

**macromodel information (continued)**

```
.subckt TLE2027 1 2 3 4 5
*
  c1      11  12  4.003E-12
  c2       6   7  20.00E-12
  dc       5  53  dz
  de      54   5  dz
  dlp      90  91  dz
  dln      92  90  dx
  dp       4   3  dz
  egnd     99   0  poly(2) (3,0)
  (4,0) 0 5 .5
  fb       7  99  poly(5) vb vc
  ve vlp vln 0 954.8E6 -1E9 1E9 1E9
  -1E9
  ga       6   0  11  12
  2.062E-3
  gcm      0   6  10  99
  531.3E-12
  iee      10   4  dc  56.01E-6
  hlim     90   0  vlim 1K
  q1      11   2  13 qx
                                         q2      12   1  14 qx
                                         r2      6   9  100.0E3
                                         rc1     3  11  530.5
                                         rc2     3  12  530.5
                                         re1     13 10 -393.2
                                         re2     14 10 -393.2
                                         ree     10 99 3.571E6
                                         ro1     8   5  25
                                         ro2     7  99  25
                                         rp      3   4  8.013E3
                                         vb      9   0  dc  0
                                         vc      3  53  dc  2.400
                                         ve      54  4  dc  2.100
                                         vlim    7   8  dc  0
                                         vlp     91  0  dc  40
                                         vln     0  92  dc  40
.model dx D(Is=800.0E-18)
.model qx NPN(Is=800.0E-18
               Bf=7.000E3)
.ends
```

**Figure 58. TLE2027 Macromodel Subcircuit**

```
.subckt TLE2037 1 2 3 4 5
*
  c1      11  12  4.003E-12
  c2       6   7  7.500E-12
  dc       5  53  dz
  de      54   5  dz
  dlp      90  91  dz
  dln      92  90  dx
  dp       4   3  dz
  egnd     99   0  poly(2) (3,0)
  (4,0) 0 .5 .5
  fb       7  99  poly(5) vb vc
  ve vip vln 0 923.4E6 A800E6
  800E6 800E6 A800E6
  ga       6   0  11 12 2.121E-3
  gcm      0   6  10  99 597.7E-12
  iee      10   4  dc  56.26E-6
  hlim     90   0  vlim 1K
  q1      11   2  13 qx
                                         q2      12   1  14 qz
                                         r2      6   9  100.0E3
                                         rc1     3  11  471.5
                                         rc2     3  12  471.5
                                         re1     13 10 A448
                                         re2     14 10 A448
                                         ree     10 99 3.555E6
                                         ro1     8   5  25
                                         ro2     7  99  25
                                         rp      3   4  8.013E3
                                         vb      9   0  dc  0
                                         vc      3  53  dc  2.400
                                         ve      54  4  dc  2.100
                                         vlim    7   8  dc  0
                                         vlp     91  0  dc  40
                                         vln     0  92  dc  40
.model dx D(Is=800.0E-18)
.model qx NPN(Is=800.0E-18
               Bf=7.031E3)
.ends
```

**Figure 59. TLE2037 Macromodel Subcircuit**

**PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
TLE2037AQDRG4Q1	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLE2037AQDRQ1	ACTIVE	SOIC	D	8	2500	Pb-Free (RoHS)	CU NIPDAU	Level-2-250C-1 YEAR/Level-1-235C-UNLIM
TLE2037QDRG4Q1	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TLE2037QDRQ1	ACTIVE	SOIC	D	8	2500	Pb-Free (RoHS)	CU NIPDAU	Level-2-250C-1 YEAR/Level-1-235C-UNLIM

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

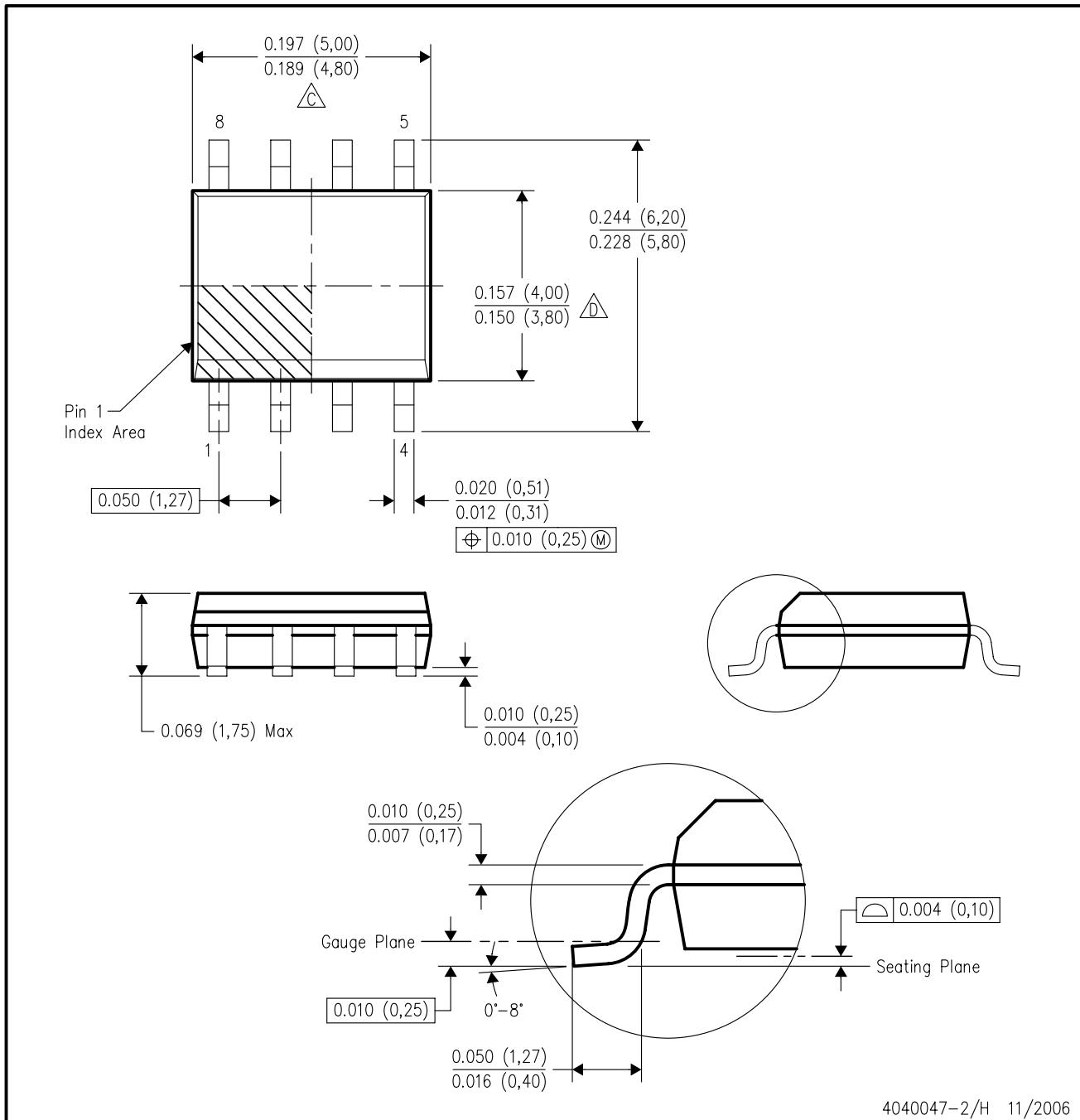
**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

## MECHANICAL DATA

### D (R-PDSO-G8)

### PLASTIC SMALL-OUTLINE PACKAGE



4040047-2/H 11/2006

NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

 C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.

 D. Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.

E. Reference JEDEC MS-012 variation AA.

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Products	Applications
Amplifiers	<a href="http://amplifier.ti.com">amplifier.ti.com</a>
Data Converters	<a href="http://dataconverter.ti.com">dataconverter.ti.com</a>
DSP	<a href="http://dsp.ti.com">dsp.ti.com</a>
Interface	<a href="http://interface.ti.com">interface.ti.com</a>
Logic	<a href="http://logic.ti.com">logic.ti.com</a>
Power Mgmt	<a href="http://power.ti.com">power.ti.com</a>
Microcontrollers	<a href="http://microcontroller.ti.com">microcontroller.ti.com</a>
RFID	<a href="http://www.ti-rfid.com">www.ti-rfid.com</a>
Low Power Wireless	<a href="http://www.ti.com/lpw">www.ti.com/lpw</a>
	Audio <a href="http://www.ti.com/audio">www.ti.com/audio</a>
	Automotive <a href="http://www.ti.com/automotive">www.ti.com/automotive</a>
	Broadband <a href="http://www.ti.com/broadband">www.ti.com/broadband</a>
	Digital Control <a href="http://www.ti.com/digitalcontrol">www.ti.com/digitalcontrol</a>
	Military <a href="http://www.ti.com/military">www.ti.com/military</a>
	Optical Networking <a href="http://www.ti.com/opticalnetwork">www.ti.com/opticalnetwork</a>
	Security <a href="http://www.ti.com/security">www.ti.com/security</a>
	Telephony <a href="http://www.ti.com/telephony">www.ti.com/telephony</a>
	Video & Imaging <a href="http://www.ti.com/video">www.ti.com/video</a>
	Wireless <a href="http://www.ti.com/wireless">www.ti.com/wireless</a>