

## N-CHANNEL ENHANCEMENT MODE FIELD EFFECT **TRANSISTOR**



#### **Features**

Low On-Resistance

Low Gate Threshold Voltage

Low Input Capacitance

Fast Switching Speed

Low Input/Output Leakage

Lead Free/RoHS Compliant (Note 2)

## **Mechanical Data**

Case: SOT-23

Case Material: Molded Plastic. UL Flammability

Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020C

Terminal Connections: See Diagram

Terminals: Solderable per MIL-STD-202, Method 208

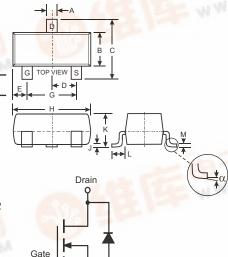
Lead Free Plating (Matte Tin Finish annealed over Alloy 42

leadframe).

Marking (See Page 2): K70

Ordering & Date Code Information: See Page 2

Weight: 0.008 grams (approximate)



O Source

	C . W.								
SOT-23									
Dim	Min	Max							
Α	0.37	0.51							
В	1.20	1.40							
С	2.30	2.50							
D	0.89	1.03							
Е	0.45	0.60							
G	1.78	2.05							
Н	2.80	3.00							
J	0.013	0.10							
K	0.903	1.10							
L	0.45	0.61							
M	0.085	0.180							
	0	8							
All Dimensions in mm									

**BS870** 

#### Maximum Ratings @ T<sub>A</sub> = 25 C unless otherwise specified

Characteristic	Symbol	BS870	Units	
Drain-Source Voltage		V <sub>DSS</sub>	60	V
Drain-Gate Voltage R <sub>GS</sub> 1.0M		V <sub>DGR</sub>	60	V
Gate-Source Voltage	Continuous	V <sub>GSS</sub>	20	V
Drain Current (Note 1)	Continuous	ID ID	250	mA
Total Power Dissipation (Note 1)	0750.0	$P_d$	300	mW
Thermal Resistance, Junction to Ambient		R <sub>JA</sub>	417	K/W
Operating and Storage Temperature Range		T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	С

Note: 1. Device mounted on FR-5 PCB 1.0 x 0.75 x 0.062 inch pad layout as shown on Diodes, Inc. suggested pad layout AP02001, WWW.DZSC.COM which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

2. No purposefully added lead.





## **Electrical Characteristics** @ T<sub>A</sub> = 25 C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 3)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	80		V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 100 A
Zero Gate Voltage Drain Current	IDSS			0.5	μA	$V_{DS} = 25V, V_{GS} = 0V$
Gate-Body Leakage	I <sub>GSS</sub>			10	nA	V <sub>GS</sub> = 15V, V <sub>DS</sub> = 0V
ON CHARACTERISTICS (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0	2.0	3.0	V	$V_{DS} = V_{GS}, I_D = 250 A$
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>		3.5	5.0		V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.2A
On-State Drain Current	I <sub>D(ON)</sub>		1.0	0.5	Α	$V_{GS} = 10V, V_{DS} = 7.5V$
Forward Transconductance	g <sub>FS</sub>	80			mS	V <sub>DS</sub> =10V, I <sub>D</sub> = 0.2A
DYNAMIC CHARACTERISTICS						
Input Capacitance	C <sub>iss</sub>		22	50	pF	
Output Capacitance	Coss		11	25	pF	$V_{DS} = 10V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>		2.0	5.0	pF	
SWITCHING CHARACTERISTICS					-	
Turn-On Delay Time	t <sub>D(ON)</sub>		2.0	20	ns	V <sub>ES</sub> = 10V, R <sub>L</sub> = 150 ,
Turn-Off Delay Time	t <sub>D(OFF)</sub>		5.0	20	ns	$V_{DS} = 10V, R_D = 100$

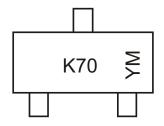
## Ordering Information (Note 4)

Device	Packaging	Shipping
BS870-7-F	SOT-23	3000/Tape & Reel

Notes: 3. Short duration test pulse used to minimize self-heating effect.

4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Marking Information**

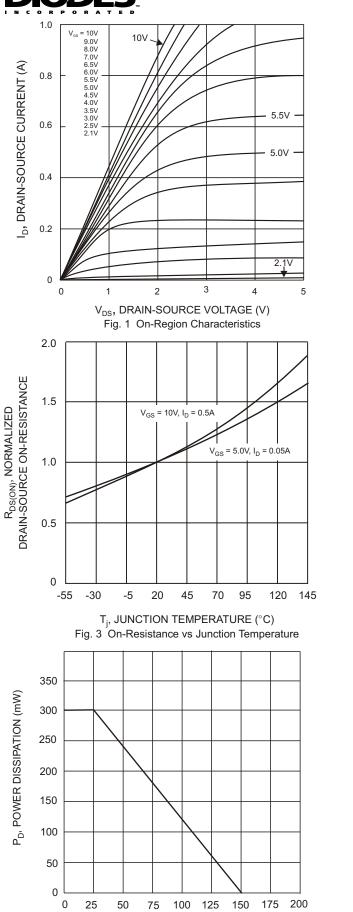


K70 = Product Type Marking Code YM = Date Code Marking Y = Year ex: N = 2002 M = Month ex: 9 = September

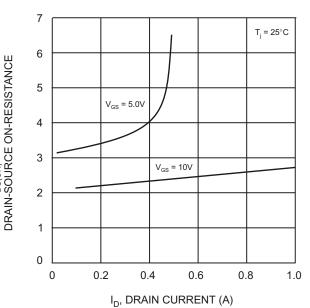
### Date Code Key

Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009
Code	J	K	L	М	N	Р	R	S	Т	U	V	W
Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
						I						

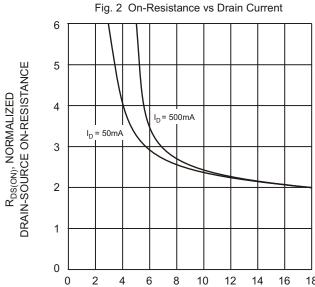
# **DECOES**



T<sub>A</sub>, AMBIENT TEMPERATURE (°C) Fig. 5, Max Power Dissipation vs Ambient Temperature



R<sub>DS(ON)</sub>, NORMALIZED



 $\rm V_{GS}$ , GATE TO SOURCE VOLTAGE (V) Fig. 4 On-Resistance vs. Gate-Source Voltage



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