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## Wireless Local Loop/Broadband Access Filters

The following products presented in this data sheet are being withdrawn.

Ordering Code	Substitute Product	Date of Withdrawal	Deadline Last Orders	Last Shipments
B39111B4540Z710		2006-12-01	2007-02-28	2007-05-31

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# SAW Components

Data Sheet B4540

Data Sheet

An abstract graphic featuring a globe with a grid pattern, overlaid with a large, stylized, 3D letter 'E' and the letters 'PCC' in a bold, sans-serif font. The letters are white with a slight shadow, giving them a three-dimensional appearance as if they are floating or attached to the globe. The background is a dark, textured gray.

EPCC

- Bandpass filter for cordless telephone
- Channel selection in DECT system
- Ceramic package for **S**urface **M**ounted **T**echnology (**SMT**)

### Terminals

- Ni, gold-plated

Dimensions in mm, a

### Pin configuration

10	Input	40
9	Input ground or balanced input	
5	Output	50
4	Output ground or balanced output	
1,3,6,8	Case - ground	
2,7	Not connected	

Type	Ordering code	Marking and Package according to	Packaging according to
B4540	B39111-B4540-Z710	C61157-A7-A49	F6107

Electrostatic Sensitive Device (ESD)

### Maximum ratings

Operable temperature range	$T$	– 40/+ 85	°C	
Storage temperature range	$T_{\text{stg}}$	– 40/+ 85	°C	
DC voltage	$V_{\text{DC}}$	0	V	
Source power	$P_{\text{s}}$	10	dBm	source impedance

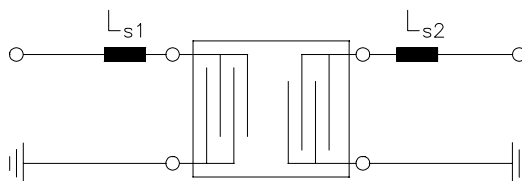
Terminating load impedance:

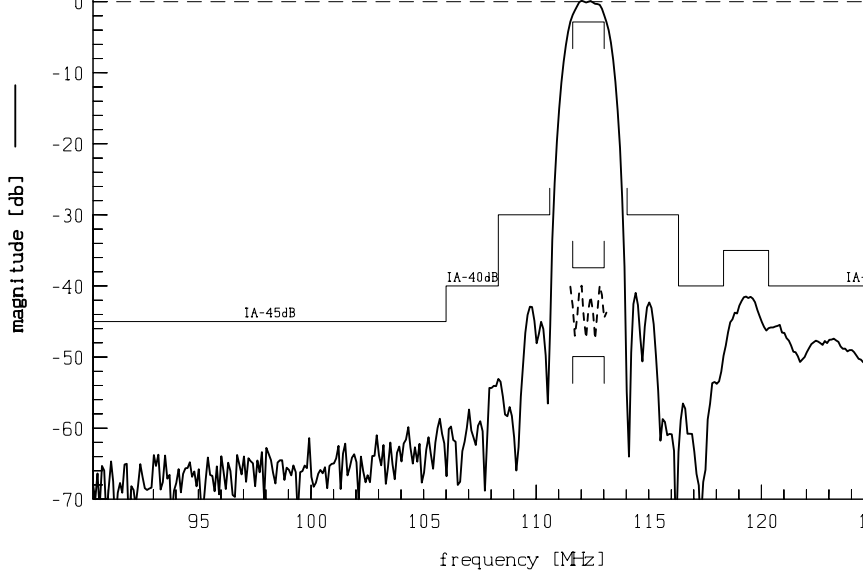
$$Z_L = 0,9 \text{ k}\Omega \parallel 340 \text{ nH}$$

		min.	typ.
<b>Nominal frequency</b>	$f_N$	—	112,32
<b>Insertion attenuation at <math>f_N</math></b> (including losses in matching network) Reference level for the following data	$\alpha_N$	—	13,5
<b>Pass bandwidth</b>	$B_{3\text{dB}}$	—	1,6
<b>Group delay ripple (p-p)</b> $f_N - 700,0 \text{ kHz} \dots f_N + 700,0 \text{ kHz}$	$\Delta\tau$	—	100
<b>Relative attenuation (relative to <math>\alpha_N</math>)</b>	$\alpha_{\text{rel}}$		
$f_N - 30,00 \text{ MHz} \dots f_N - 6,32 \text{ MHz}$		45	59
$f_N - 6,32 \text{ MHz} \dots f_N - 4,00 \text{ MHz}$		40	53
$f_N - 4,00 \text{ MHz} \dots f_N - 1,72 \text{ MHz}$		30	42
$f_N + 1,72 \text{ MHz} \dots f_N + 4,00 \text{ MHz}$		30	41
$f_N + 4,00 \text{ MHz} \dots f_N + 6,00 \text{ MHz}$		40	50
$f_N + 6,00 \text{ MHz} \dots f_N + 8,00 \text{ MHz}$		35	41
$f_N + 8,00 \text{ MHz} \dots f_N + 30,00 \text{ MHz}$		40	45
$f_N + 17,28 \text{ MHz}$		45	57
<b>Impedance at <math>f_N</math></b>			
Input: $Z_{\text{IN}} = R_{\text{IN}} \parallel C_{\text{IN}}$		—	3,9 $\parallel$ 5,0
Output: $Z_{\text{OUT}} = R_{\text{OUT}} \parallel C_{\text{OUT}}$		—	3,3 $\parallel$ 6,1
<b>Temperature coefficient of frequency <sup>1)</sup></b>	$TC_f$	—	- 0,03
<b>Turnover temperature</b>	$T_0$	—	30

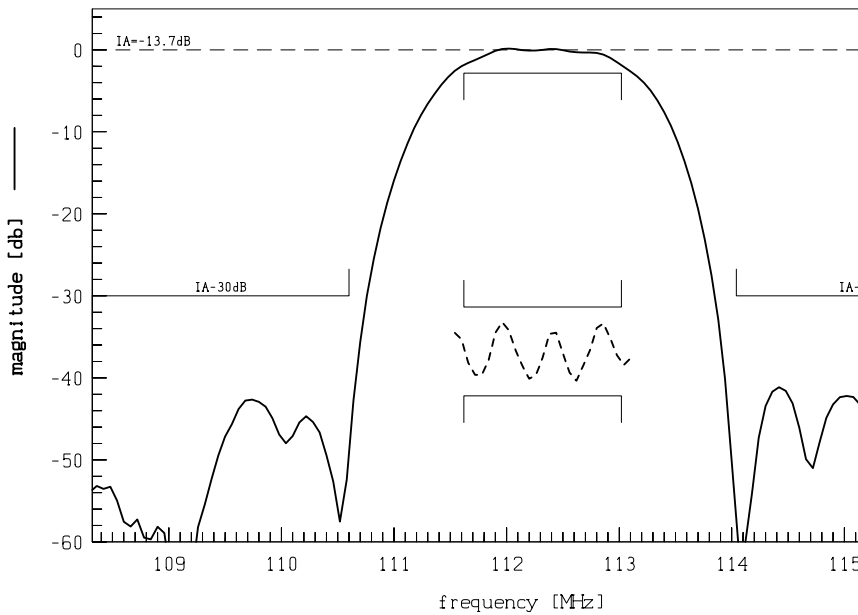
<sup>1)</sup> Temperature dependance of  $f_c$ :  $f_c(T) = f_c(T_0)(1 + TC_f(T - T_0)^2)$

**Matching network to 50  $\Omega$**  (element values depend on pcb layout)





### Transfer function (pass band)



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