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Tantalum Chip Capacitors

Multiple Anode, Ultra-low ESR

Series/Type: **B45396R**
Date: July 2006

RoHS
compatible

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Multiple Anode, Ultra-low ESR**Construction**

- Polar tantalum capacitors with solid electrolyte
- Conventional TaMnO₂ technology
- Three parallel low-ESR tantalum capacitors in one case
- Flame-retardant plastic case (UL 94 V-0)
- Tinned terminals

**Features**

- High volumetric efficiency
- Ultra-low ESR values
- High ripple current capability
- Excellent solderability
- Stable temperature and frequency characteristics
- Low leakage current, low dissipation factor
- Low self-inductance
- High resistance to shock and vibration
- Suitable for use without series resistor (special operating conditions recommended)
- Lead-free and material content compatible with RoHS

Applications

- Telecommunications (e.g. mobile phones, infrastructure)
- Data processing (e.g. laptops, mainframes)
- Measuring and control engineering (e.g. voltage regulators)
- Medical engineering
- DC/DC converters

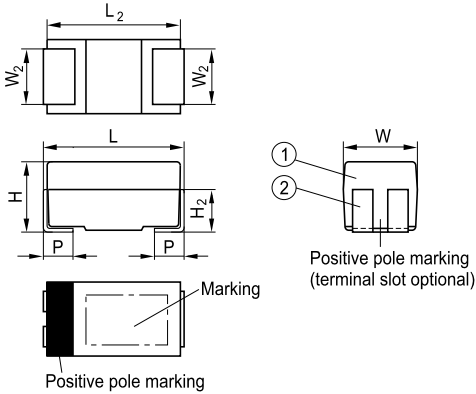
Soldering

Suitable for reflow soldering (IR and vapor phase, in compliance with JEDEC J-STD-020 C) and wave soldering

Delivery mode

Taped and reeled in accordance with IEC 60286-3

Multiple Anode, Ultra-low ESR



① Encapsulation: molded epoxy resin

② NiFe; tinned surface Sn100

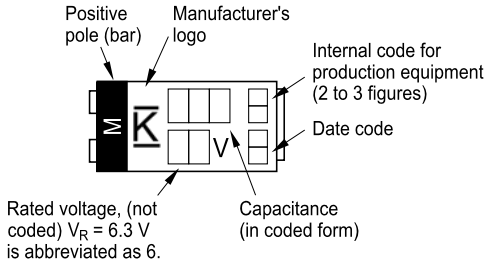
Dimensions

Case size		Dimensions in mm (inches)						
KEMET	EIA/IECQ	L	W	H	L ₂ typ.	W ₂ ±0.1 ±(.004)	H ₂ typ.	P ±0.3 ±(.012)
E	7343-44	7.3 ±0.3 (.287 ±0.012)	4.3 ±0.3 (.169 ±0.012)	4.1 ±0.3 (.161 ±0.012)	7.3 (.287)	2.4 (.094)	1.8 (.071)	1.3 (.051)

Multiple Anode, Ultra-low ESR

Marking

Case size E



Capacitance coding

1st and 2nd digit	Capacitance in pF	
3rd digit	Multiplier:	6 = 10^6 pF
		7 = 10^7 pF
		8 = 10^8 pF

Date coding

Year	Month	
S = 2004	1 = January	7 = July
T = 2005	2 = February	8 = August
U = 2006	3 = March	9 = September
V = 2007	4 = April	O = October
W = 2008	5 = May	N = November
X = 2009	6 = June	D = December

Specifications and characteristics in brief

Series		Multiple Anode	
Ordering code		B45396R	
Technology		Ta-MnO ₂	
Terminals		Tinned	
Rated voltage (up to 85 °C)	V _R	4 ... 50	VDC
Rated capacitance (20 °C, 120 Hz)	C _R	10 ... 1000	μF
Capacitance tolerance		±20%, ±10%	
Maximum equivalent series resistance (20 °C, 100 kHz)	ESR _{max}	30 ... 150	mΩ
Operating temperature range	T _{op}	-55 ... +125	°C
Failure rate (at 40 °C; ≤ V _R , R _S ≤ 0.1 Ω/V) (1 fit = 1 · 10 ⁻⁹ failures/h) C _R · V _R > 330 μF · V		≤288	fit
Service life		>500000	h
Leakage current (V _R , 5 min, 20 °C)	I _{leak}	10	nA/μC
Detail specification (tinned terminals)		CECC 30801-805	
Climatic category (to IEC 60068-1)		55/125/56	



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B45396R

Multiple Anode, Ultra-low ESR

Overview of types

(VDC) (up to 85 °C)	4	6.3	10	16	20	25	35	50
C_R (μF)								
10								E
15								E
22							E	E
33							E	
47						E	E	
68					E	E		
100					E			
150				E				
220				E				
330			E					
470		E	E					
680	E	E						
1000	E							

Upon request

Ordering code structure

B45	396R	1	477	+	5	0	*
Tantalum capacitor							
Series 396R = Multiple Anode, Ultra-low ESR; tinned terminals							
Rated voltage 0 = 4 V, 1 = 6.3 V, 2 = 10 V, 3 = 16 V, 4 = 20 V, 5 = 25 V, 6 = 35 V, 7 = 50 V							
Rated capacitance + exponent C [pF] · 10 ^x E.g.: 686 = 68 pF · 10 ⁶ = 68 μF, 157 = 15 pF · 10 ⁷ = 150 μF							
Capacitance tolerance M = ±20%, K = ±10%							
Case size 5 = E							
ESR quality 9 = standard quality 0 = premium quality							
Reel diameter 9 = 180 mm, 6 = 330 mm							

Technical data and ordering codes

C _R (20 °C, 120 Hz) μF	Case size	DF _{max} (20 °C, 120 Hz) %	I _{leak,max} (20 °C, V _R , 5 min) μA	ESR _{max} ¹⁾ (20 °C, 100 kHz) mΩ	I _{AC,max} (20 °C, 100 kHz) A	Ordering code
V _R (up to 85 °C) = 4 VDC, V _R (up to 125 °C) = 2.5 VDC						
680	E	6	27	30	3.0	B45396R0687+50*
680	E	6	27	35	2.8	B45396R0687+59*
1000	E	6	40	30	3.0	B45396R0108+50*
1000	E	6	40	35	2.8	B45396R0108+59*
V _R (up to 85 °C) = 6.3 VDC, V _R (up to 125 °C) = 4 VDC						
470	E	6	30	30	3.0	B45396R1477+50*
470	E	6	30	35	2.8	B45396R1477+59*
680	E	6	43	30	3.0	B45396R1687+50* ●
680	E	6	43	35	2.8	B45396R1687+59* ●
V _R (up to 85 °C) = 10 VDC, V _R (up to 125 °C) = 6.3 VDC						
330	E	6	33	35	2.8	B45396R2337+50*
330	E	6	33	40	2.6	B45396R2337+59*
470	E	6	47	35	2.8	B45396R2477+50* ●
470	E	6	47	40	2.6	B45396R2477+59* ●
V _R (up to 85 °C) = 16 VDC, V _R (up to 125 °C) = 10 VDC						
150	E	6	24	40	2.6	B45396R3157+50*
150	E	6	24	45	2.4	B45396R3157+59*
220	E	6	35	40	2.6	B45396R3227+50*
220	E	6	35	45	2.4	B45396R3227+59*
V _R (up to 85 °C) = 20 VDC, V _R (up to 125 °C) = 13 VDC						
68	E	6	14	45	2.4	B45396R4686+50*
68	E	6	14	55	2.2	B45396R4686+59*
100	E	6	20	45	2.4	B45396R4107+50*
100	E	6	20	55	2.2	B45396R4107+59*
V _R (up to 85 °C) = 25 VDC, V _R (up to 125 °C) = 16 VDC						
47	E	6	12	45	2.4	B45396R5476+50* ●
47	E	6	12	55	2.2	B45396R5476+59* ●
68	E	6	17	45	2.4	B45396R5686+50* ●
68	E	6	17	55	2.2	B45396R5686+59* ●
V _R (up to 85 °C) = 35 VDC, V _R (up to 125 °C) = 23 VDC						

● Preliminary Data

* = Code number for reel diameter

6 = 330-mm reel

9 = 180-mm reel

+ = Capacitance tolerance

M = ±20%

K = ±10%



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B45396R

Multiple Anode, Ultra-low ESR

C_R (20 °C, 120 Hz) μF	Case size	DF_{max} (20 °C, 120 Hz) %	$I_{\text{leak,max}}$ (20 °C, V_R , 5 min) μA	$ESR_{\text{max}}^{(1)}$ (20 °C, 100 kHz) $\text{m}\Omega$	$I_{\text{AC,max}}$ (20 °C, 100 kHz) A	Ordering code
22	E	6	8	60	2.1	B45396R6226+50* ●
22	E	6	8	100	1.6	B45396R6226+59* ●
33	E	6	12	50	2.3	B45396R6336+50* ●
33	E	6	12	65	2.0	B45396R6336+59* ●
47	E	6	16	55	2.2	B45396R6476+50* ●
47	E	6	16	65	2.0	B45396R6476+59* ●
V_R (up to 85 °C) = 50 VDC, V_R (up to 125 °C) = 33 VDC						
10	E	6	5	120	1.5	B45396R7106+50* ●
10	E	6	5	150	1.3	B45396R7106+59* ●
15	E	6	8	110	1.6	B45396R7156+50* ●
15	E	6	8	140	1.4	B45396R7156+59* ●
22	E	6	11	110	1.6	B45396R7226+50* ●
22	E	6	11	140	1.4	B45396R7226+59* ●

● Preliminary Data

* = Code number for reel diameter
 6 = 330-mm reel
 9 = 180-mm reel

+ = Capacitance tolerance
 M = $\pm 20\%$
 K = $\pm 10\%$

Cautions and warnings

When using tantalum capacitors with a manganese dioxide coating, the following cautions and warnings should be taken into account:

Polarity

Because tantalum capacitors are *polar capacitors*, it is important to observe their polarity markings (positive pole on the anode, negative pole on the cathode). Any incorrect polarity resulting from the sum of the AC and DC voltage components must be smaller than or equal to the permitted *polarity reversal voltage*. To avoid reducing their reliability, this voltage may only occur for a short time, at most five times for a duration of one minute per hour.

Voltage

The *maximum continuous voltage* depends on the ambient temperature. Within the temperature range of -55 to $+85$ °C, the rated voltage is equal to the maximum continuous voltage. Between $+85$ and $+125$ °C the maximum continuous voltage must be reduced linearly from the full rated voltage to 2/3 of it (derating). At 150 °C (only B4519*P series) the voltage must be reduced to 1/2 and at 175 °C (only B4519*T series) to 1/3 of the rated voltage. Operation below the maximum continuous voltage has a positive effect on the capacitor's failure rate. The maximum continuous voltage must not be exceeded.

All unfavourable operating conditions (such as possible line overvoltages, unfavourable tolerances of the transformation ratio of the line transformer in the equipment, repeated overvoltages when the equipment is switched on, high ambient temperatures) must be taken into account when determining the *operating voltage*.

The *surge voltage* is the maximum voltage (peak value) that may be applied to the capacitor for short periods, at most five times for a total duration of up to 1 minute per hour. The surge voltage must not be applied for periodic charging and discharging in the course of normal operation and cannot be part of the operating voltage. The permissible surge voltage for all capacitors in this data book is 1.3 x the rated voltage. The occurrence of voltage impulses (transient voltages) that exceed the surge voltage may lead to irreparable damage.

Capacitance

The actual *capacitance* of a capacitor can deviate from the rated capacitance by as much as the full magnitude of the tolerance at delivery. The capacitance varies with the temperature (at $+125$ °C $+12\%$ up to $+20\%$) and decreases with increasing frequency.

**Multiple Anode, Ultra-low ESR****Low-resistance applications and voltage networks**

For *low-resistance applications*, KEMET recommends a maximum operating voltage of half the permissible maximum continuous voltage, so that the capacitors have sufficient tolerance to withstand voltage peaks. Depending on the conditions of use, the early failure rate is higher here by a factor of 2 to 20 than in the range with a constant failure rate as specified in the data book.

When operated directly in a *voltage network*, the capacitor should be protected against overvoltage, e.g. by a suppressor diode, and against polarity reversal by a diode. If a capacitor is operated in an unprotected low-impedance circuit and fails because the permissible conditions for the forward DC voltage, reverse DC voltage, surge current, power dissipation or temperature are exceeded, the continued current flow through the overstressed capacitor may produce overheating. The overheated capacitor may damage the surrounding components and the circuit board.

Storage conditions

Capacitors with solid electrolyte may be stored at temperatures down to $-80\text{ }^{\circ}\text{C}$. The upper *storage temperature* must not exceed $+85\text{ }^{\circ}\text{C}$ (for blister tape the temperature limit is $+40\text{ }^{\circ}\text{C}$). The storage conditions should not exceed $+40\text{ }^{\circ}\text{C}$ and 70% R. H. A drypack is useful to prevent the effects of humidity.

Important notes

The following applies to all products named in this publication:

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