



Advanced Power Electronics Corp.

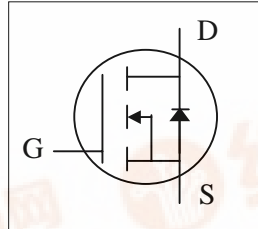
AP03N70H/J-H

Pb Free Plating Product

N-CHANNEL ENHANCEMENT MODE

POWER MOSFET

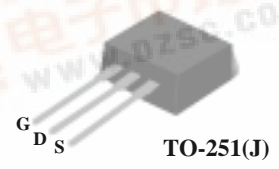
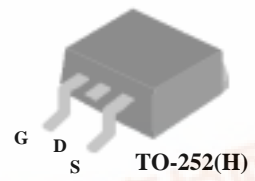
- Repetitive Avalanche Rated
- Fast Switching Speed
- Simple Drive Requirement
- RoHS Compliant



BV_{DSS}	700V
$R_{DS(ON)}$	4.4
I_D	2.5A

Description

The TO-252 package is universally preferred for all commercial-industrial surface mount applications and suited for AC/DC converters. The through-hole version (AP03N70J) is available for low-profile applications.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	700	V
V_{GS}	Gate-Source Voltage	±30	V
$I_D @ T_C=25$	Continuous Drain Current, $V_{GS} @ 10V$	2.5	A
$I_D @ T_C=100$	Continuous Drain Current, $V_{GS} @ 10V$	1.6	A
I_{DM}	Pulsed Drain Current ¹	8	A
$P_D @ T_C=25$	Total Power Dissipation	54.3	W
	Linear Derating Factor	0.44	W/
E_{AS}	Single Pulse Avalanche Energy ²	31	mJ
I_{AR}	Avalanche Current	2.5	A
T_{STG}	Storage Temperature Range	-55 to 150	
T_J	Operating Junction Temperature Range	-55 to 150	

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Thermal Resistance Junction-case	Max. 2.3	/W
Rthj-a	Thermal Resistance Junction-ambient	Max. 110	/W





Electrical Characteristics @T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =1mA	700	-	-	V
BV _{DSS} / T _j	Breakdown Voltage Temperature Coefficient	Reference to 25 , I _D =1mA	-	0.6	-	V/
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =1.6A	-	-	4.4	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2	-	4	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =1.6A	-	2	-	S
I _{DSS}	Drain-Source Leakage Current (T _j =25°C)	V _{DS} =600V, V _{GS} =0V	-	-	10	uA
	Drain-Source Leakage Current (T _j =150°C)	V _{DS} =480V, V _{GS} =0V	-	-	100	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±30V	-	-	±100	nA
Q _g	Total Gate Charge ³	I _D =1A	-	12	20	nC
Q _{gs}	Gate-Source Charge	V _{DS} =480V	-	3	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =10V	-	4	-	nC
t _{d(on)}	Turn-on Delay Time ³	V _{DD} =300V	-	8.5	-	ns
t _r	Rise Time	I _D =2.5A	-	6	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =10 , V _{GS} =10V	-	19	-	ns
t _f	Fall Time	R _D =120	-	8	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	590	950	pF
C _{oss}	Output Capacitance	V _{DS} =25V	-	50	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	6	-	pF
R _g	Gate Resistance	f=1.0MHz	-	3.4	5.1	

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ³	I _S =2.5A, V _{GS} =0V	-	-	1.5	V
t _{rr}	Reverse Recovery Time ²	I _S =2.5A, V _{GS} =0V,	-	407	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	2110	-	nC

Notes:

- 1.Pulse width limited by safe operating area.
- 2.Starting T_j=25°C , V_{DD}=50V , L=10mH , R_G=25 , I_{AS}=2.5A.
- 3.Pulse width ≤300us , duty cycle ≤2%.

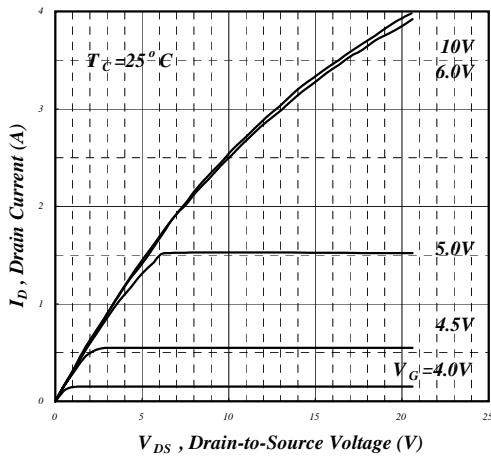


Fig 1. Typical Output Characteristics

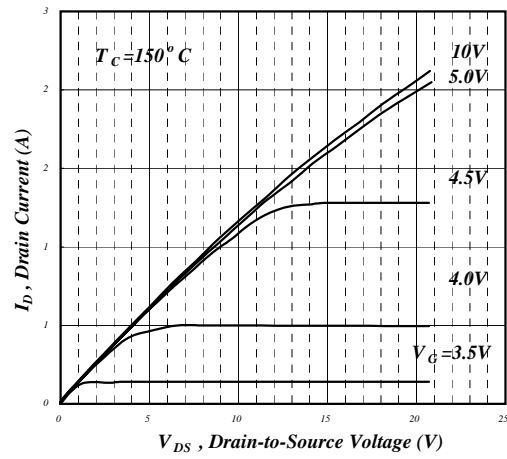


Fig 2. Typical Output Characteristics

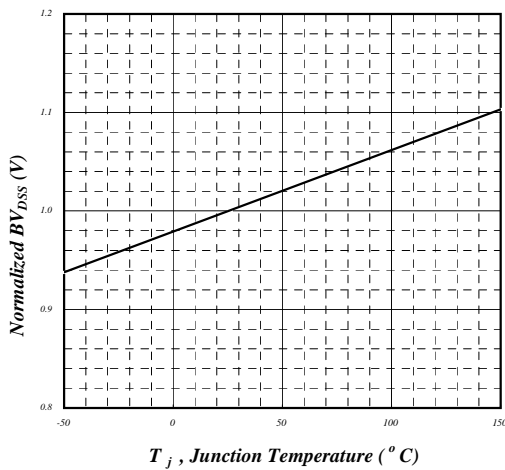


Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

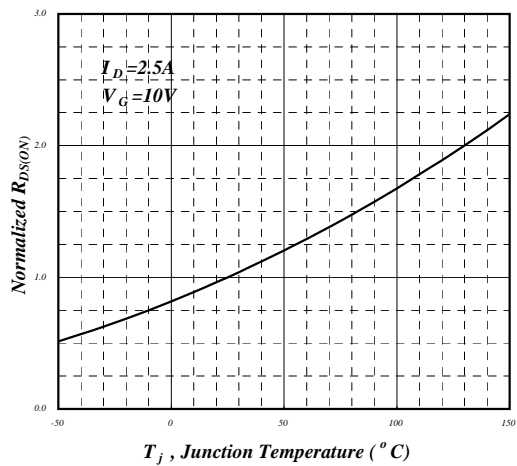


Fig 4. Normalized On-Resistance v.s. Junction Temperature

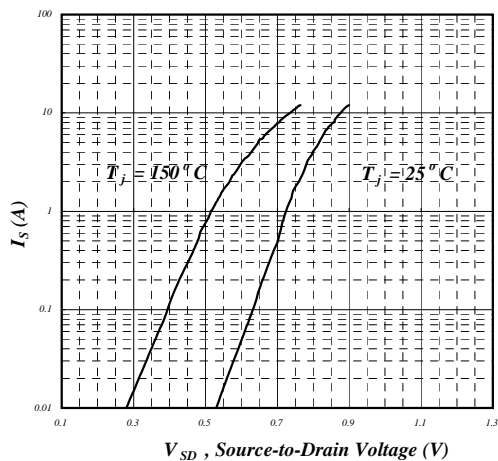


Fig 5. Forward Characteristic of Reverse Diode

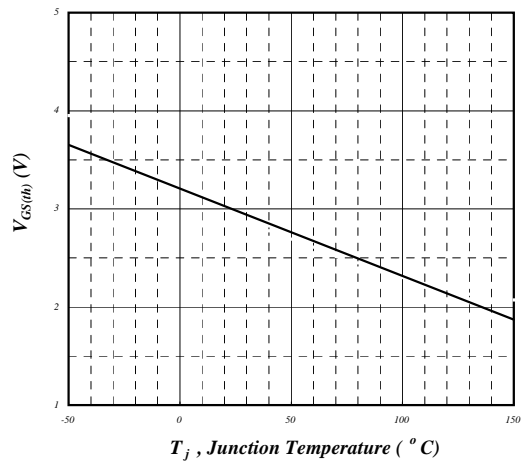


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

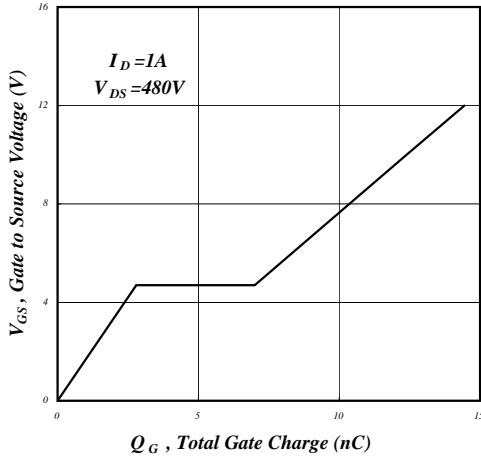


Fig 7. Gate Charge Characteristics

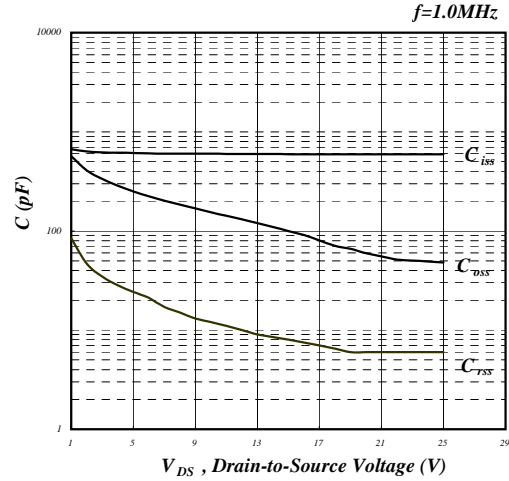


Fig 8. Typical Capacitance Characteristics

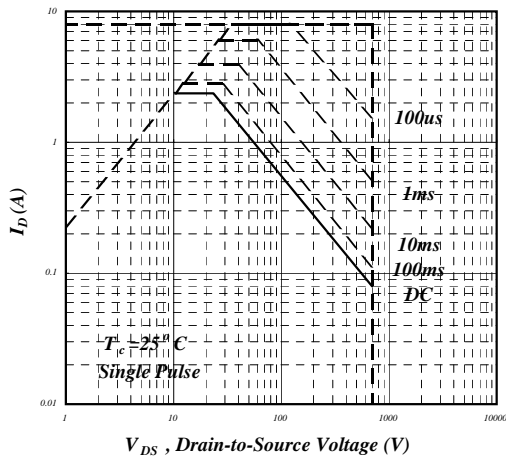


Fig 9. Maximum Safe Operating Area

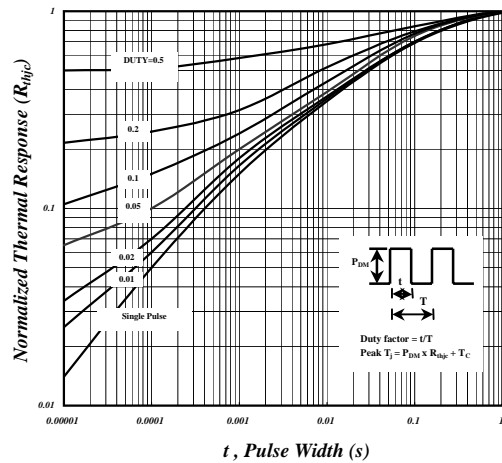


Fig 10. Effective Transient Thermal Impedance

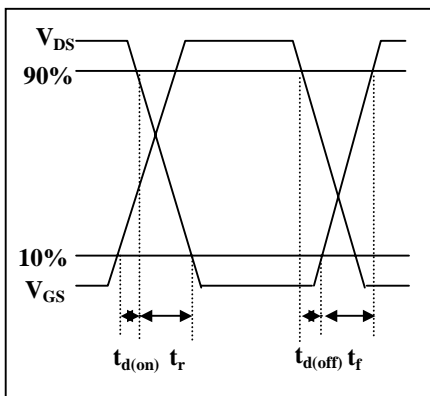


Fig 11. Switching Time Waveform

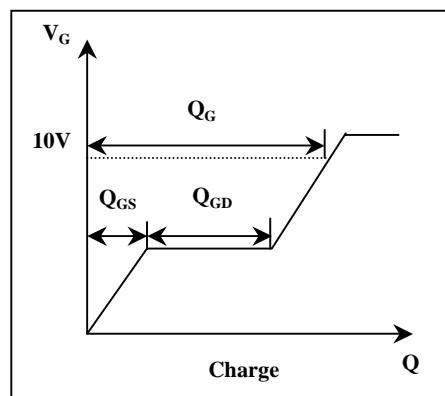


Fig 12. Gate Charge Waveform