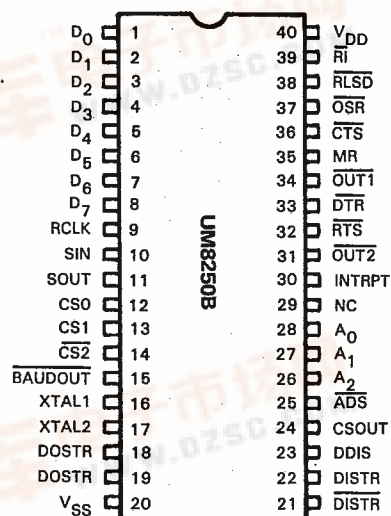


UM8250B Asynchronous Communication Element (ACE)

FEATURES

- Adds or Deletes Standard Asynchronous Communication Bits (Start, Stop, and Parity) to or from Serial Data Stream
- Full Double Buffering Eliminates Need for Precise Synchronization
- Independently Controlled Transmit, Receive, Line Status, and Data Set Interrupts
- Programmable Baud Rate Generator Allows Division of Any Input Clock by 1 to $(2^{16} - 1)$ and Generates the Internal 16x Clock
- Independent Receiver Clock Input
- Modem Control Functions (CTS, RTS, DSR, DTR, RI, and Carrier Detect)
- Easily Interfaces to Most Popular Micro-processors
- Line Break Generation and Detection
- Single +5 Volt Power Supply
- Fully Programmable Serial Interface Characteristics
 - 5-, 6-, 7-, or 8-bit Characters
 - Even, Odd, or No-parity Bit Generation and Detection
 - 1-, 1½-, or 2-stop Bit Generation
 - Baud Rate Generation (DC to 56k Baud)
- INS 8250-B Spec. Compatible
- False Start Bit Detection
- Complete Status Reporting Capabilities
- Internal Diagnostic Capabilities
 - Loopback Controls for Communications Link Fault Isolation
 - Break, Parity, Overrun, Framing Error Simulation
- Full Prioritized Interrupt System Controls

PIN CONFIGURATION



UM53C80 SCSI Bus Controller

FEATURES

SCSI Interface:

- Support ANSI X3T9.2 SCSI Standard.
- Asynchronous Data Transfers to 1.5M BPS.
- Support Both Initiator and Target Roles
- Parity Generation W/optional Checking.
- Support Arbitration with Firmware Dependant.

- Controls All Bus Signal Except Reset.
- Single-buffered Data Register.

MPU Interface:

- Memory or I/O Mapped MPU Interface.
- DMA or Programmed I/O Transfer
- No Transfer Counter
- Programmable (Re) Selection Timeouts
- Interrupts MPU on All Bus Conditions Requiring Service

PIN CONFIGURATION

