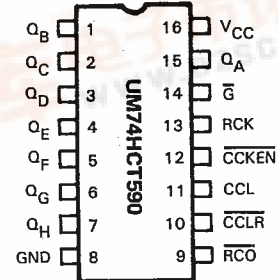# UM74HCT590 8 Bit Binary Counters with Output Registers

FEATURES

- 8-bit Binary Counter with Parallel Register Output
- 3-state Register Output
- Direct Clear to Counter
- High Speed Counter with Guaranteed Frequency: DC to 20 MHz
- High Driving Capability (I_{OL} = 24mA, V_{OL} = 0.5V) for Direct Interface with TTL, NMOS and CMOS Devices
- Can Drive up to 20 TTL Load Min.

PIN CONFIGURATION

	UM74HCT590	
Q_B 1		16 V_{CC}
Q_C 2		15 Q_A
Q_D 3		14 $\overline{G}$
Q_E 4		13 RCK
Q_F 5		12 $\overline{CCKEN}$
Q_G 6		11 CCL
Q_H 7		10 $\overline{CCLR}$
GND 8		9 $\overline{RCO}$

UM74HCT612 Memory Mapper

FEATURES

- Fully Compatible with TTL, NMOS and CMOS Devices
- Expands 4 Address Lines to 12 Address Lines
- Designed for Paged Memory Mapping
- High-current 3-state Outputs

PIN CONFIGURATION

	UM74HCT612	
RS2 1		40 V_{CC}
MA3 2		39 MA2
RS3 3		38 RS1
$\overline{CS}$ 4		37 MA1
$\overline{STROB}$ 5		36 RSo
R/$\overline{W}$ 6		35 MAo
Do 7		34 D11
D1 8		33 D10
D2 9		32 D9
D3 10		31 D8
D4 11		30 D7
D5 12		29 D6
$\overline{MM}$ 13		28 NC
MOo 14		27 MO11
MO1 15		26 MO10
MO2 16		25 MO9
MO3 17		24 MO8
MO4 18		23 MO7
MO5 19		22 MO6
GND 20		21 $\overline{ME}$

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