Ordering number : ENN6661

NPN Triple Diffused Planar Silicon Transistor





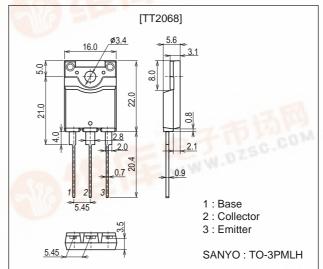
# Ultrahigh-Definition CRT Display Horizontal Deflection Output Applications

### **Features**

- · High speed.
- High breakdown voltage (VCBO=1500V).
- · High reliability (Adoption of HVP process).
- Adoption of MBIT process.

## **Package Dimensions**

unit : mm 2174



# **Specifications**

## **Absolute Maximum Ratings** at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		1500	V
Collector-to-Emitter Voltage	VCEO		800	V
Emitter-to-Base Voltage	VEBO		5	V
Collector Current	IC		30	Α
Collector Current (Pulse)	ICP		50	Α
Collector Dissipation	PC	- 5 9/6 -	3.0	W
		Tc=25°C	100	W
Junction Temperature	Tj	COM	150	°C
Storage Temperature	Tstg	0-	-55 to +150	°C

### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	Offic
Collector Cutoff Current	ICBO	V <sub>CB</sub> =800V, I <sub>E</sub> =0			10	μΑ
Collector Cutoff Current	ICES	V <sub>CE</sub> =1500V, R <sub>BE</sub> =0		E	1.0	mA
Collector Sustain Voltage	VCEO(sus)	I <sub>C</sub> =100mA, I <sub>B</sub> =0	800	AL WILLIAM		V
Emitter Cutoff Current	IEBO	V <sub>EB</sub> =4V, I <sub>C</sub> =0			1.0	mA

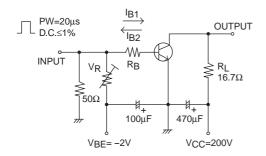
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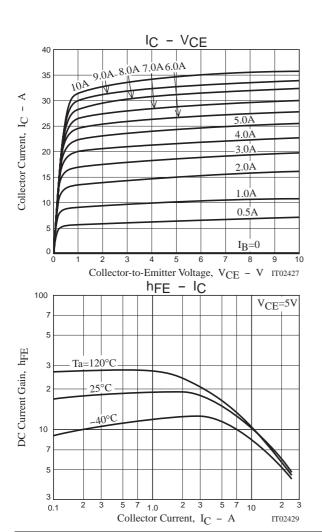
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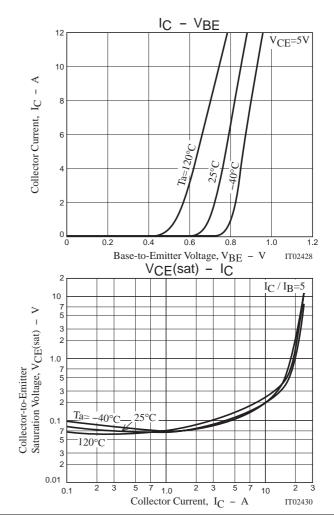
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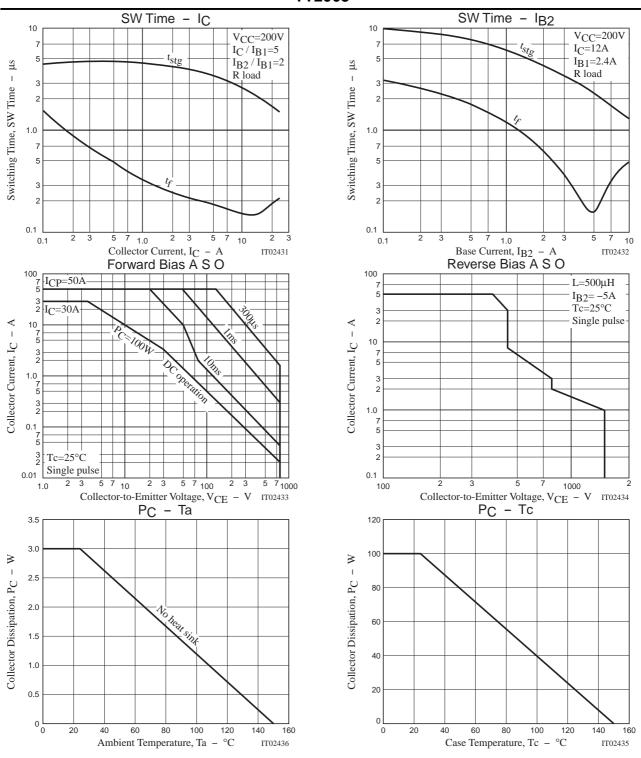
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Emitter Saturation Voltage	VCE(sat)	IC=18A, IB=4.5A			3	V
Base-to-Emitter Saturation Voltage	V <sub>BE</sub> (sat)	I <sub>C</sub> =18A, I <sub>B</sub> =4.5A			1.5	V
DC Current Gain	hFE1	V <sub>CE</sub> =5V, I <sub>C</sub> =1.0A	15			
	hFE2	VCE=5V, IC=20A	4		7	
Storage Time	tstg	I <sub>C</sub> =12A, I <sub>B1</sub> =2.4A, I <sub>B2</sub> =-4.8A			3.0	μs
Fall Time	tf	I <sub>C</sub> =12A, I <sub>B1</sub> =2.4A, I <sub>B2</sub> =-4.8A			0.2	μs

## **Switching Time Test Circuit**









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