

T-37-25

# TP0610 SERIES

P-Channel Enhancement-Mode MOS Transistors

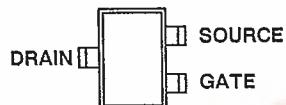
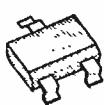
## PRODUCT SUMMARY

PART NUMBER	$V_{(BR)DSS}$ (V)	$r_{DS(ON)}$ ( $\Omega$ )	$I_D$ (A)	PACKAGE
TP0610E	-60	10	-0.25	TO-206AC
TP0610L	-60	10	-0.18	TO-92
TP0610T	-60	10	-0.12	SOT-23

Performance Curves: VPDS06 (See Section 7)

SOT-23

TOP VIEW



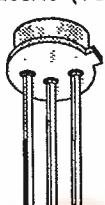
TO-92

BOTTOM VIEW


 1 SOURCE  
 2 GATE  
 3 DRAIN

TO-206AC (TO-52)

BOTTOM VIEW


 1 SOURCE  
 2 GATE  
 3 DRAIN & CASE

## ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	TP0610E <sup>2</sup>	TP0610L	TP0610T	UNITS
Drain-Source Voltage		$V_{DS}$	-60	-60	-60	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	$\pm 30$	$\pm 30$	
Continuous Drain Current	$T_A = 25^\circ\text{C}$	$I_D$	-0.25	-0.18	-0.12	A
	$T_A = 100^\circ\text{C}$		-0.15	-0.11	-0.07	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	-1	-0.8	-0.4	
Power Dissipation	$T_A = 25^\circ\text{C}$	$P_D$	1.5	0.80	0.36	W
	$T_A = 100^\circ\text{C}$		0.60	0.32	0.14	
Operating Junction and Storage Temperature		$T_J, T_{stg}$	-55 to 150			°C
Lead Temperature (1/16" from case for 10 seconds)		$T_L$	300			

## THERMAL RESISTANCE

THERMAL RESISTANCE		SYMBOL	TP0610E	TP0610L	TP0610T	UNITS
Junction-to-Ambient		$R_{thJA}$	400	156	350	°C/W

<sup>1</sup>Pulse width limited by maximum junction temperature<sup>2</sup>Reference  $T_C$  for all temperature testing



## TP0610 SERIES

T-37-25

PARAMETER	SYMBOL	TEST CONDITIONS	TYP <sup>2</sup>	LIMITS		TYP <sup>2</sup>		UNIT
				MIN	MAX	MIN	MAX	
<b>STATIC</b>								
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = -10 μA	-70	-60		-60		V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -1 mA	-1.7	-1	-2.4	-1	-2.4	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V V <sub>GS</sub> = ±20 V T <sub>J</sub> = 125°C	±1 ±5		±10 ±50		±10 ±50	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -48 V V <sub>GS</sub> = 0 V T <sub>J</sub> = 125°C	-0.02 -0.2		-1 -200		-1 -200	μA
On-State Drain Current <sup>3</sup>	I <sub>D(ON)</sub>	V <sub>DS</sub> = -10 V, V <sub>GS</sub> = -4.5 V	-80	-50		-50		mA
Drain-Source On-Resistance <sup>3</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -25 mA	11		25		25	Ω
		V <sub>GS</sub> = -10 V I <sub>D</sub> = -0.5 A T <sub>J</sub> = 125°C	8 15		10 20		10 20	
Forward Transconductance <sup>3</sup>	g <sub>FS</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -0.5 A	135	80		80		μS
Common Source Output Conductance <sup>3</sup>	g <sub>OS</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -0.1 A	400					μS
<b>DYNAMIC</b>								
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -25 V V <sub>GS</sub> = 0 V f = 1 MHz	15		60		60	pF
Output Capacitance	C <sub>oss</sub>		10		25		25	
Reverse Transfer Capacitance	C <sub>rss</sub>		3		5		5	
<b>SWITCHING</b>								
Turn-On Time	t <sub>d(ON)</sub>	V <sub>DD</sub> = -25 V, R <sub>L</sub> = 133 Ω I <sub>D</sub> = -0.18 A, V <sub>GEN</sub> = -10 V R <sub>G</sub> = 25 Ω (Switching time is essentially independent of operating temperature)	6		10		10	ns
	t <sub>r</sub>		10		15		15	
Turn-Off Time	t <sub>d(OFF)</sub>		7		15		15	
	t <sub>f</sub>		8		20		20	

NOTES: 1. T<sub>A</sub> = 25 °C unless otherwise noted, T<sub>C</sub> = 25 °C for TP0610E.

2. For design aid only, not subject to production testing.

3. Pulse test; PW = 300 μs, duty cycle ≤ 2%.

6

## TP0610 SERIES

T-37-25

 Siliconix  
incorporated

ELECTRICAL CHARACTERISTICS <sup>1</sup>			LIMITS			
PARAMETER	SYMBOL	TEST CONDITIONS	TYP <sup>2</sup>	TP0610T		UNIT
				MIN	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = -10 μA	-70	-60		V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -1 mA	-1.7	-1	-2.4	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V V <sub>GS</sub> = ±20 V	±1		±10	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -48 V V <sub>GS</sub> = 0 V	-0.02		-1	μA
On-State Drain Current <sup>3</sup>	I <sub>D(ON)</sub>	V <sub>DS</sub> = -10 V, V <sub>GS</sub> = -4.5 V	-80	-50		mA
Drain-Source On-Resistance <sup>3</sup>	r <sub>DS(ON)</sub>	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -25 mA	11		25	Ω
		V <sub>GS</sub> = -10 V I <sub>D</sub> = -0.2 A	6		10	
		T <sub>J</sub> = 125°C	12		20	
Forward Transconductance <sup>3</sup>	g <sub>FS</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -0.1 A	90	60		mS
Common Source Output Conductance <sup>3</sup>	g <sub>OS</sub>		400			μS
<b>DYNAMIC</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -25 V V <sub>GS</sub> = 0 V f = 1 MHz	15		60	pF
Output Capacitance	C <sub>oss</sub>		10		25	
Reverse Transfer Capacitance	C <sub>rss</sub>		3		5	
<b>SWITCHING</b>						
Turn-On Delay Time	t <sub>d(ON)</sub>	V <sub>DD</sub> = -25 V, R <sub>L</sub> = 133 Ω I <sub>D</sub> = -0.18 A, V <sub>GEN</sub> = -10 V R <sub>G</sub> = 25 Ω  (Switching time is essentially independent of operating temperature)	6		10	ns
	t <sub>r</sub>		10		15	
Turn-Off Delay Time	t <sub>d(OFF)</sub>		7		15	
	t <sub>f</sub>		8		20	

- NOTES: 1. T<sub>A</sub> = 25 °C unless otherwise noted.  
 2. For design aid only, not subject to production testing.  
 3. Pulse test; PW = 300 μs, duty cycle ≤ 2%.